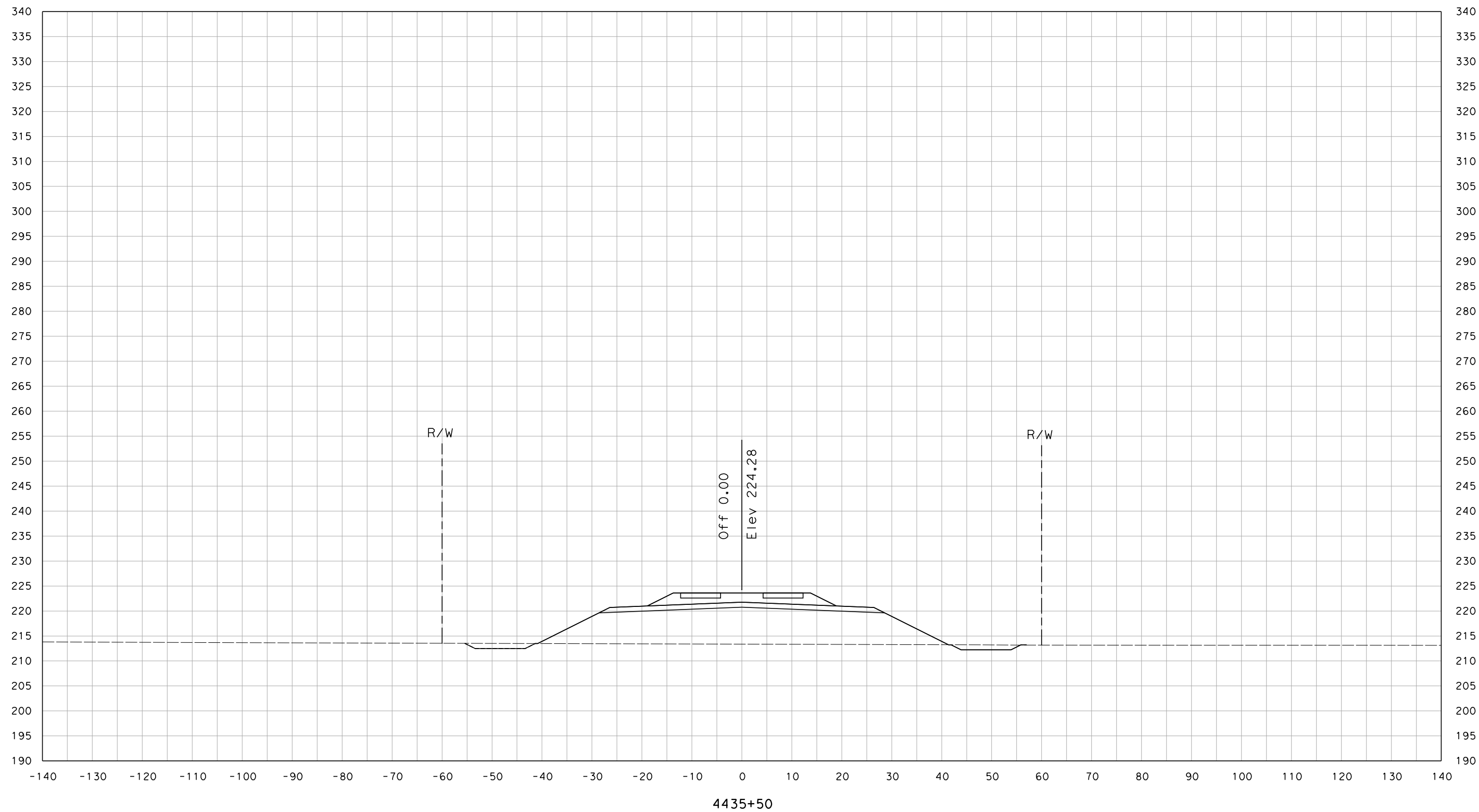
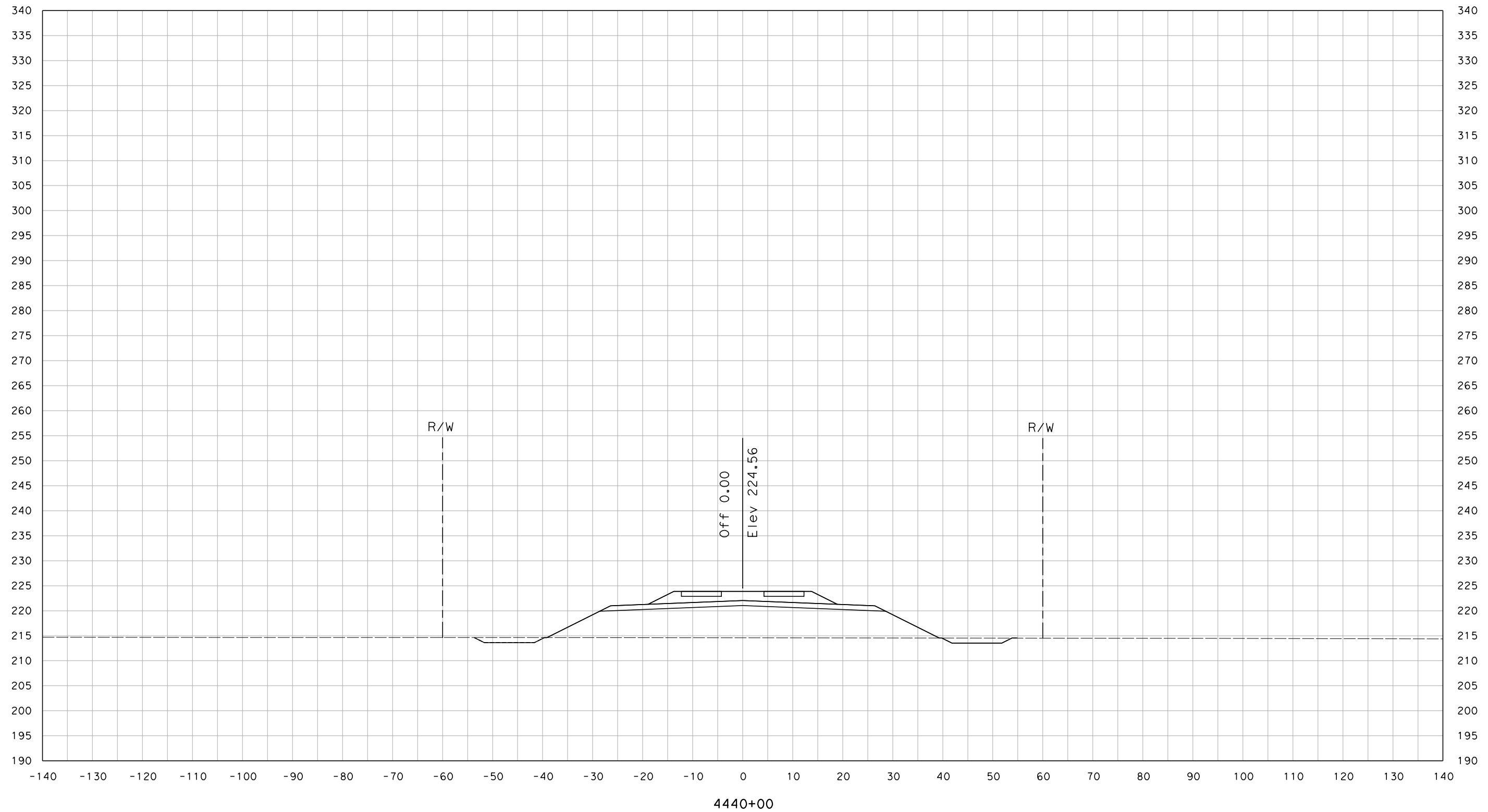


ALIGNMENT

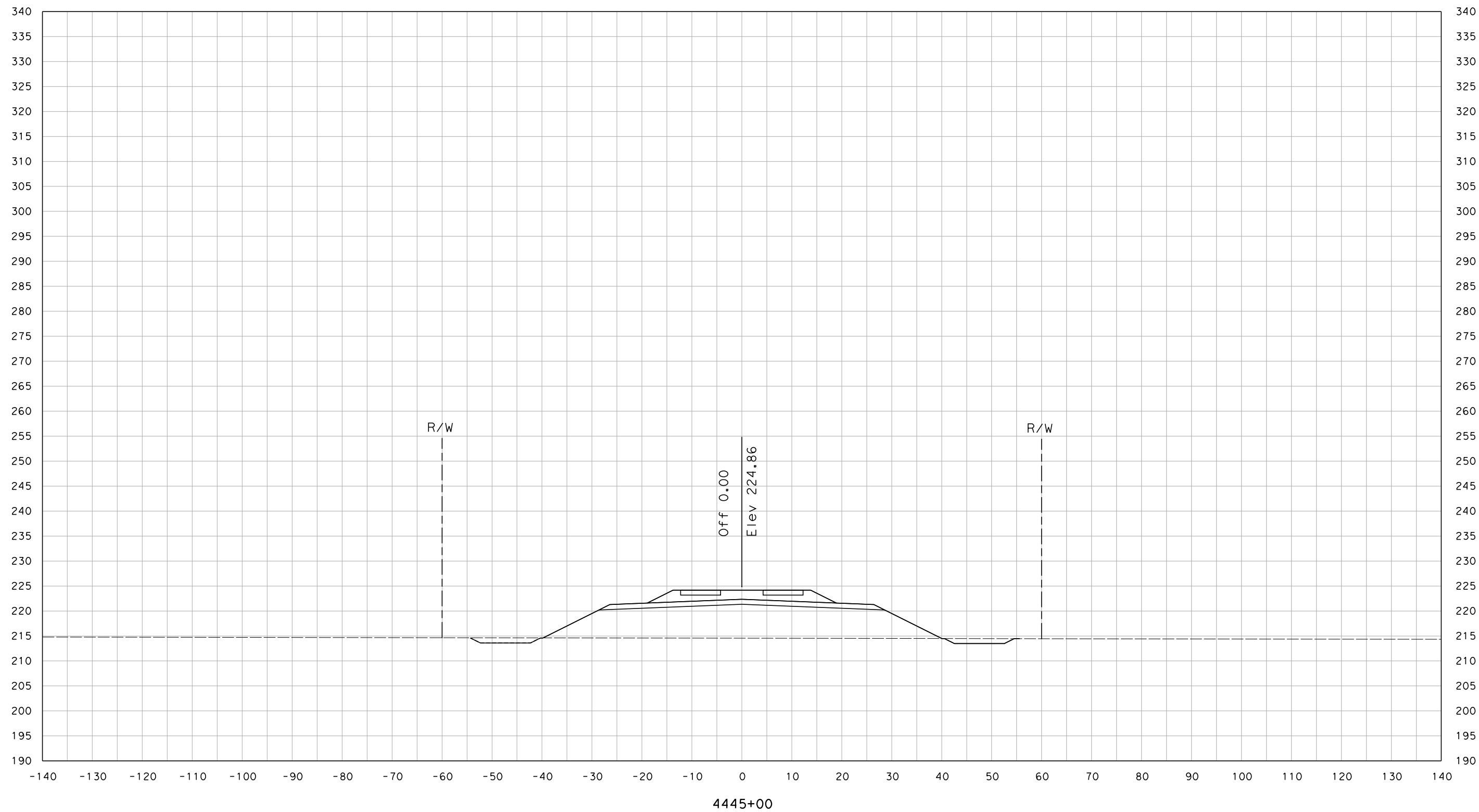
"A1"



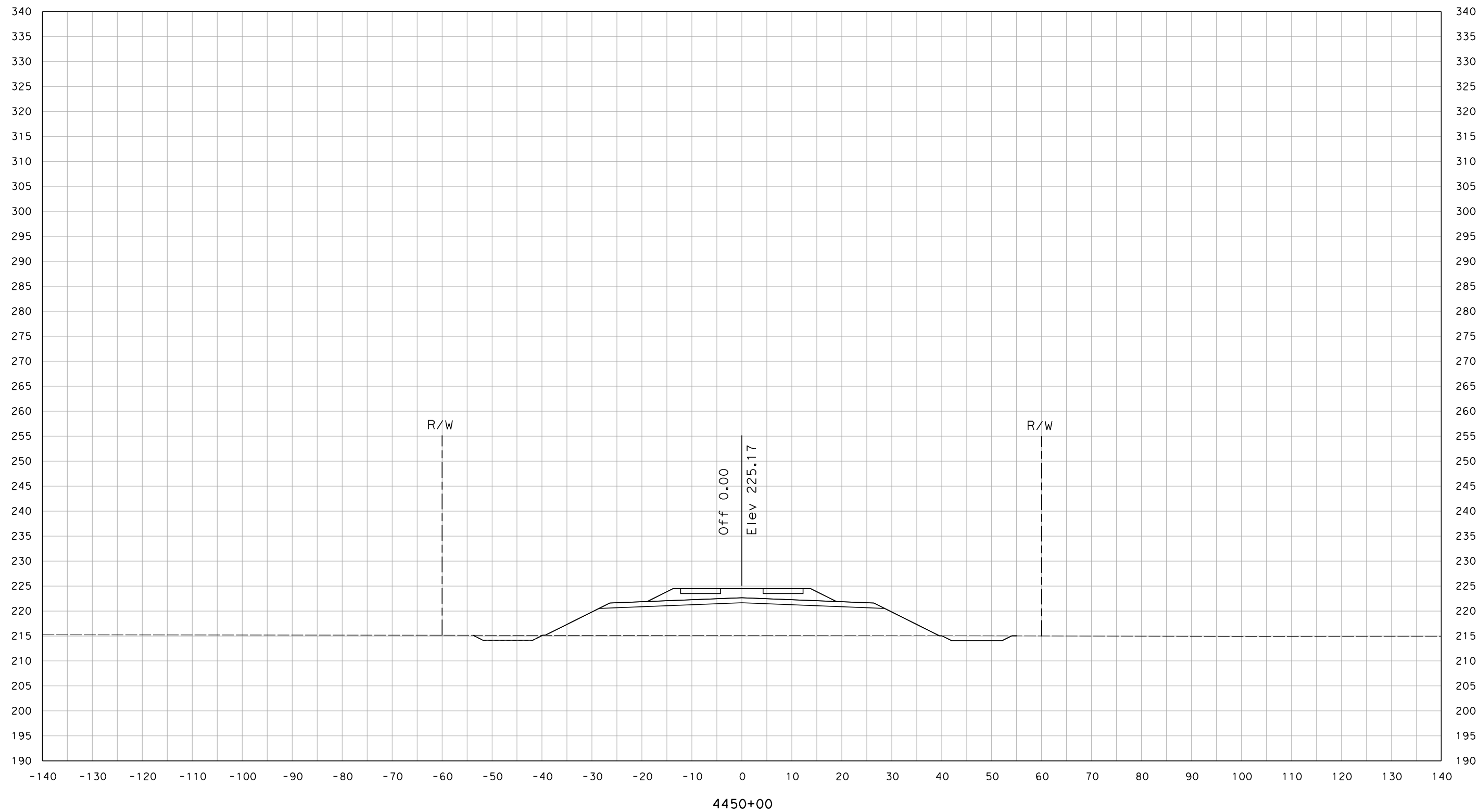
STA. 4435+50 TO STA. 4435+50



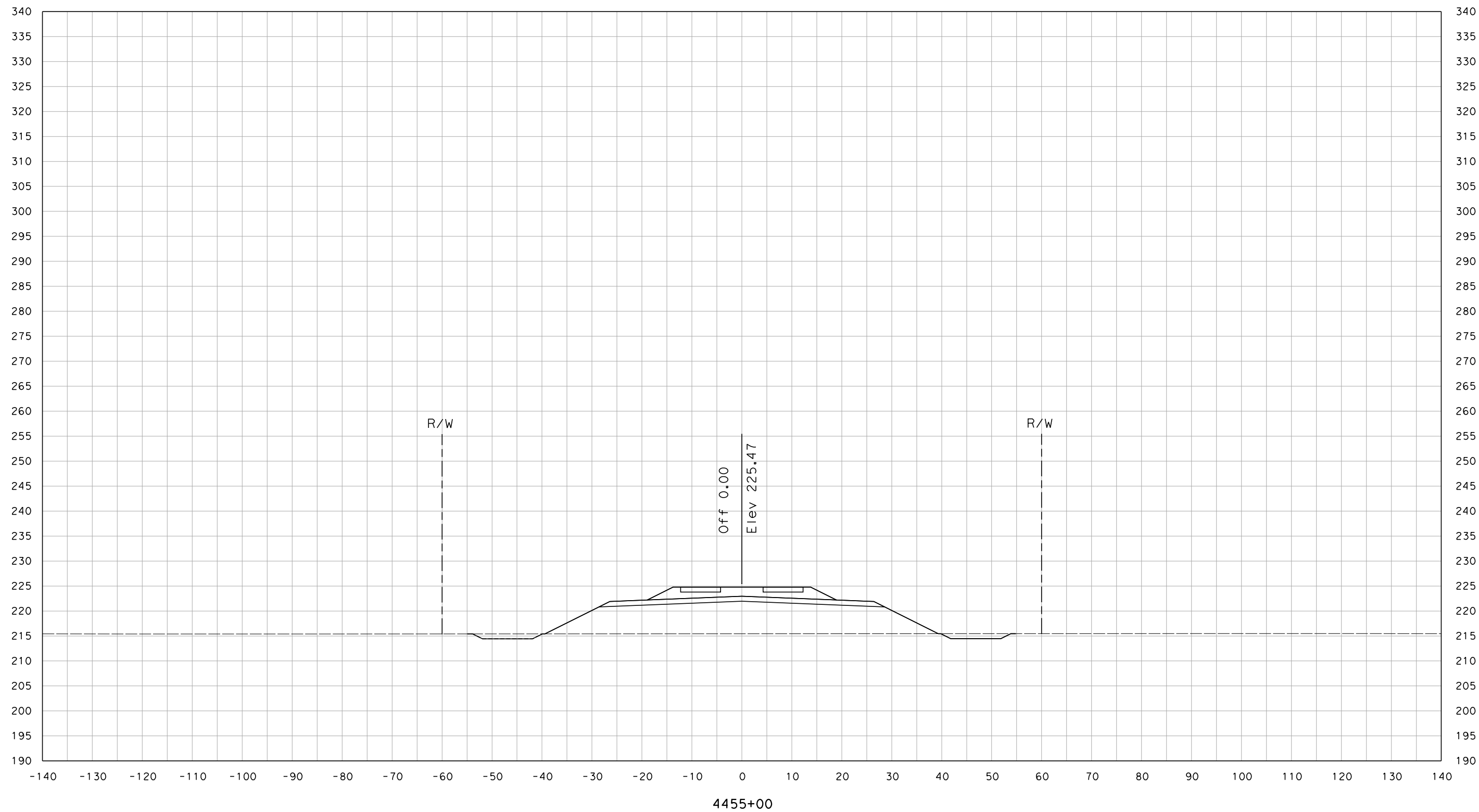
STA. 4440+00 TO STA. 4440+00



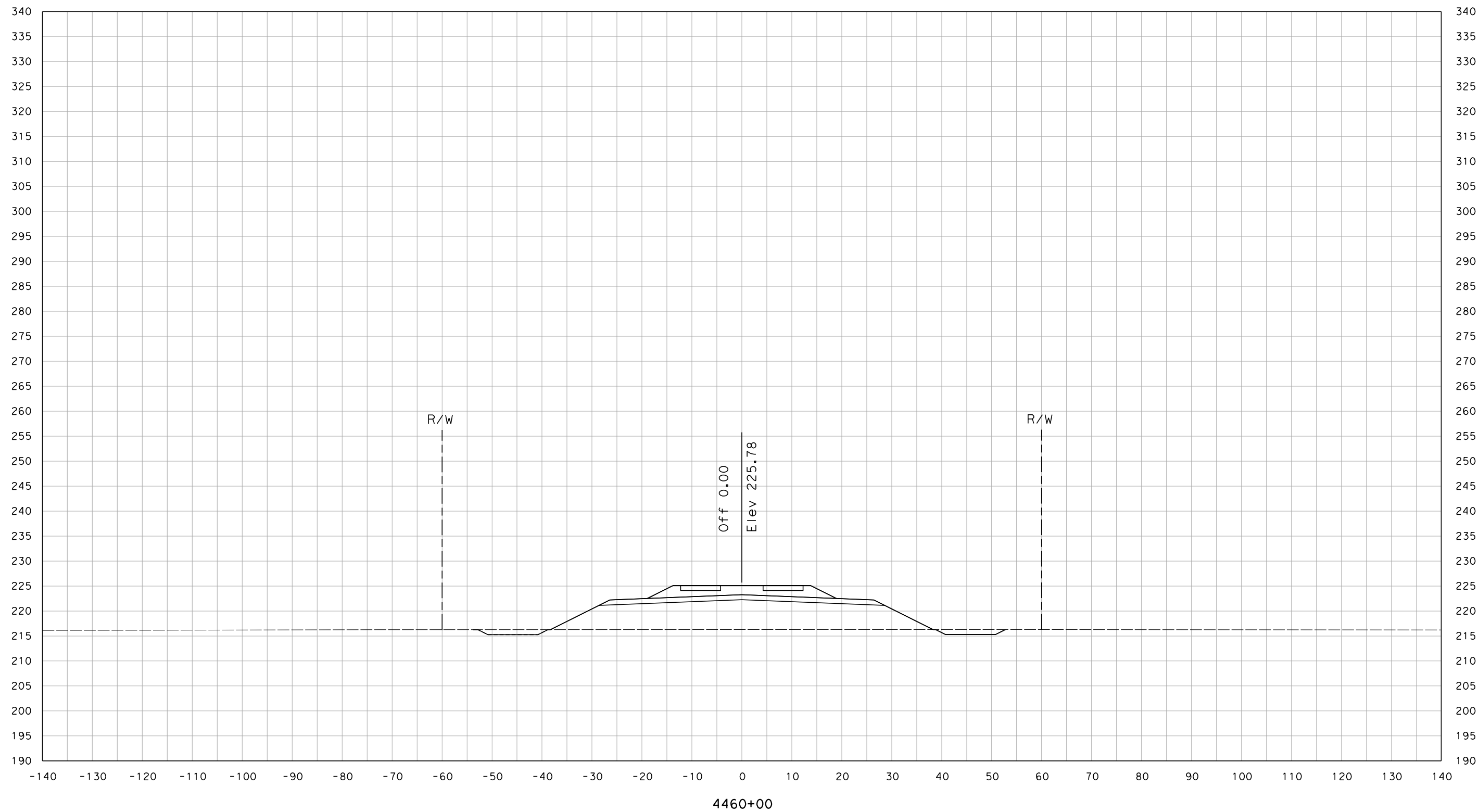
STA. 4445+00 TO STA. 4445+00



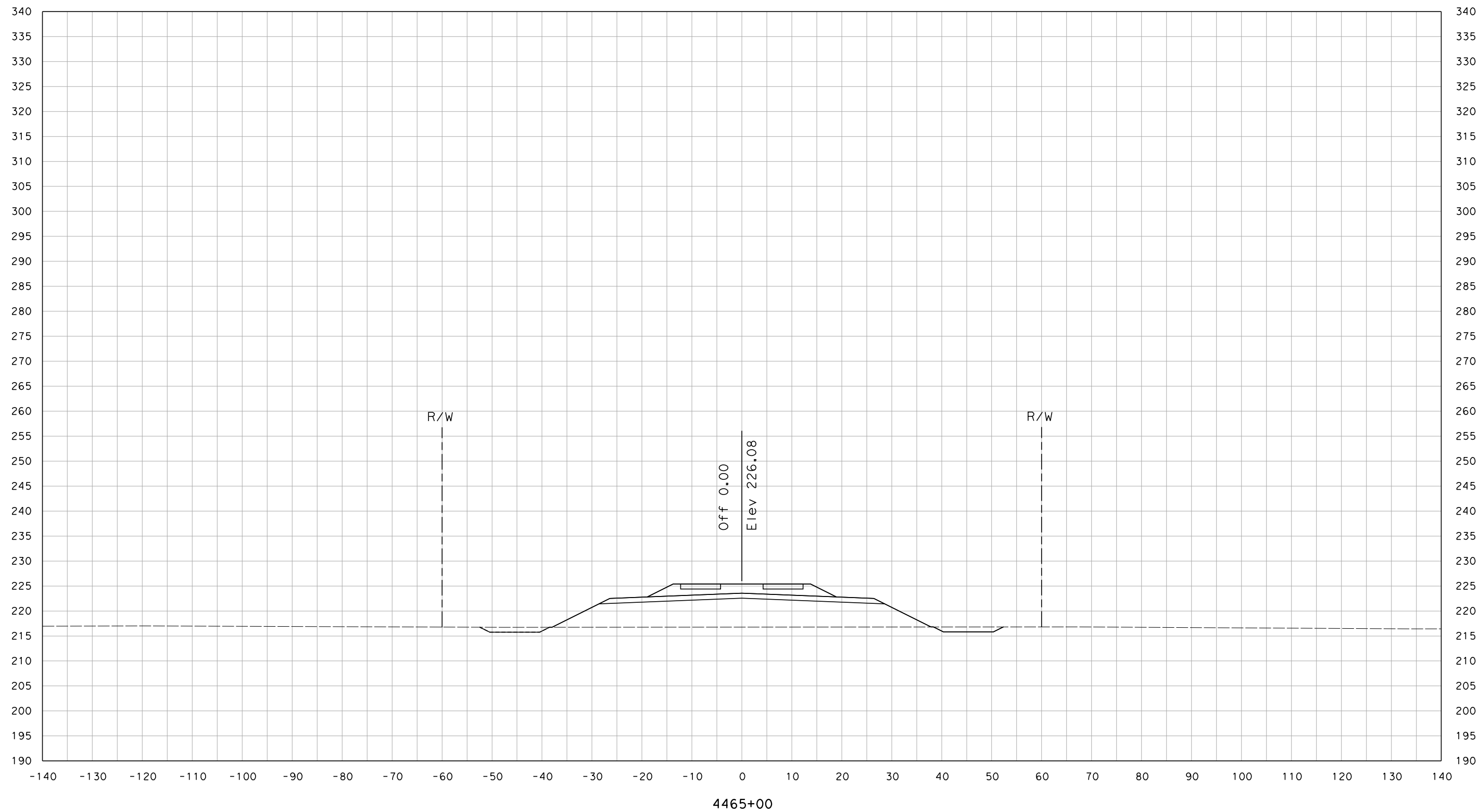
STA. 4450+00 TO STA. 4450+00



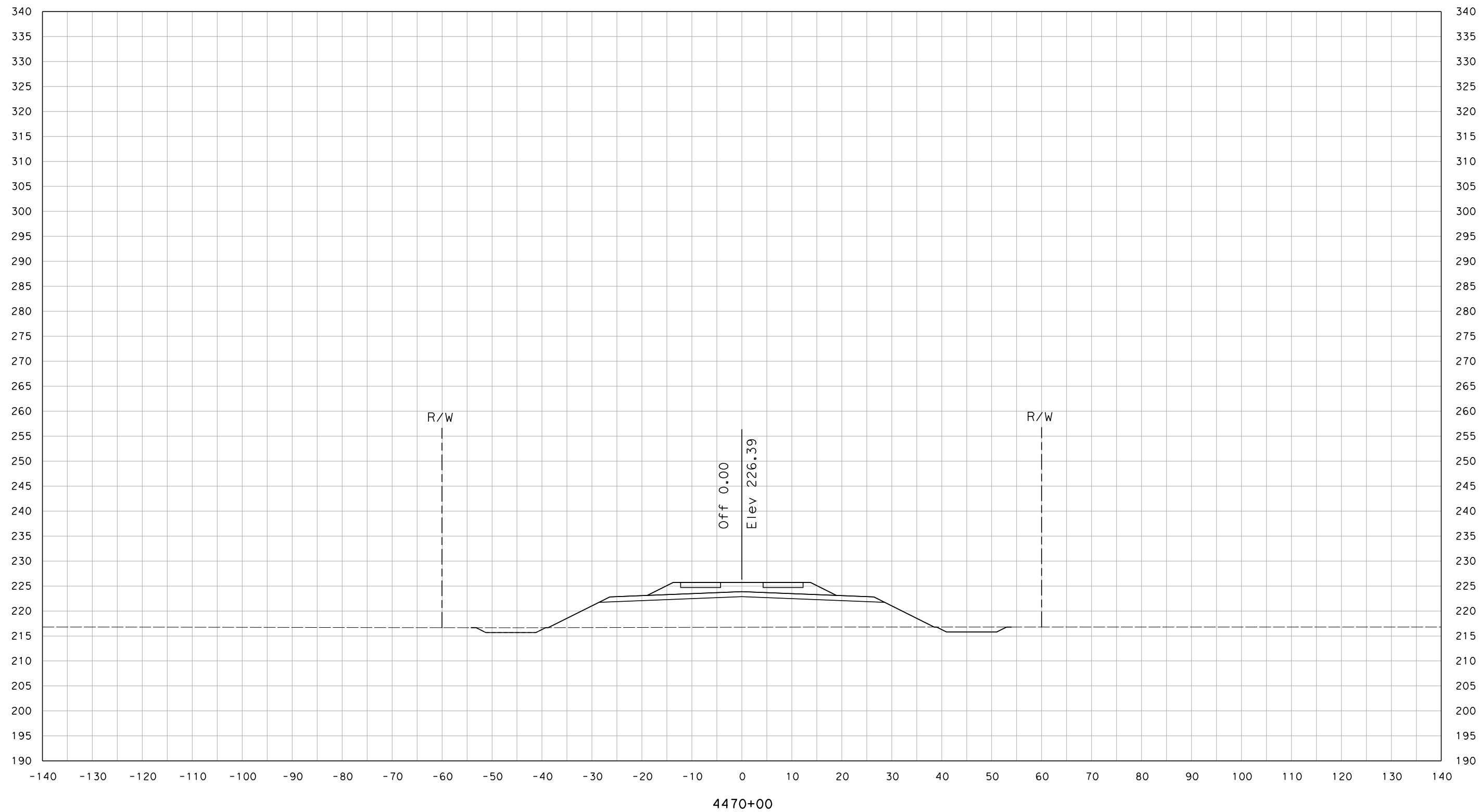
STA. 4455+00 TO STA. 4455+00



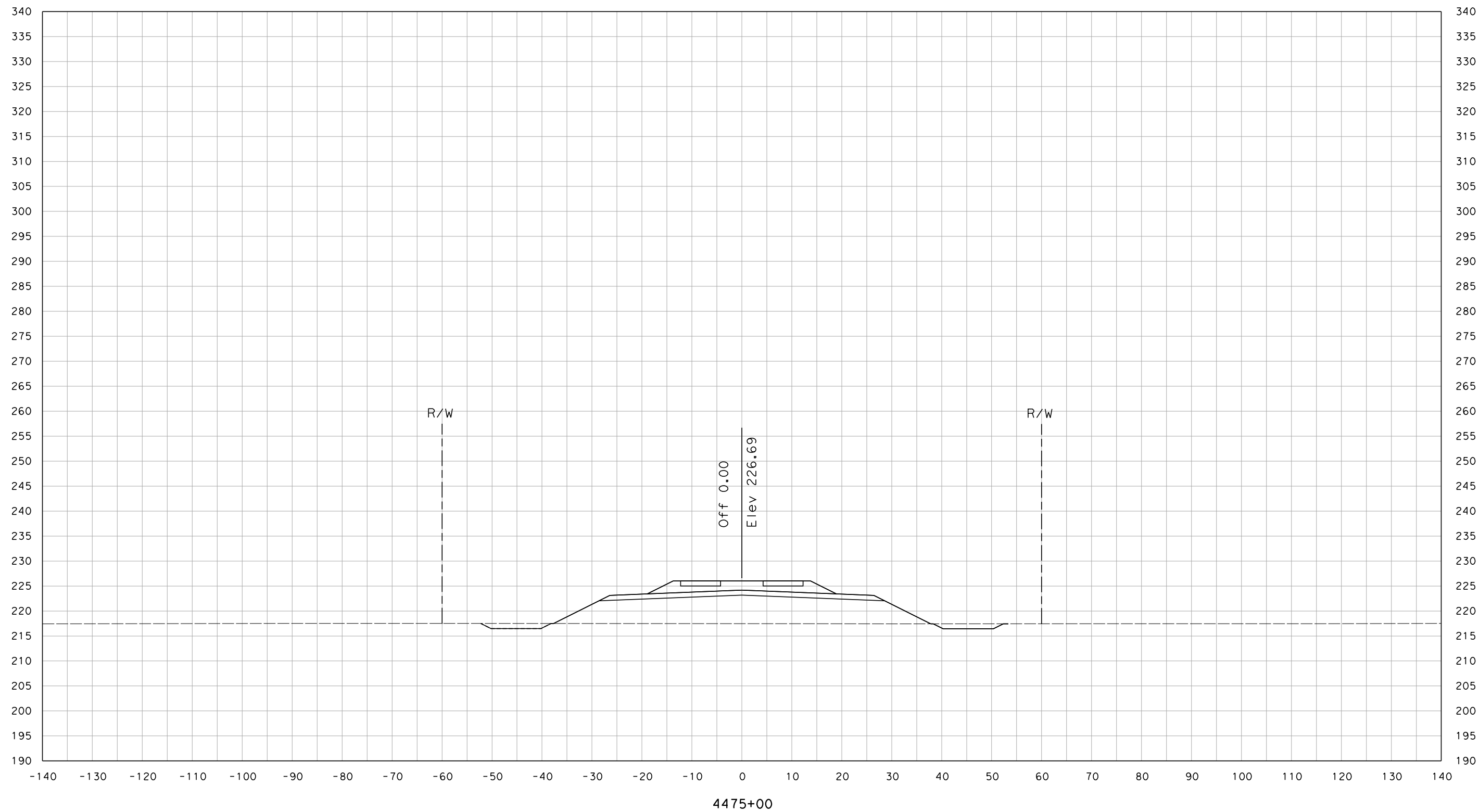
STA. 4460+00 TO STA. 4460+00



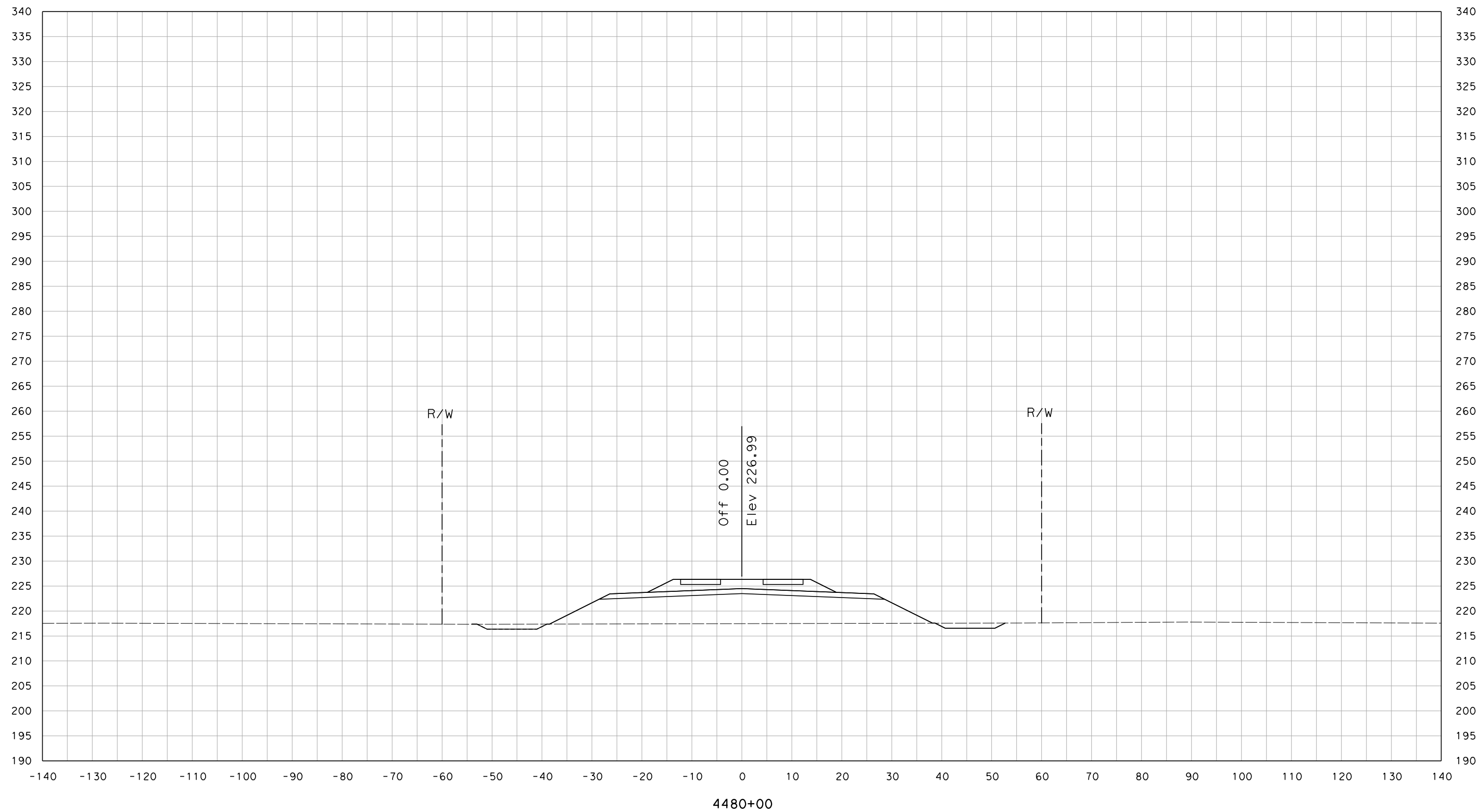
STA. 4465+00 TO STA. 4465+00



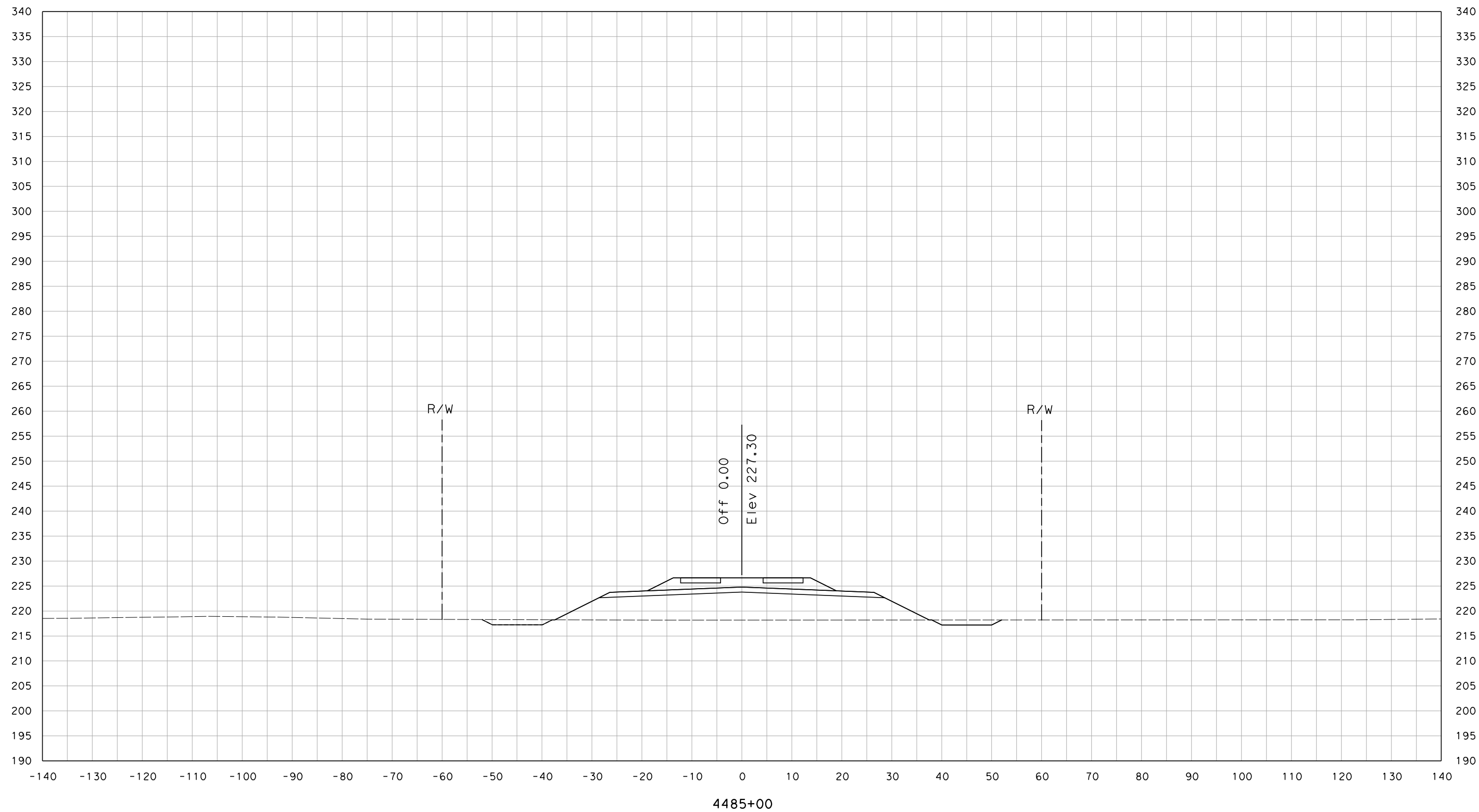
STA. 4470+00 TO STA. 4470+00



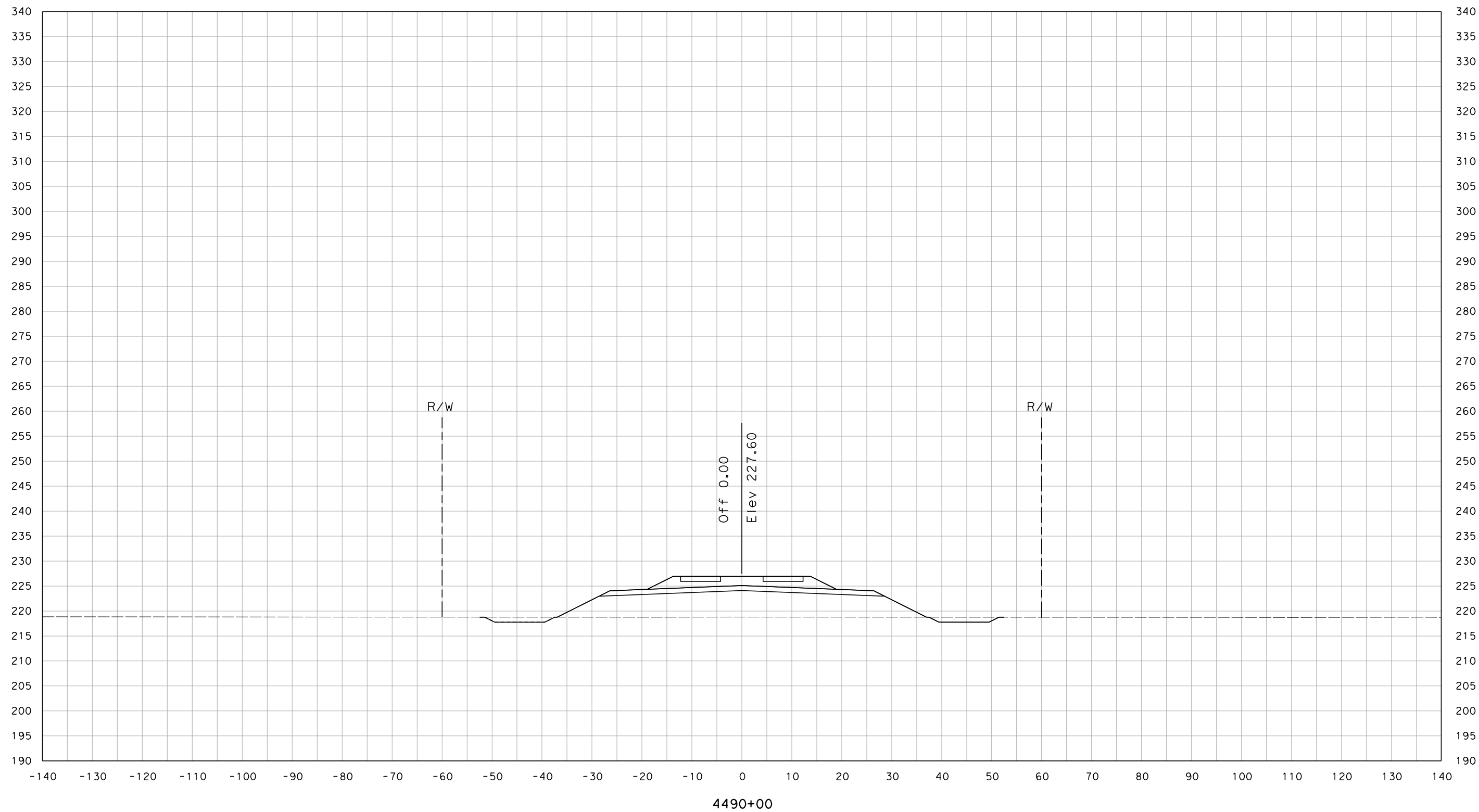
STA. 4475+00 TO STA. 4475+00



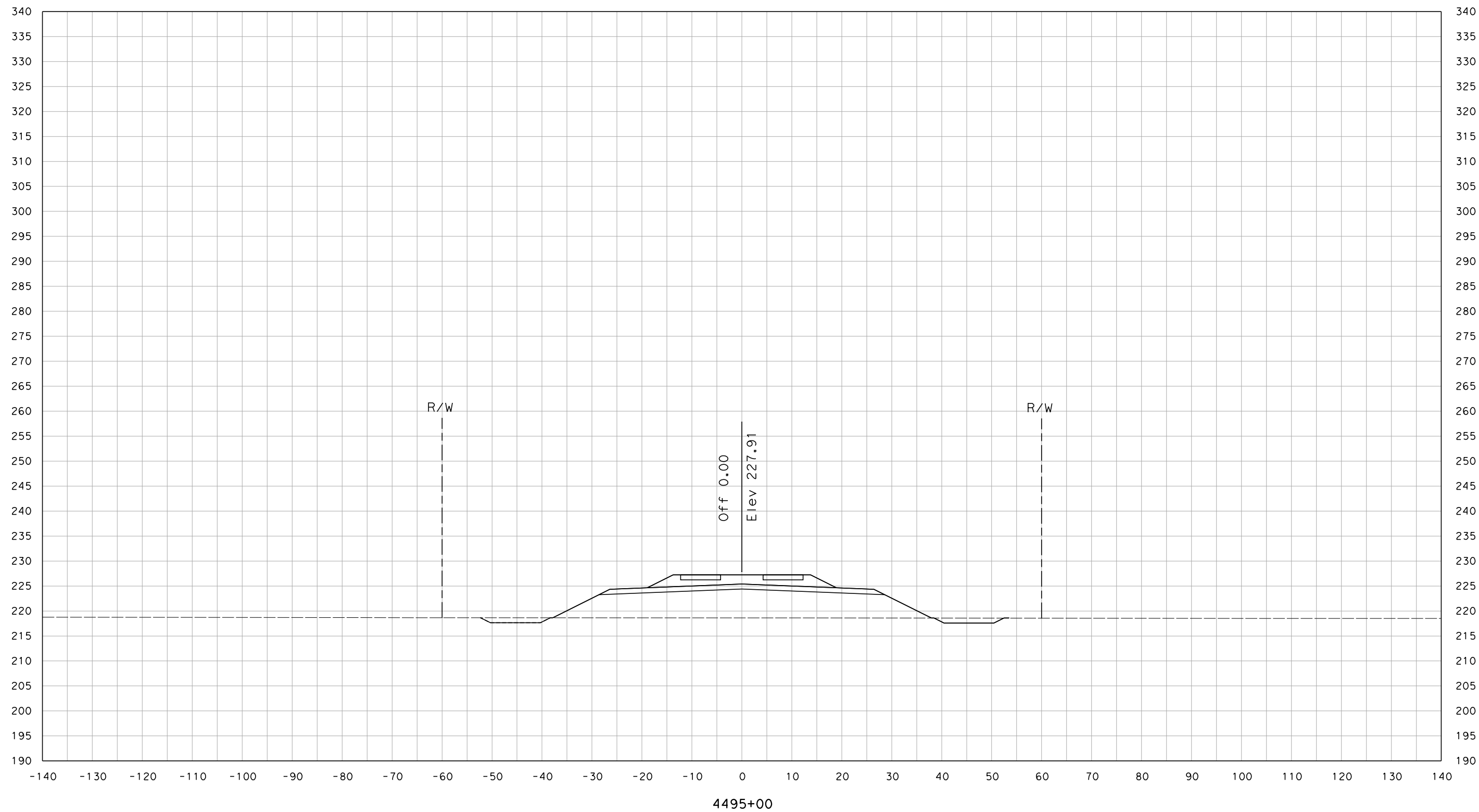
STA. 4480+00 TO STA. 4480+00



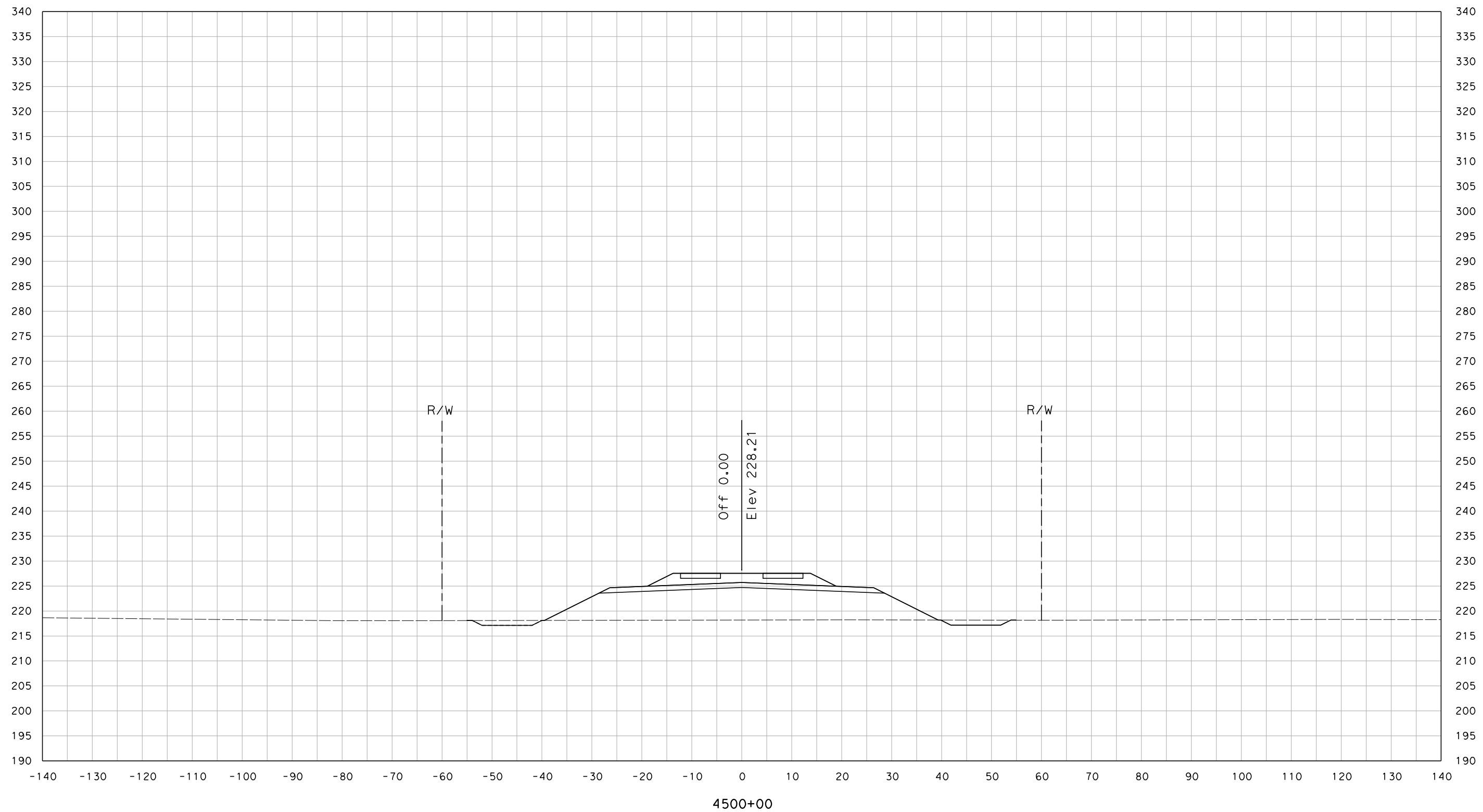
STA. 4485+00 TO STA. 4485+00



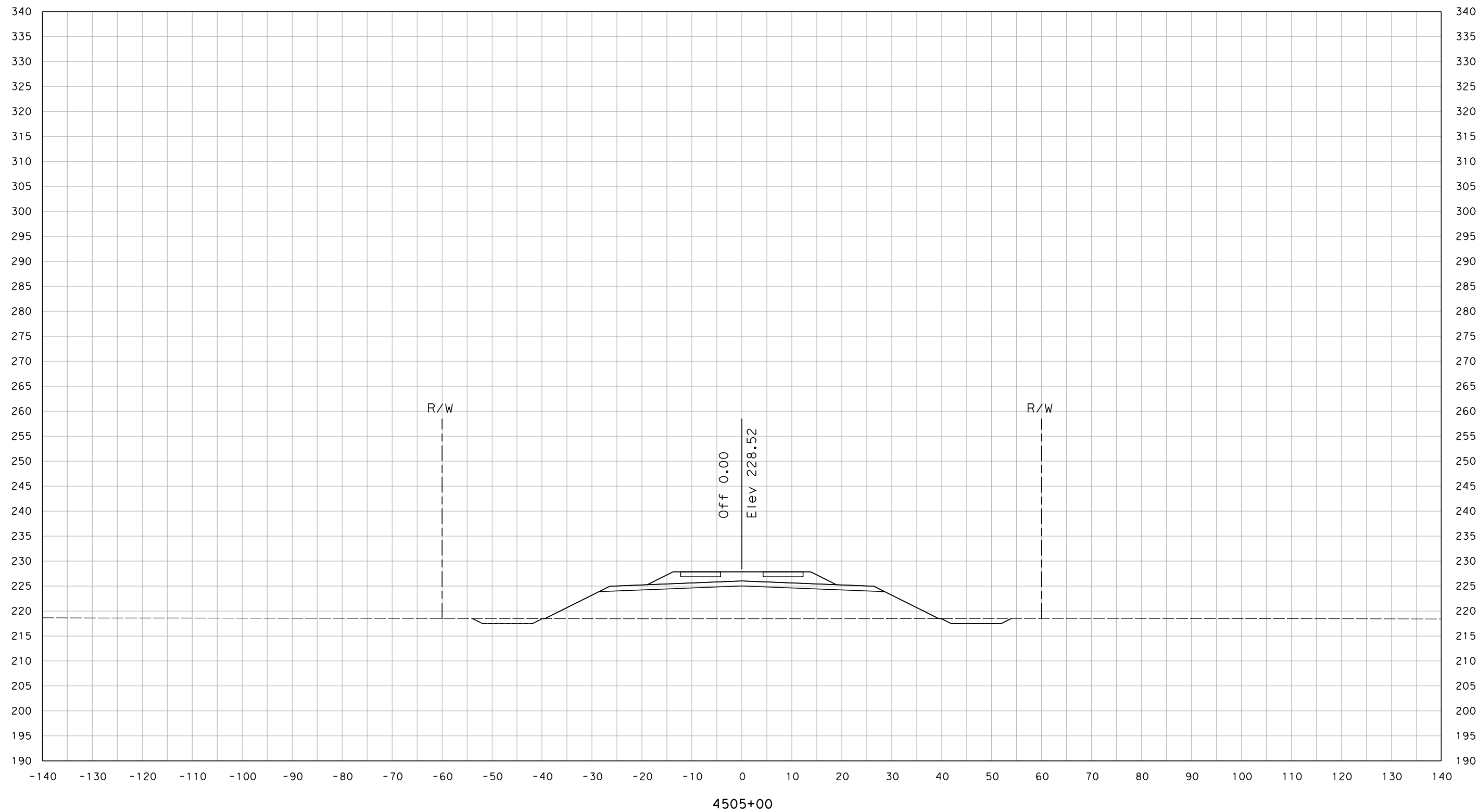
STA. 4490+00 TO STA. 4490+00



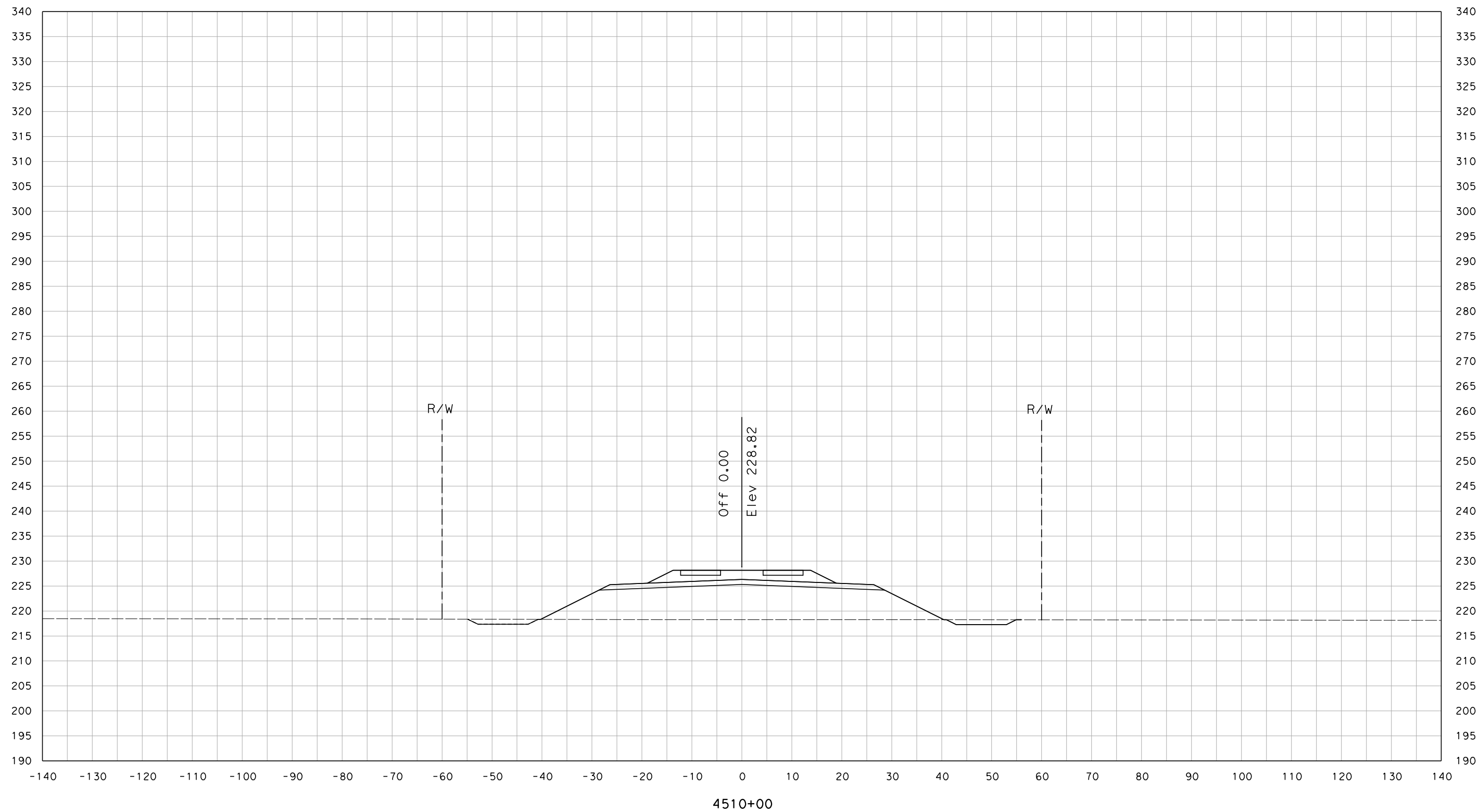
STA. 4495+00 TO STA. 4495+00



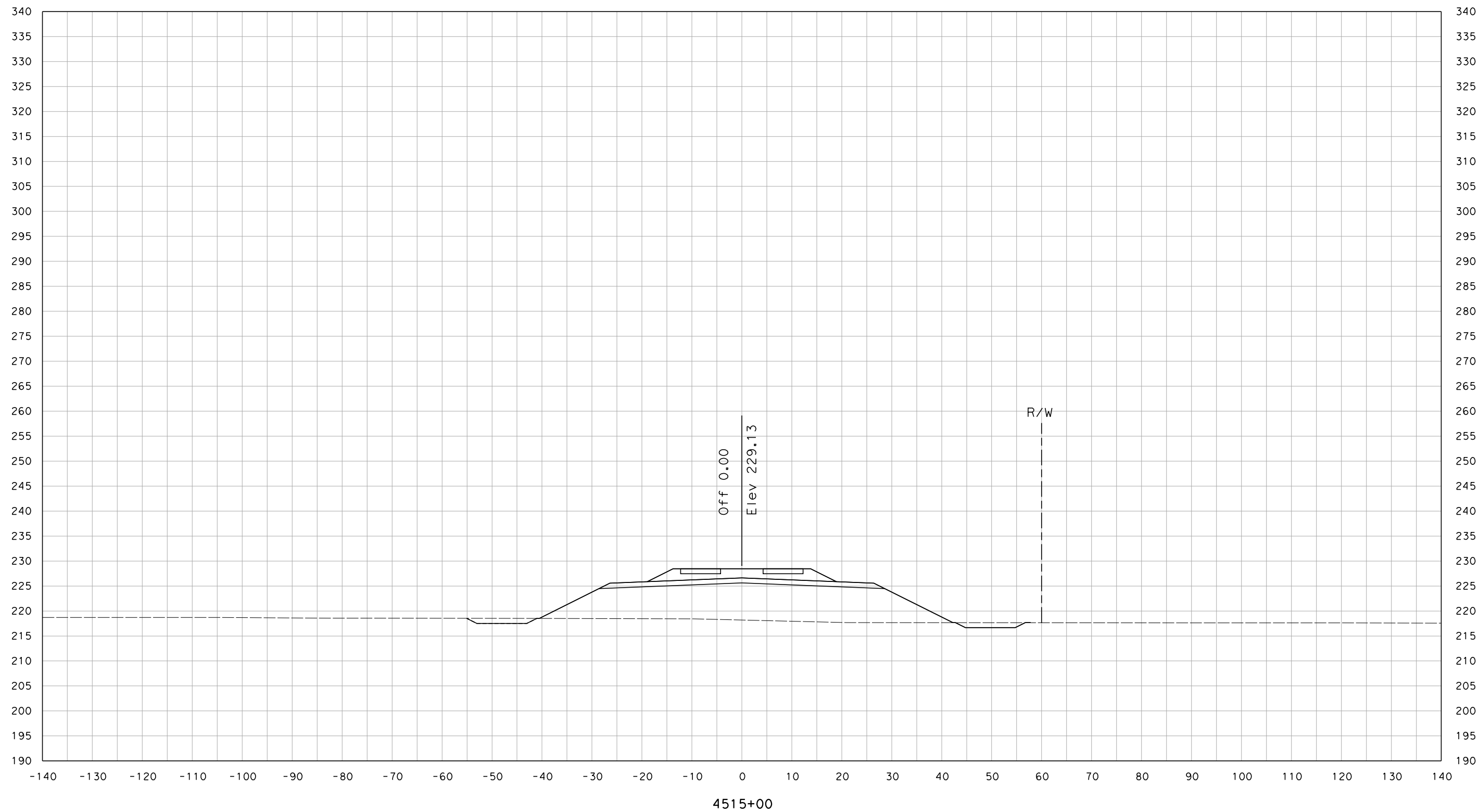
STA. 4500+00 TO STA. 4500+00



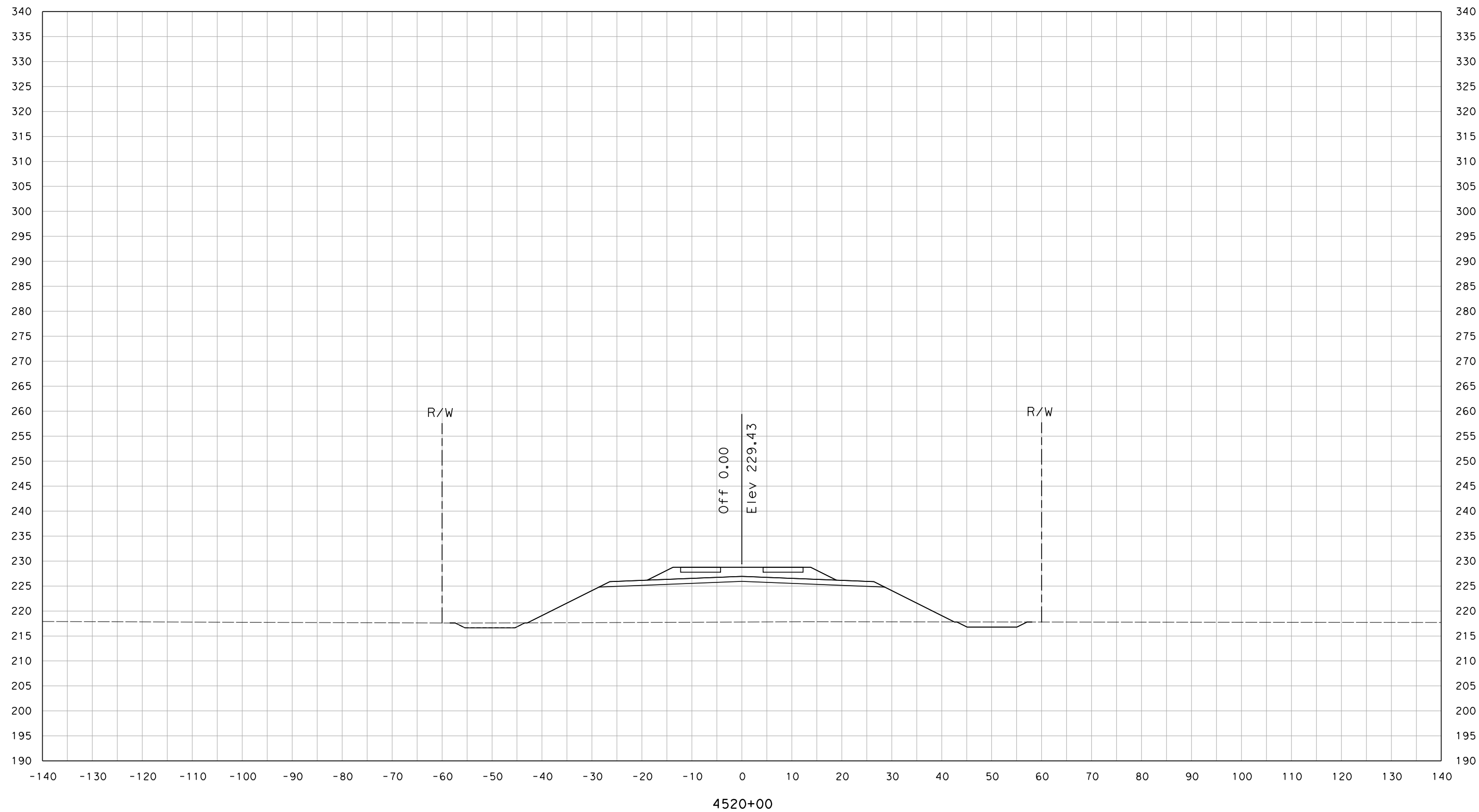
STA. 4505+00 TO STA. 4505+00



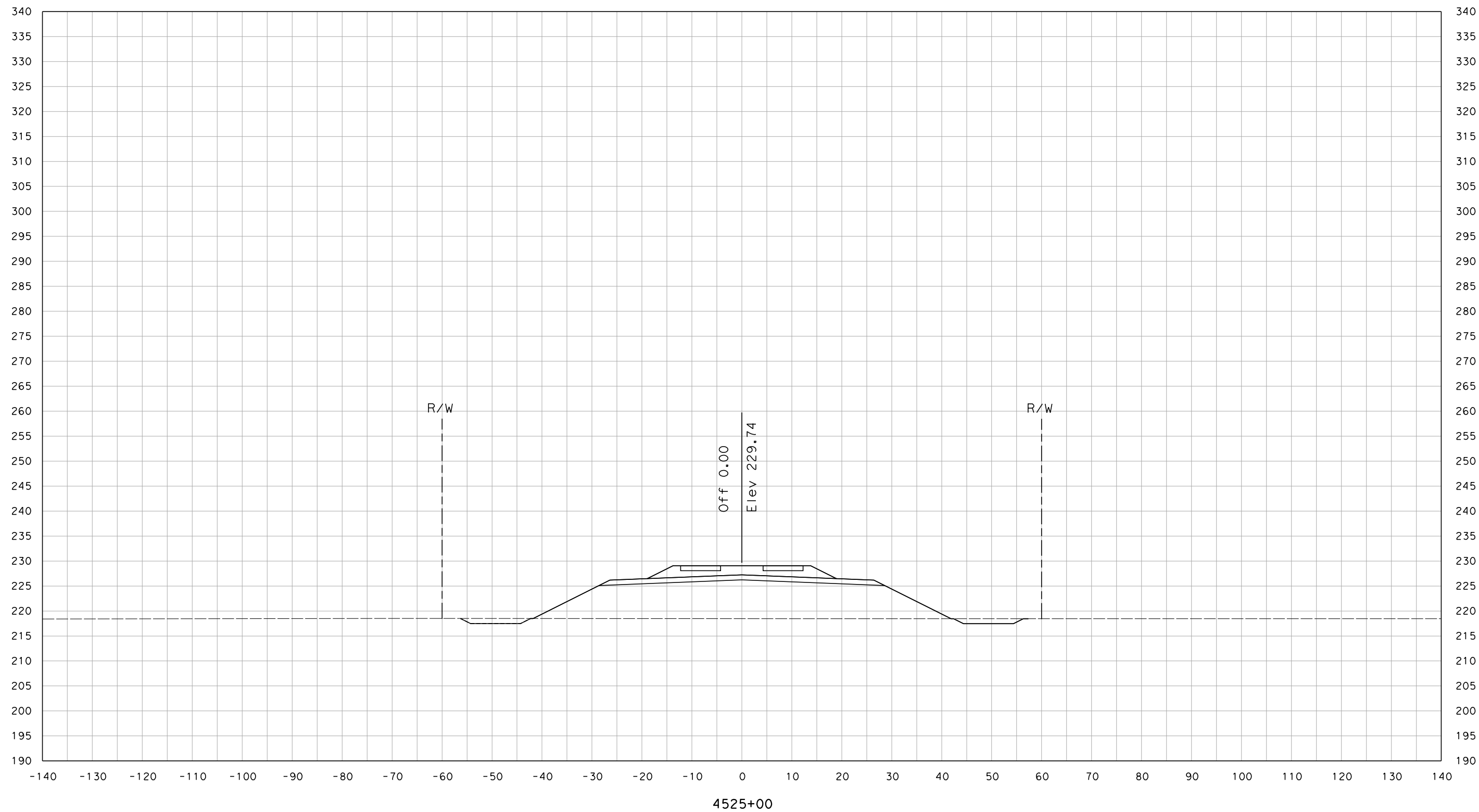
STA. 4510+00 TO STA. 4510+00



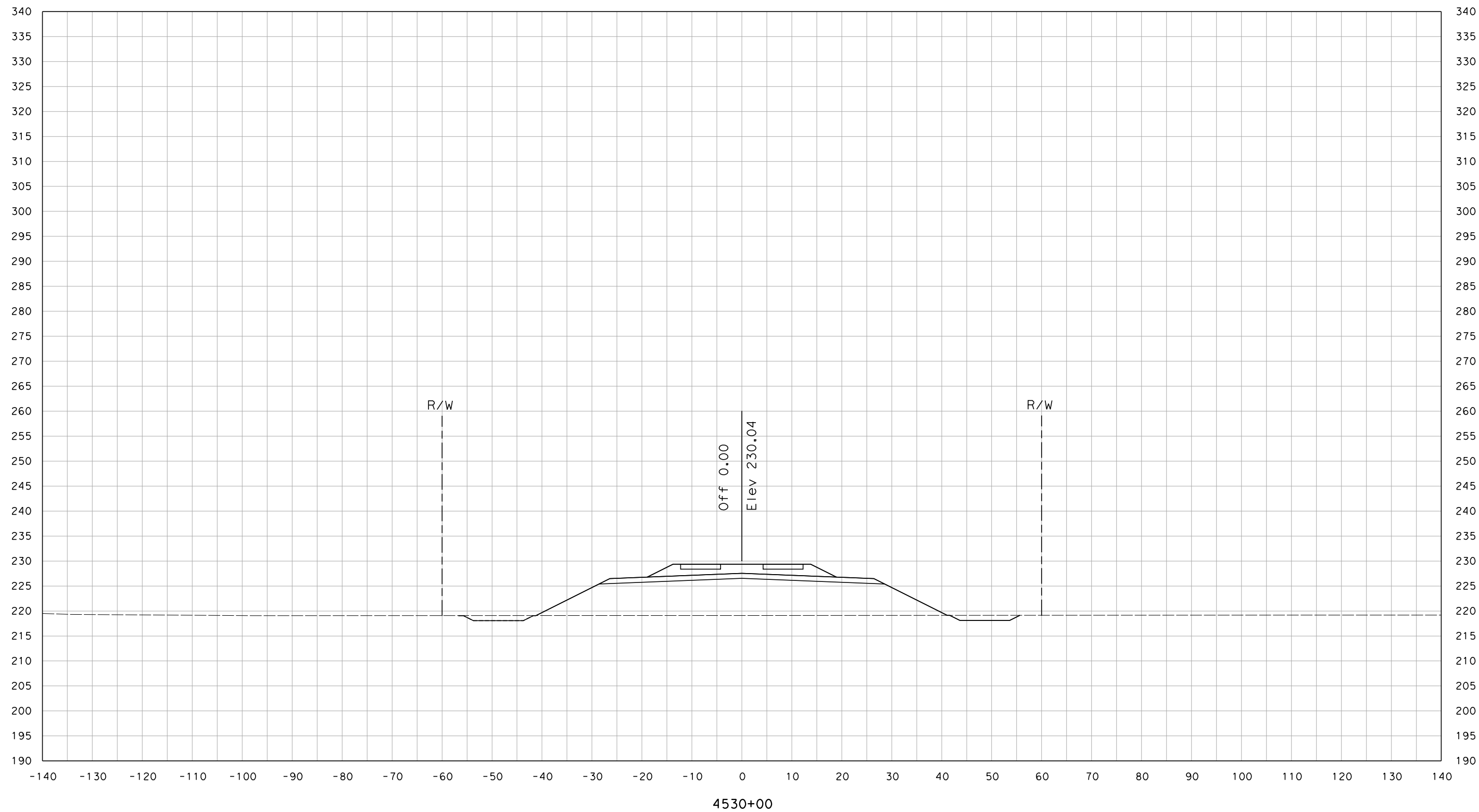
STA. 4515+00 TO STA. 4515+00



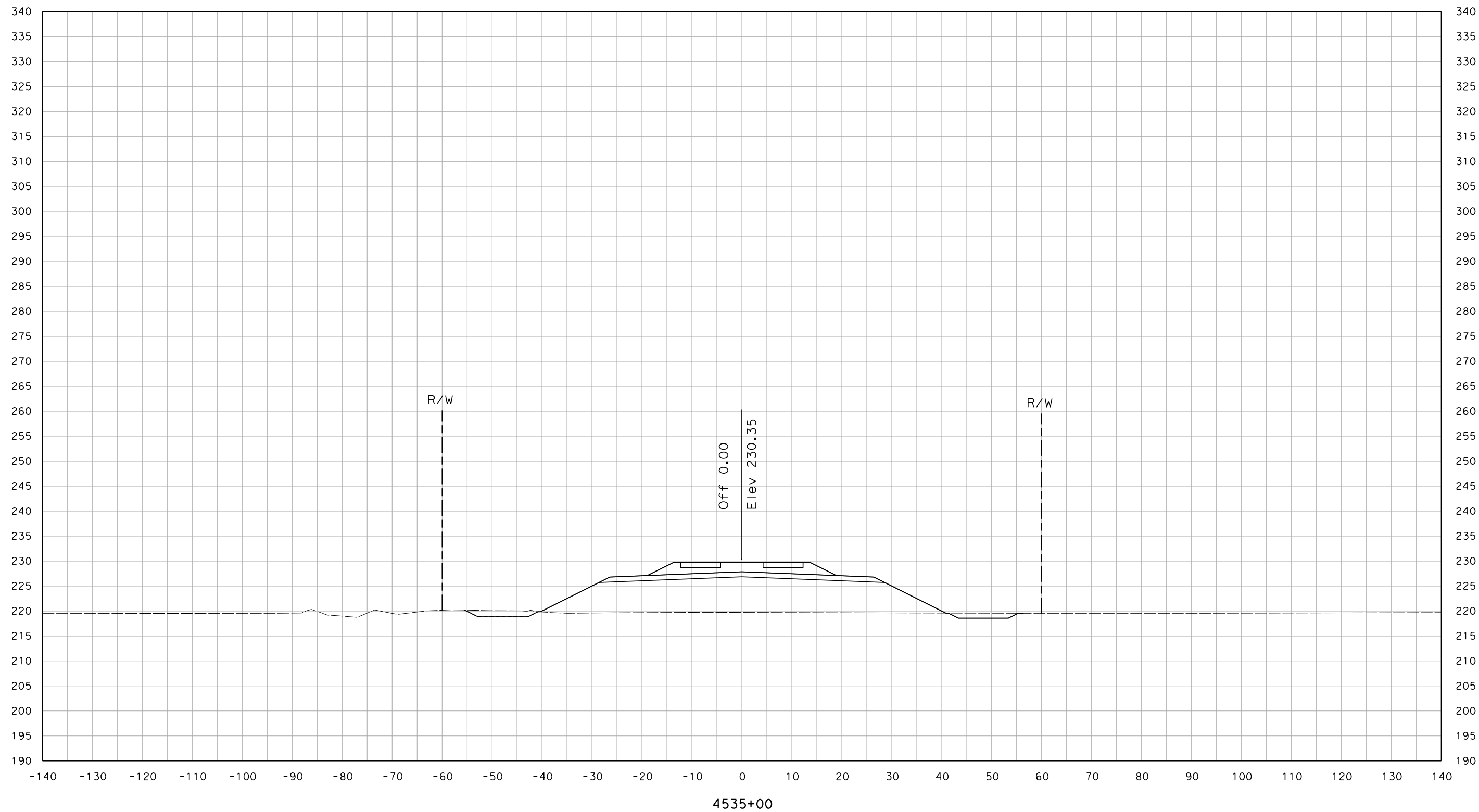
STA. 4520+00 TO STA. 4520+00



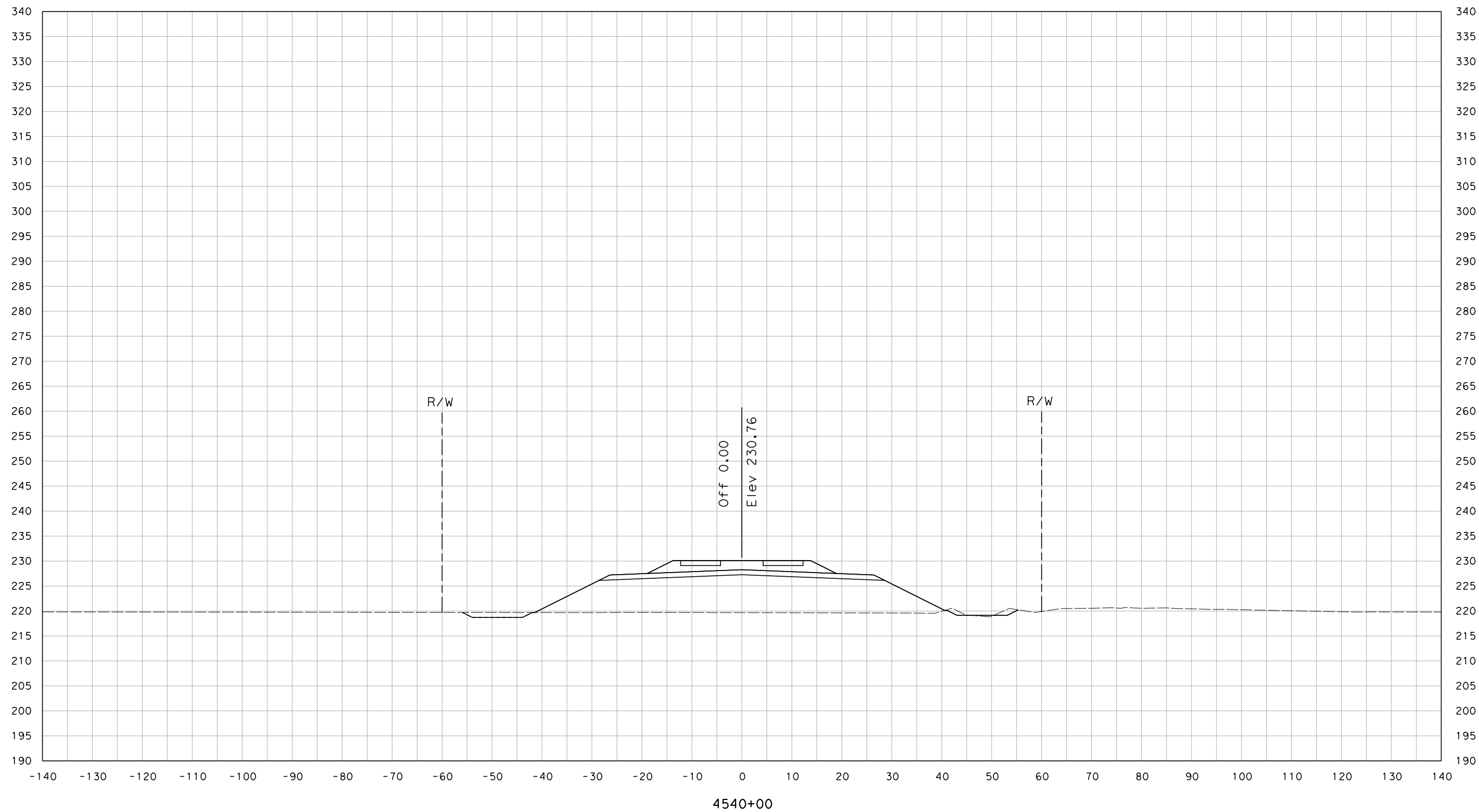
STA. 4525+00 TO STA. 4525+00



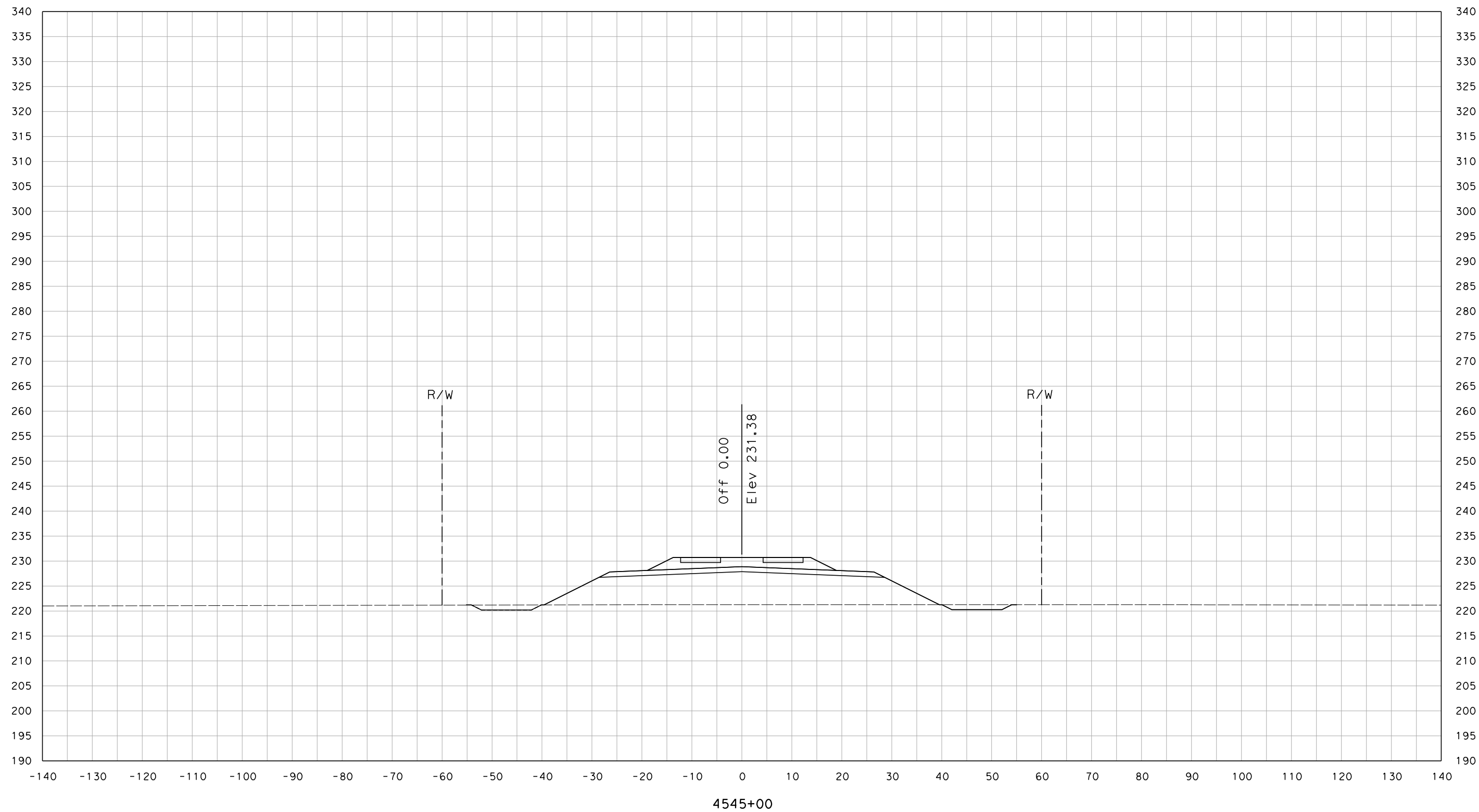
STA. 4530+00 TO STA. 4530+00



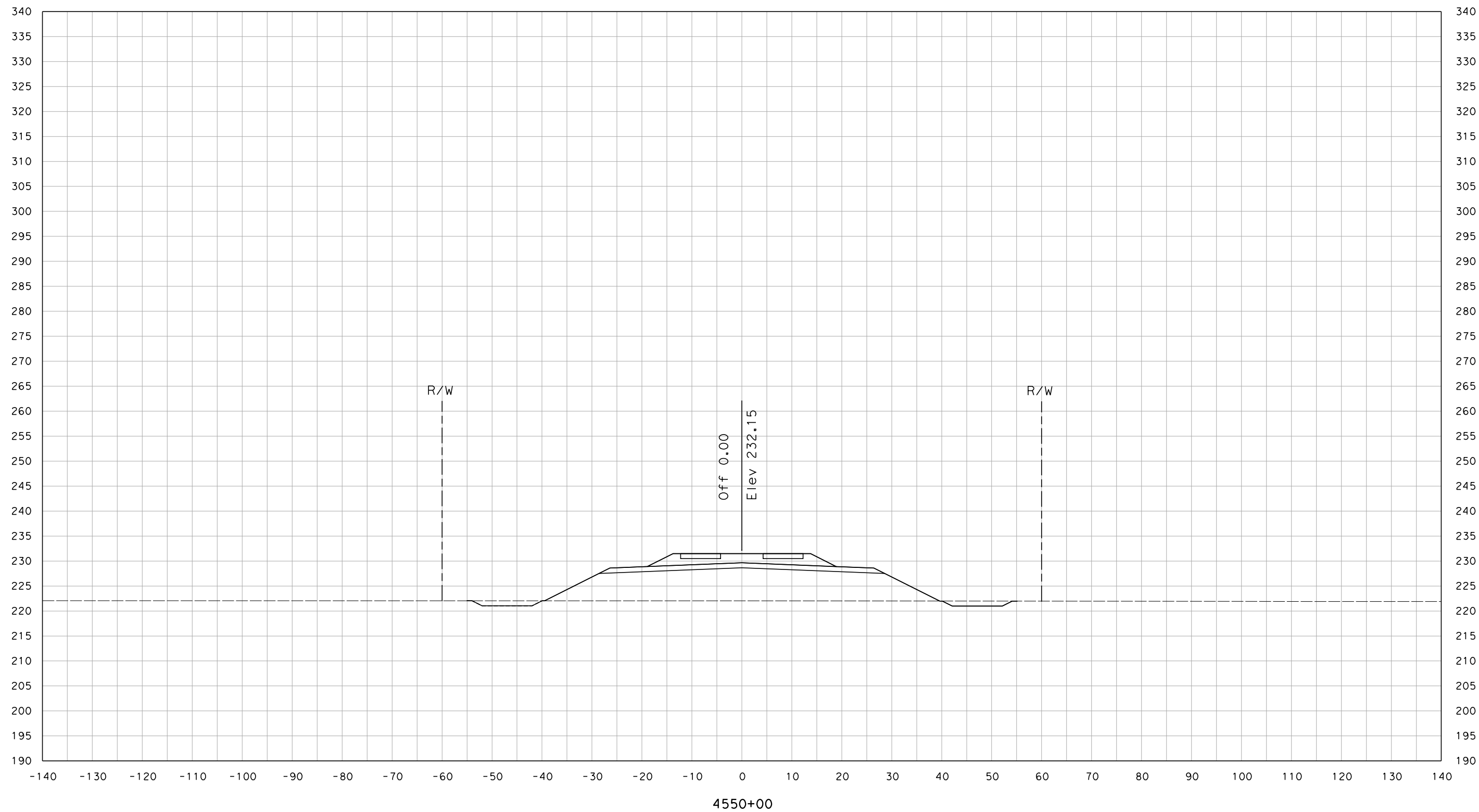
STA. 4535+00 TO STA. 4535+00



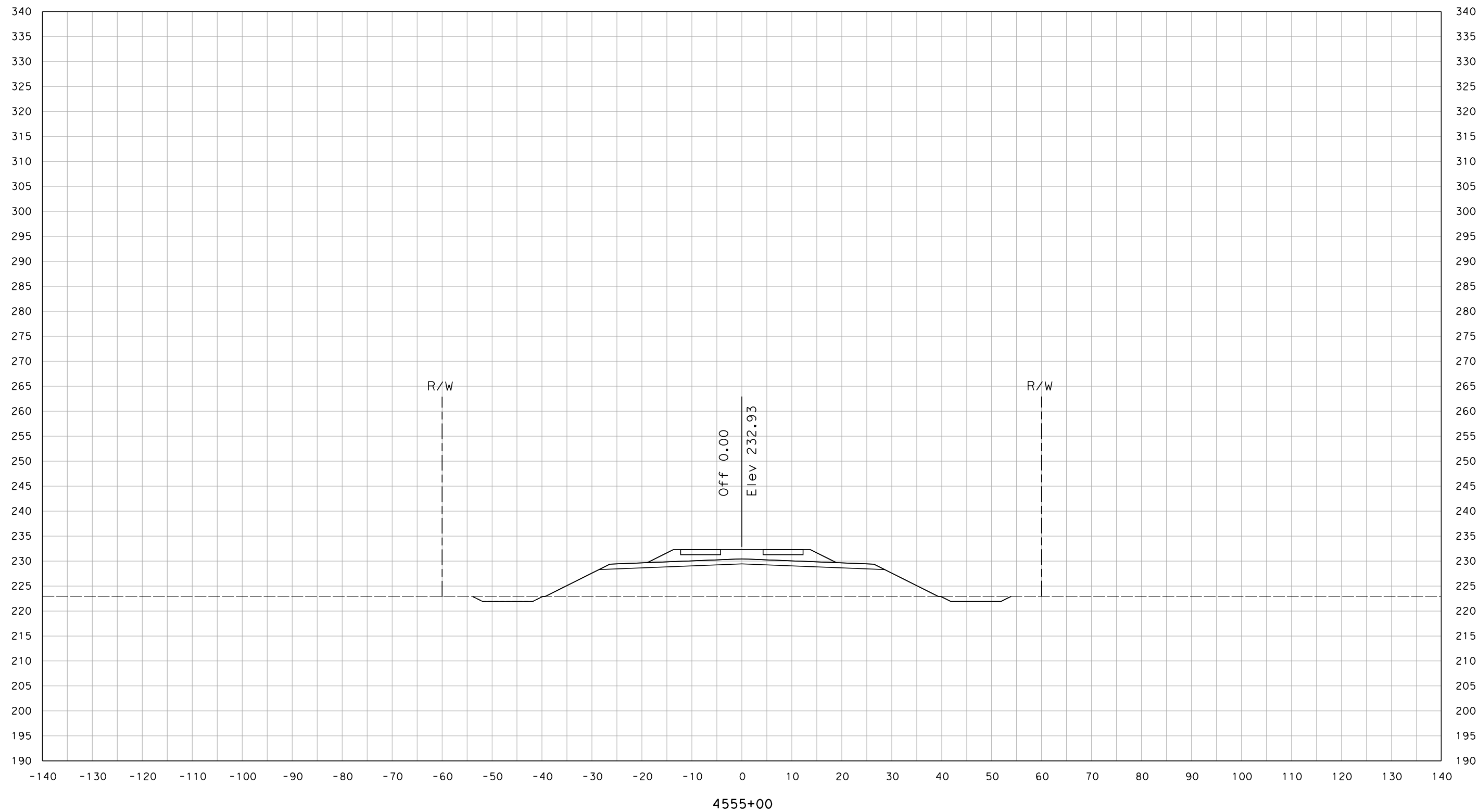
STA. 4540+00 TO STA. 4540+00



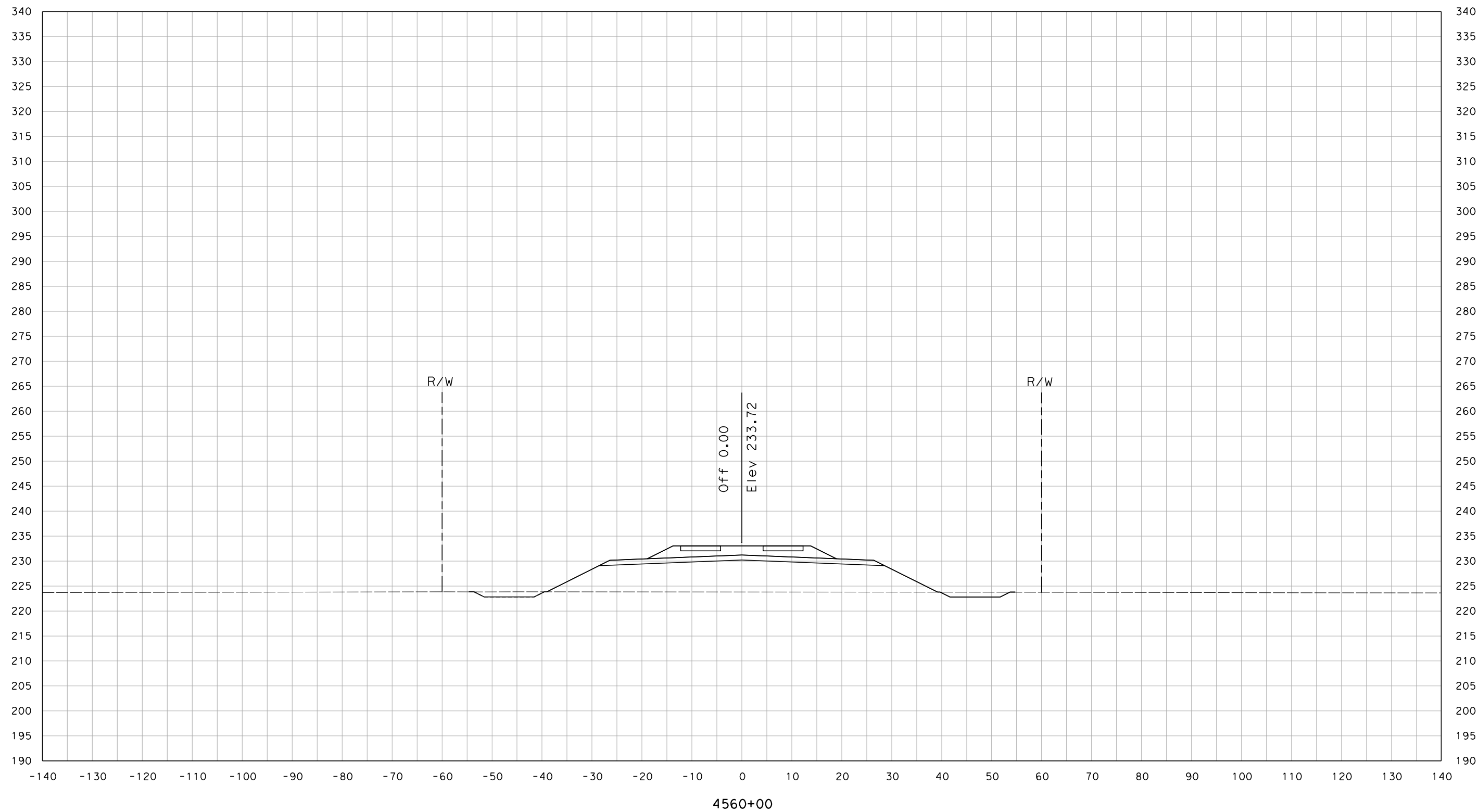
STA. 4545+00 TO STA. 4545+00



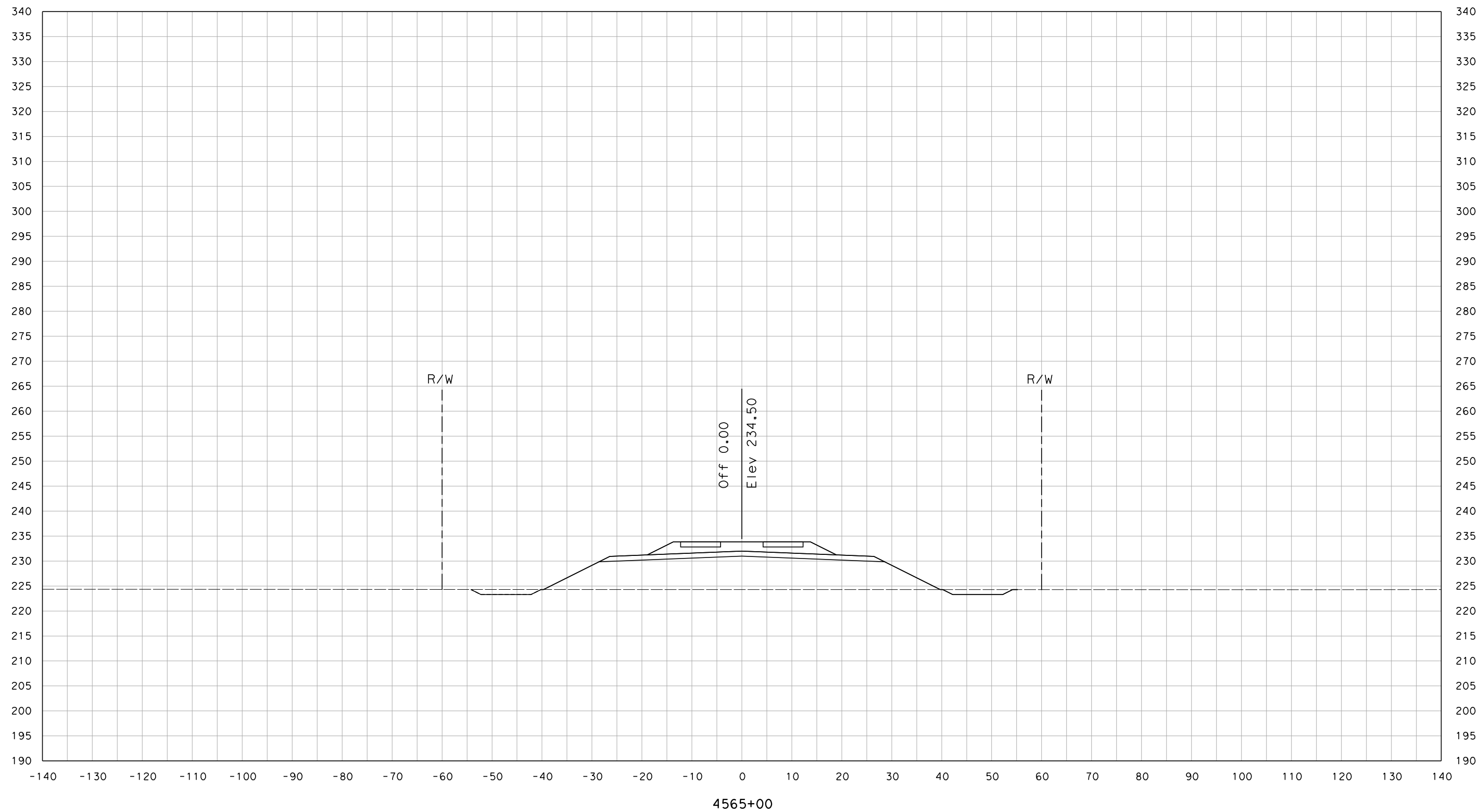
STA. 4550+00 TO STA. 4550+00



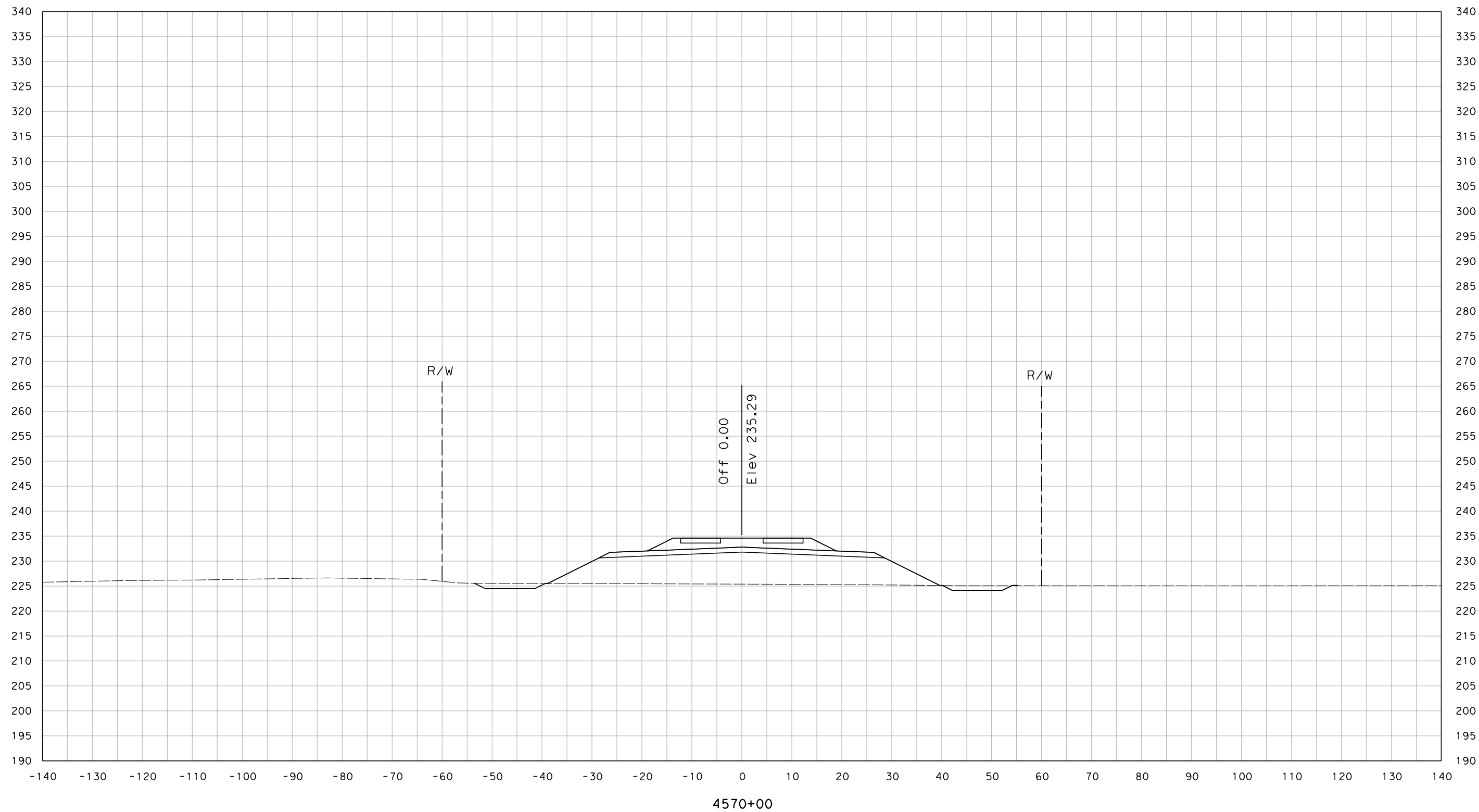
STA. 4555+00 TO STA. 4555+00



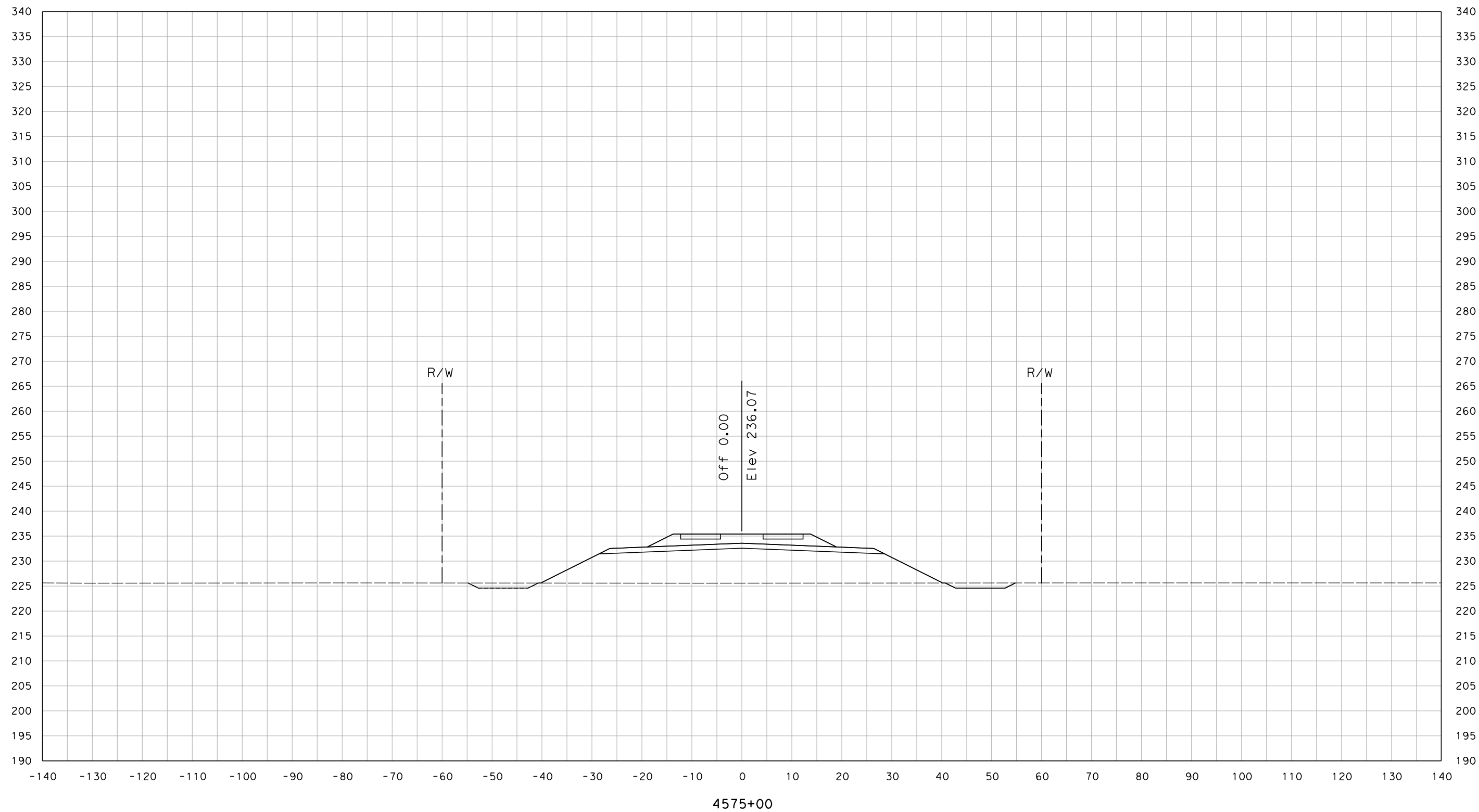
STA. 4560+00 TO STA. 4560+00



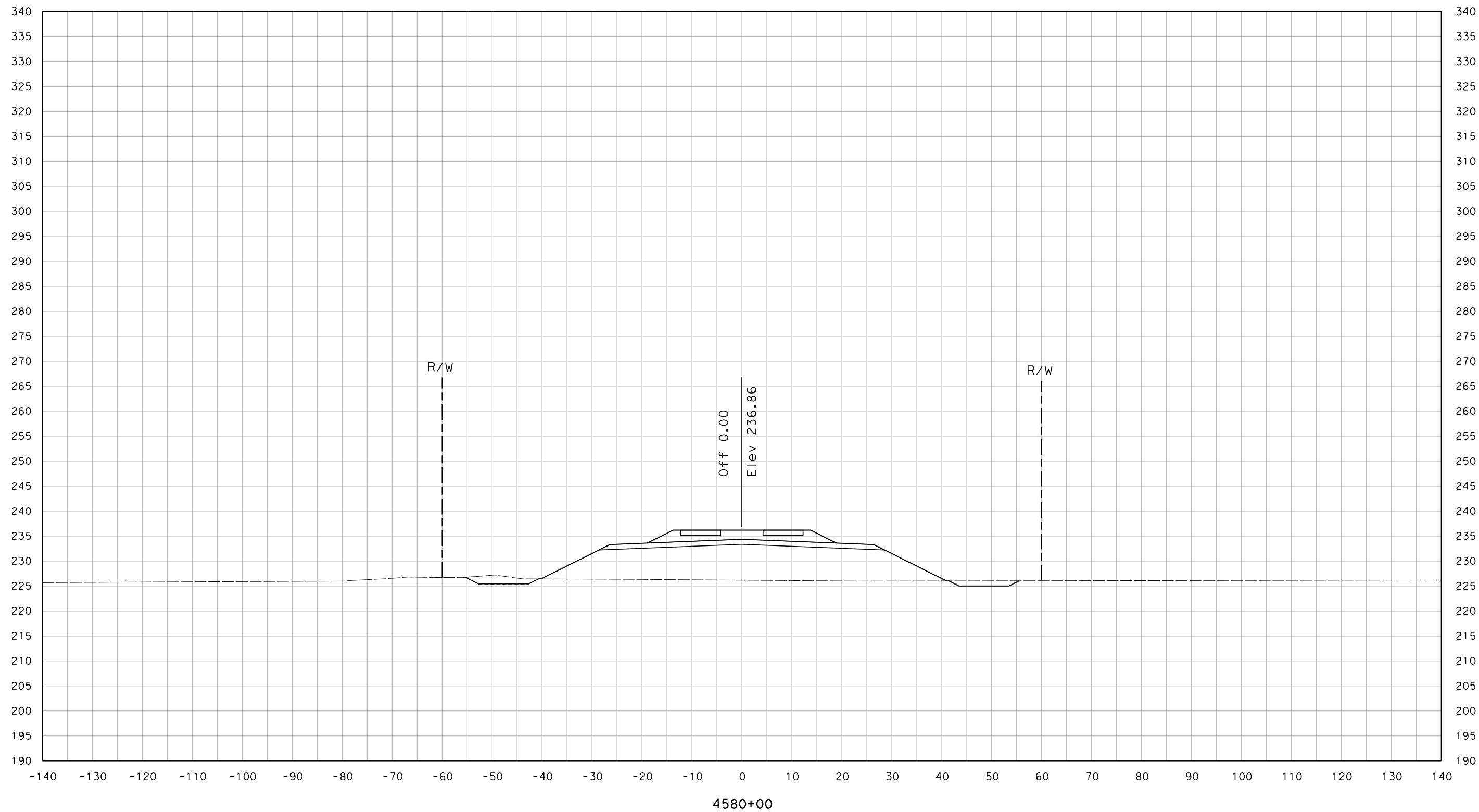
STA. 4565+00 TO STA. 4565+00



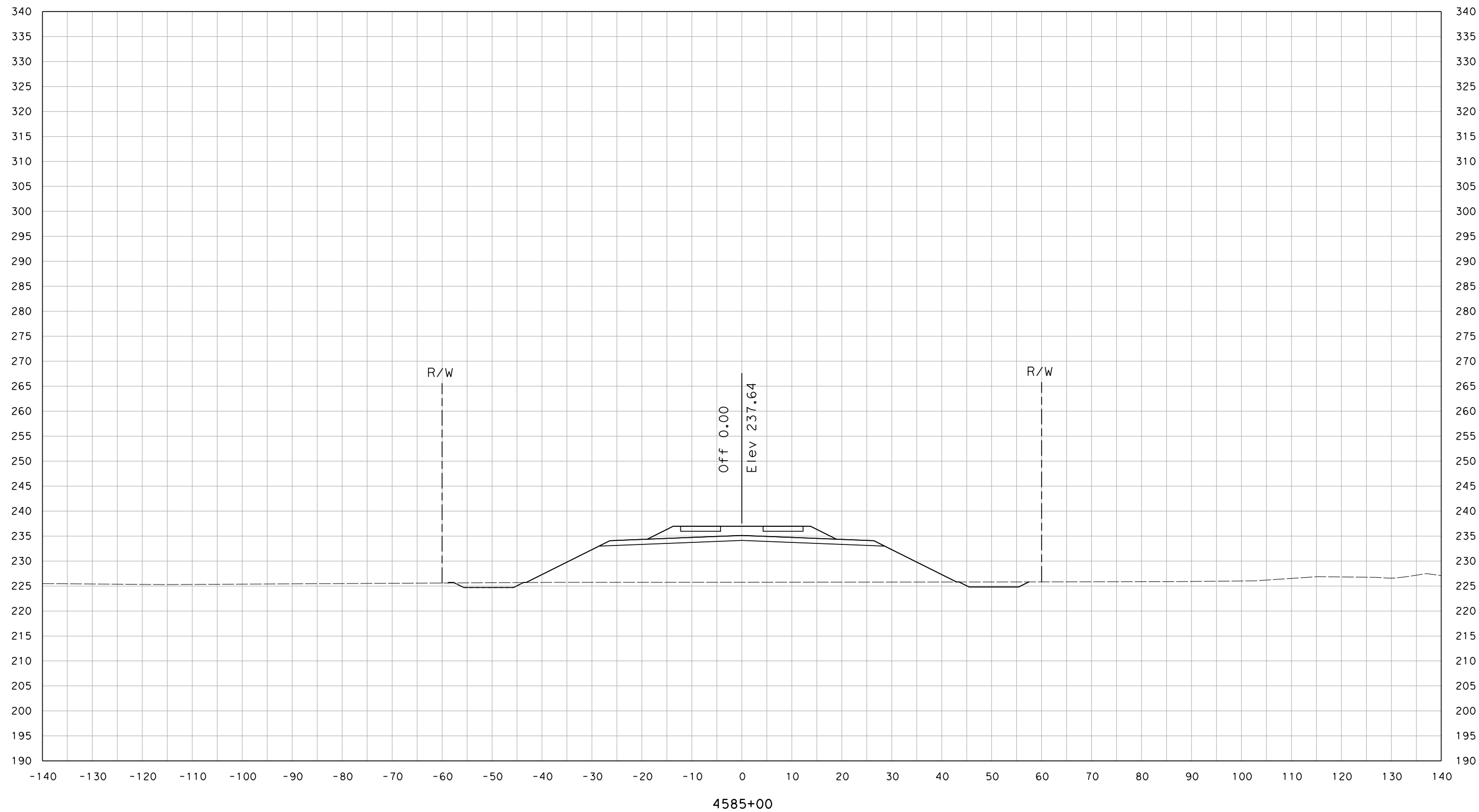
STA. 4570+00 TO STA. 4570+00



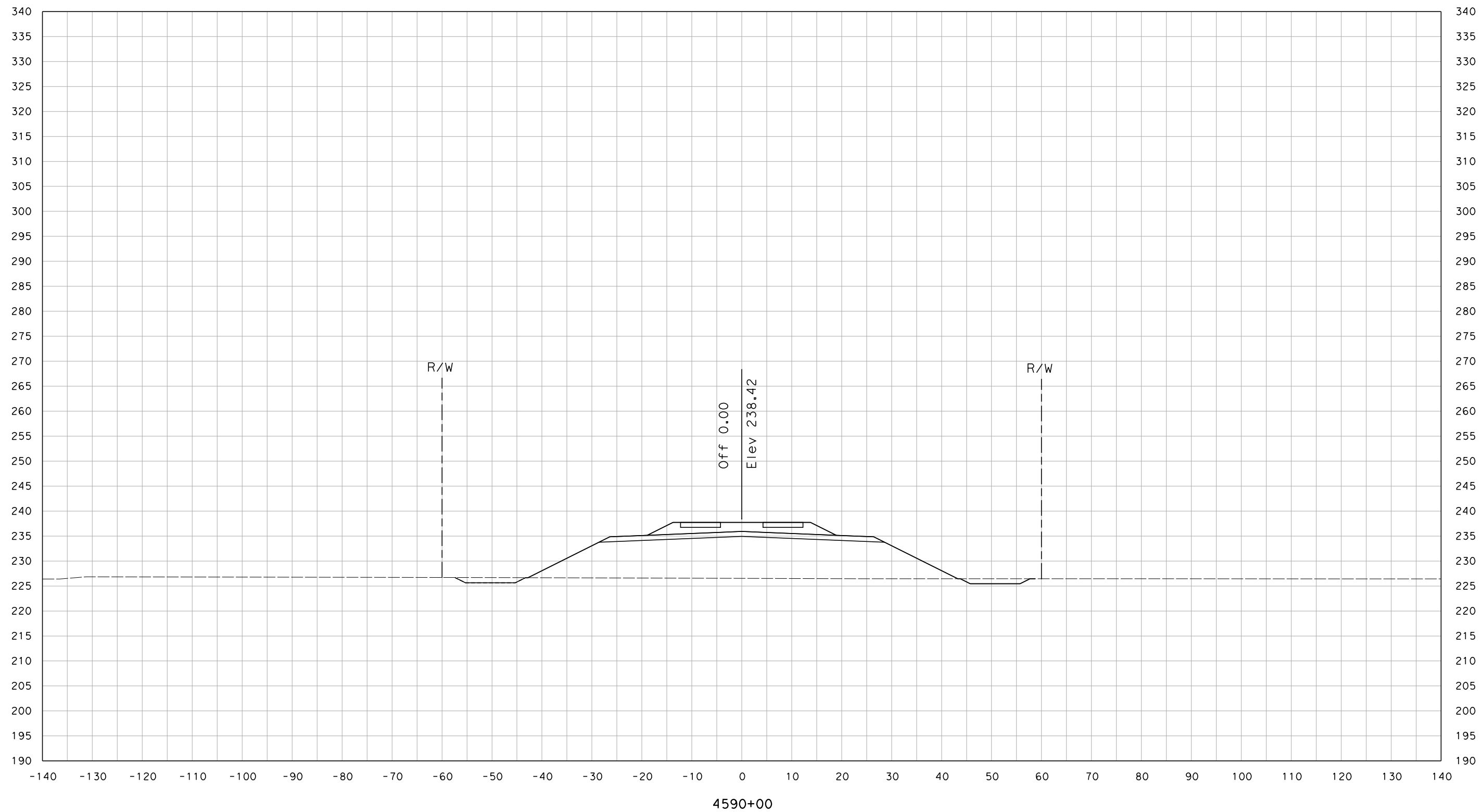
STA. 4575+00 TO STA. 4575+00



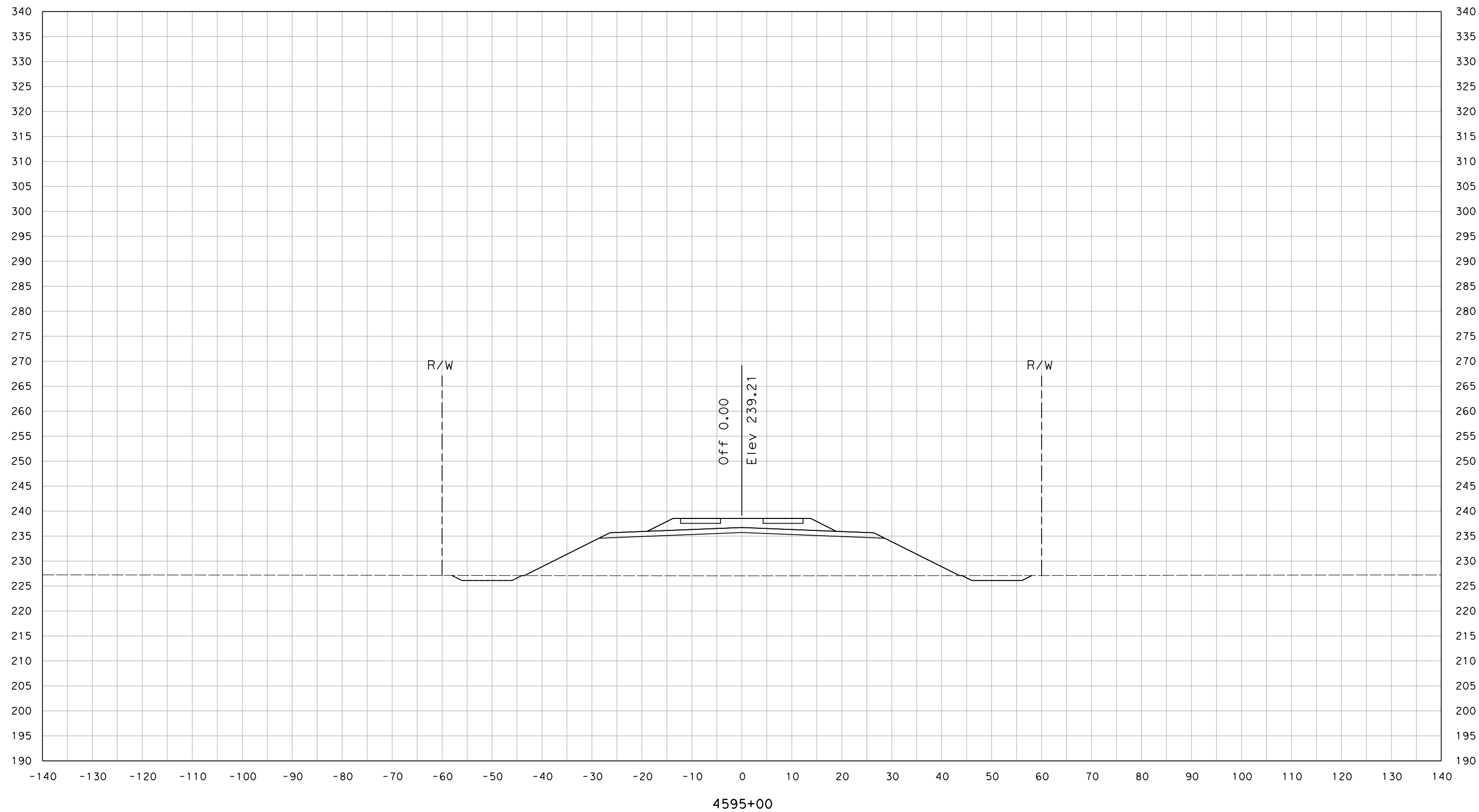
STA. 4580+00 TO STA. 4580+00



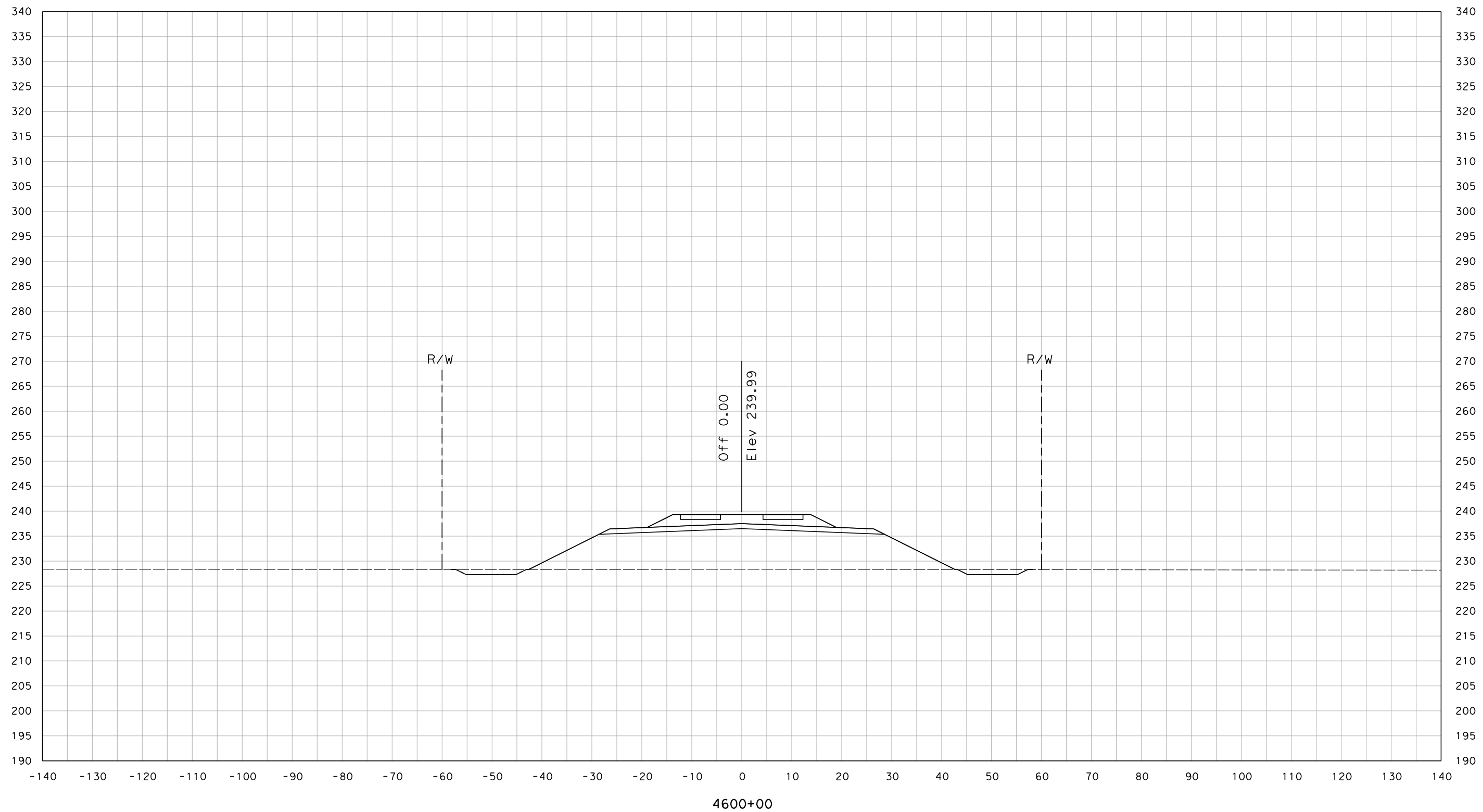
STA. 4585+00 TO STA. 4585+00



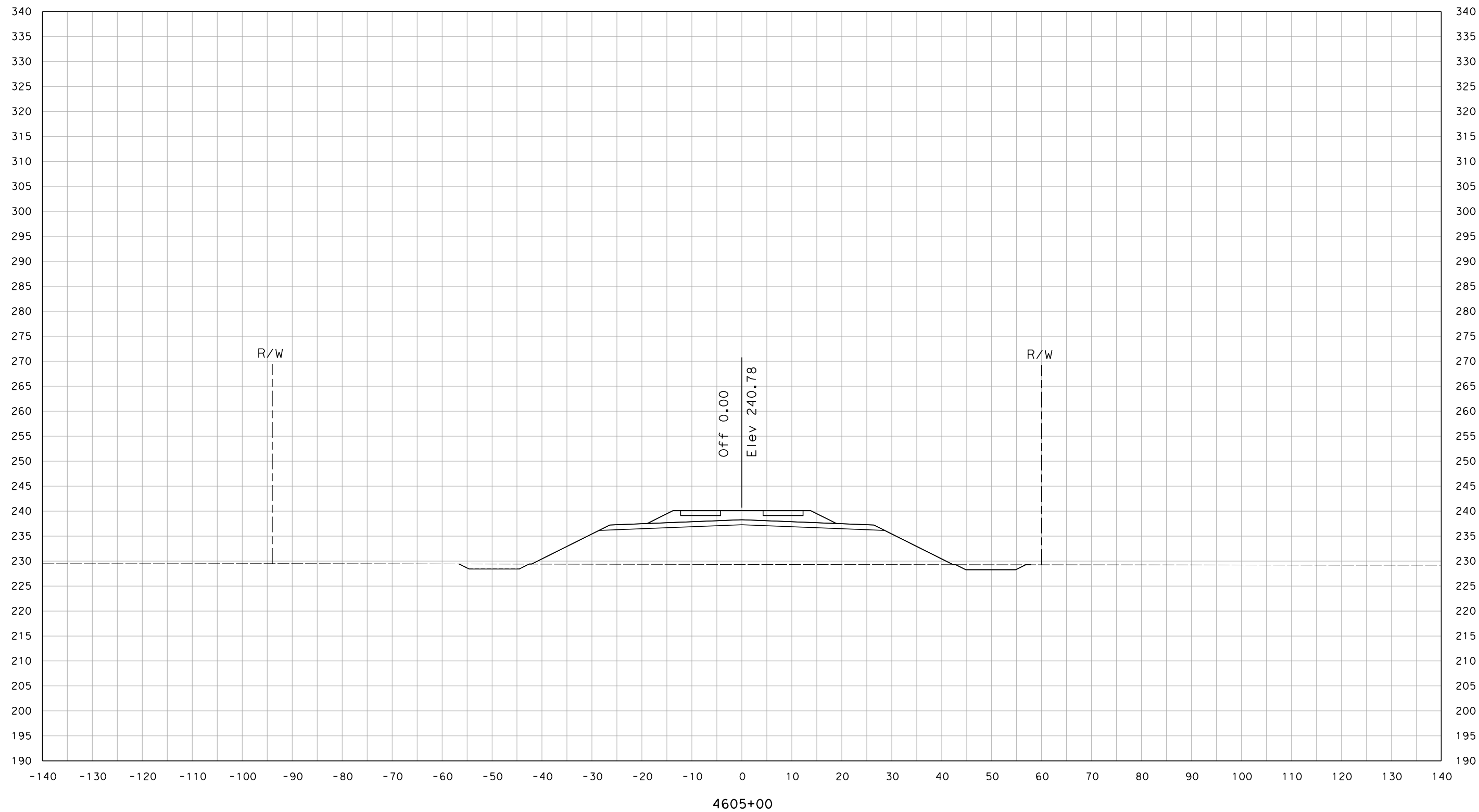
STA. 4590+00 TO STA. 4590+00



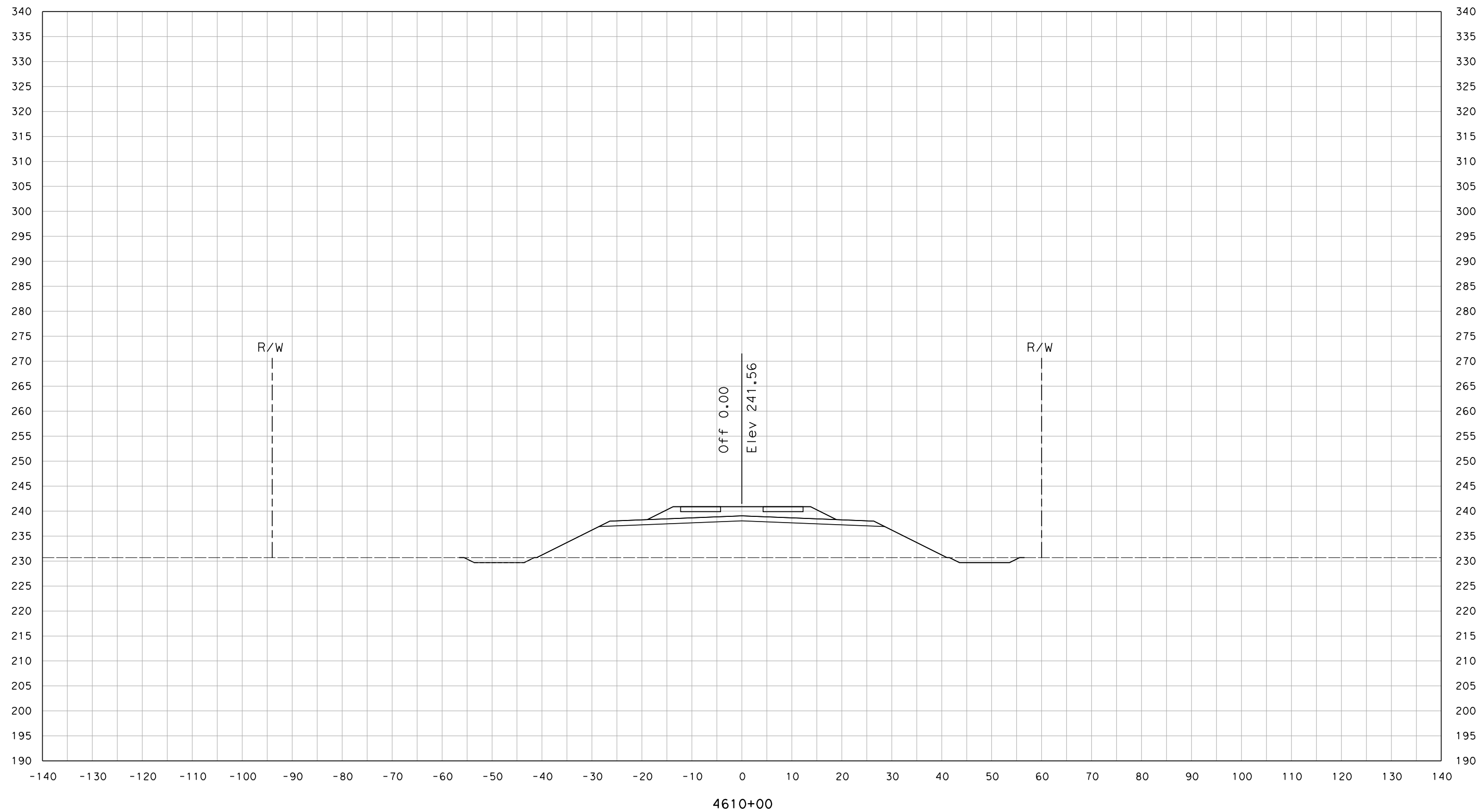
STA. 4595+00 TO STA. 4595+00



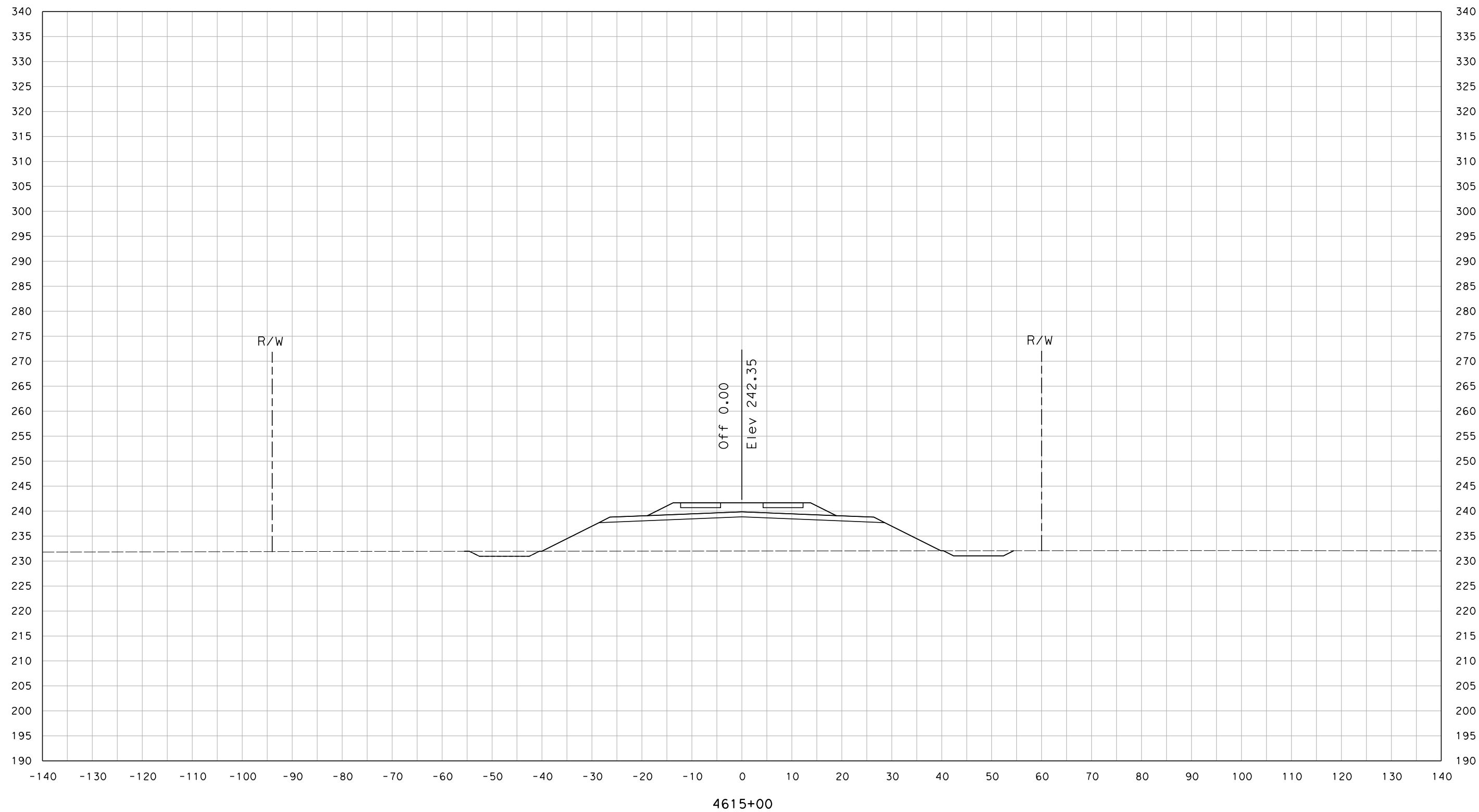
STA. 4600+00 TO STA. 4600+00



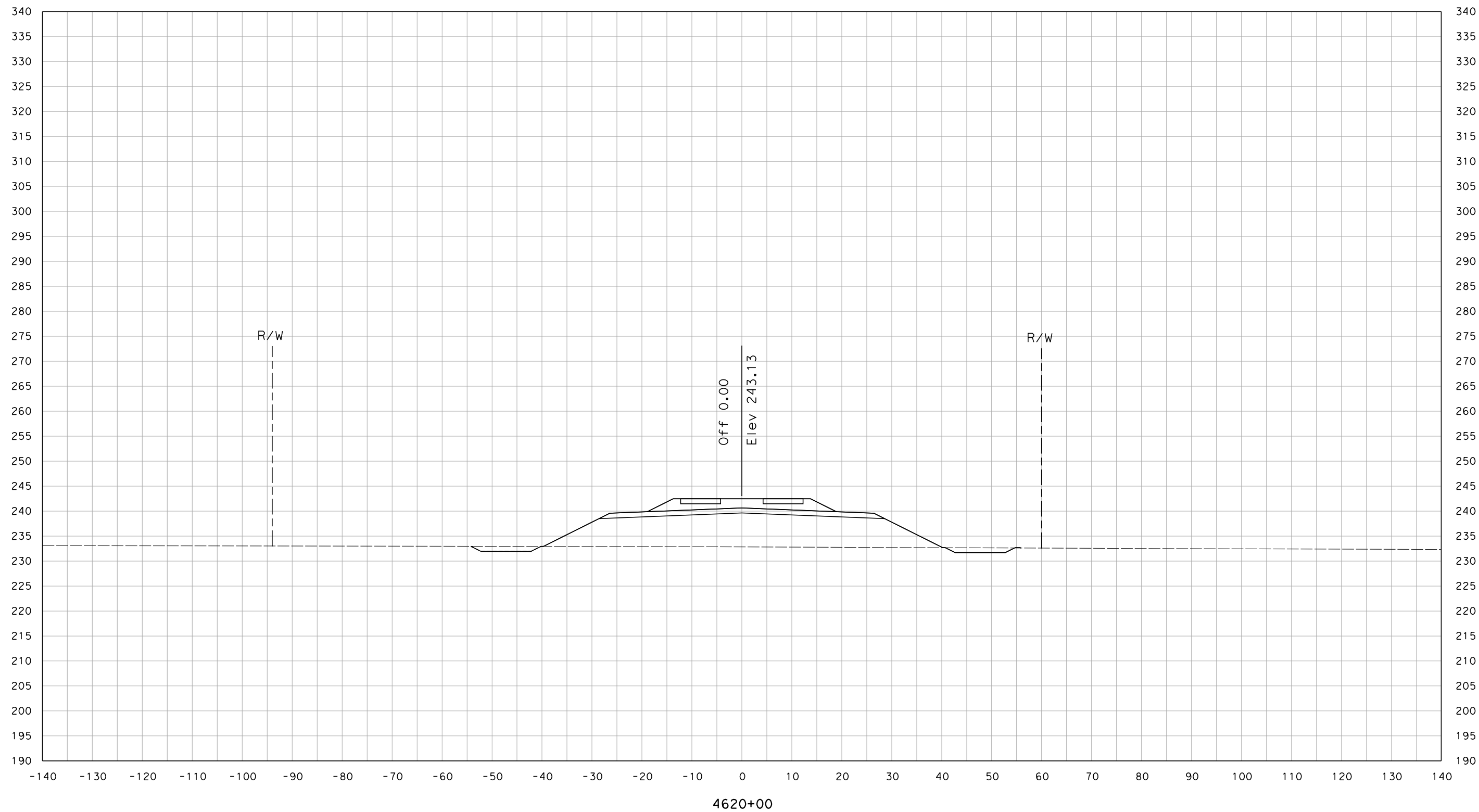
STA. 4605+00 TO STA. 4605+00



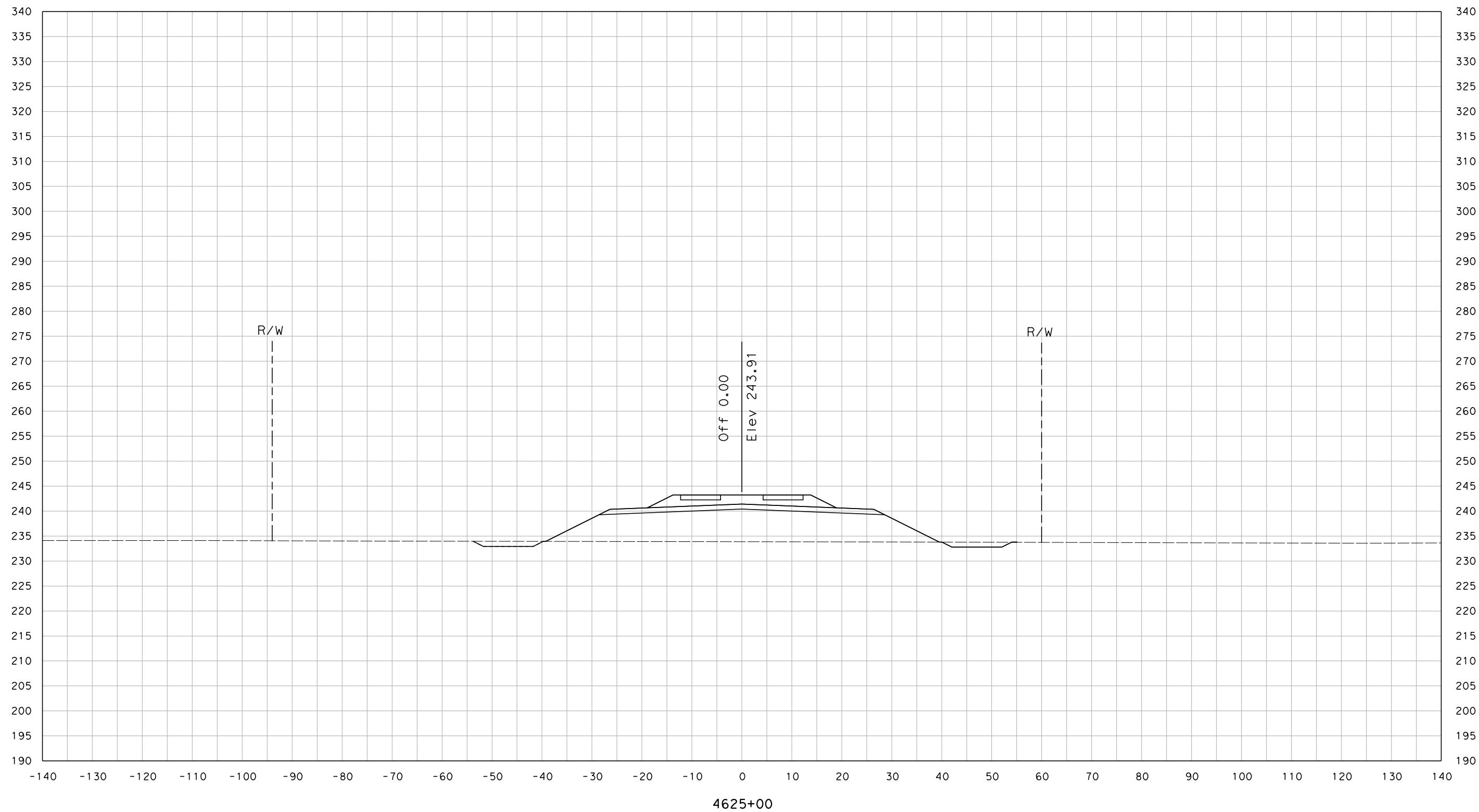
STA. 4610+00 TO STA. 4610+00



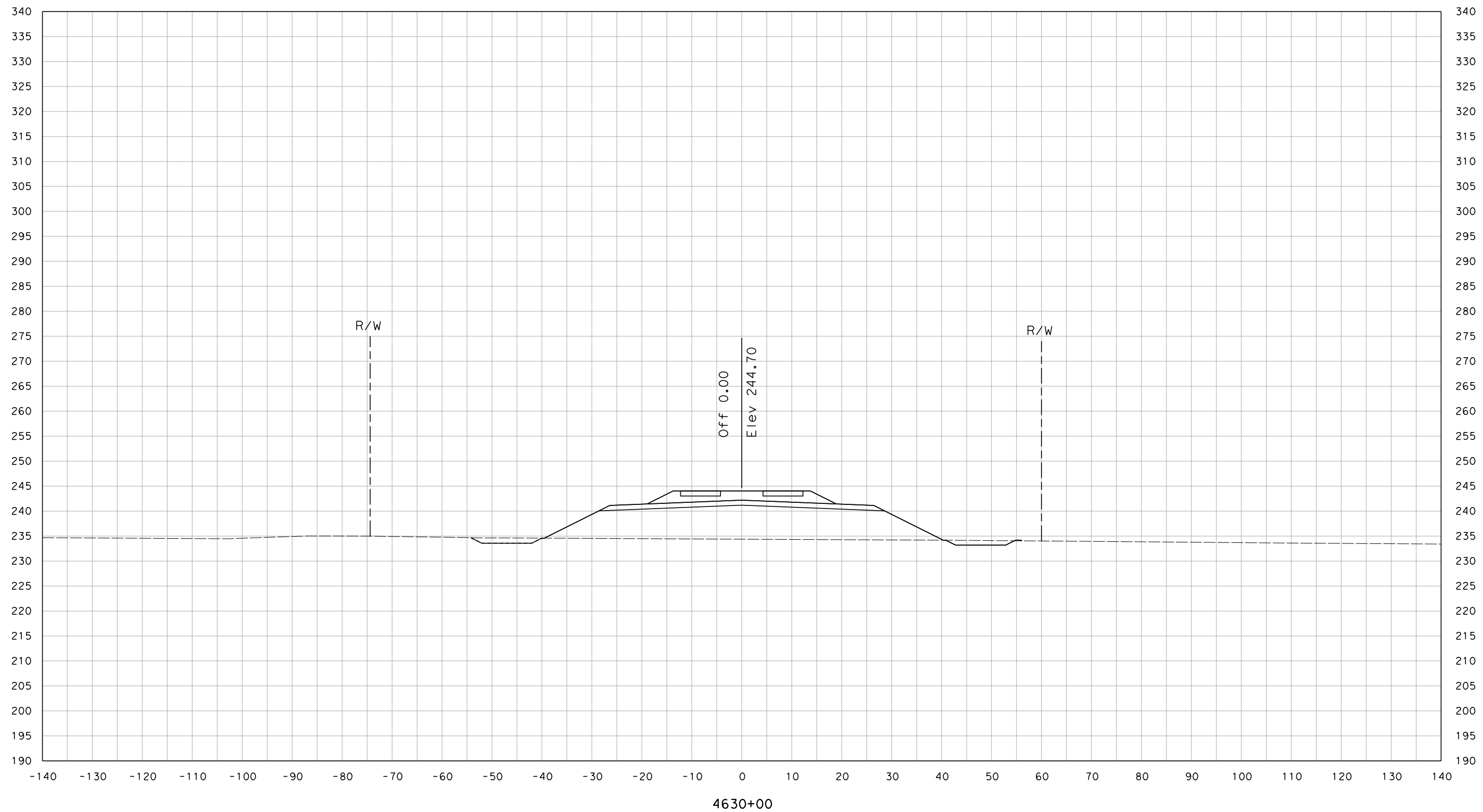
STA. 4615+00 TO STA. 4615+00



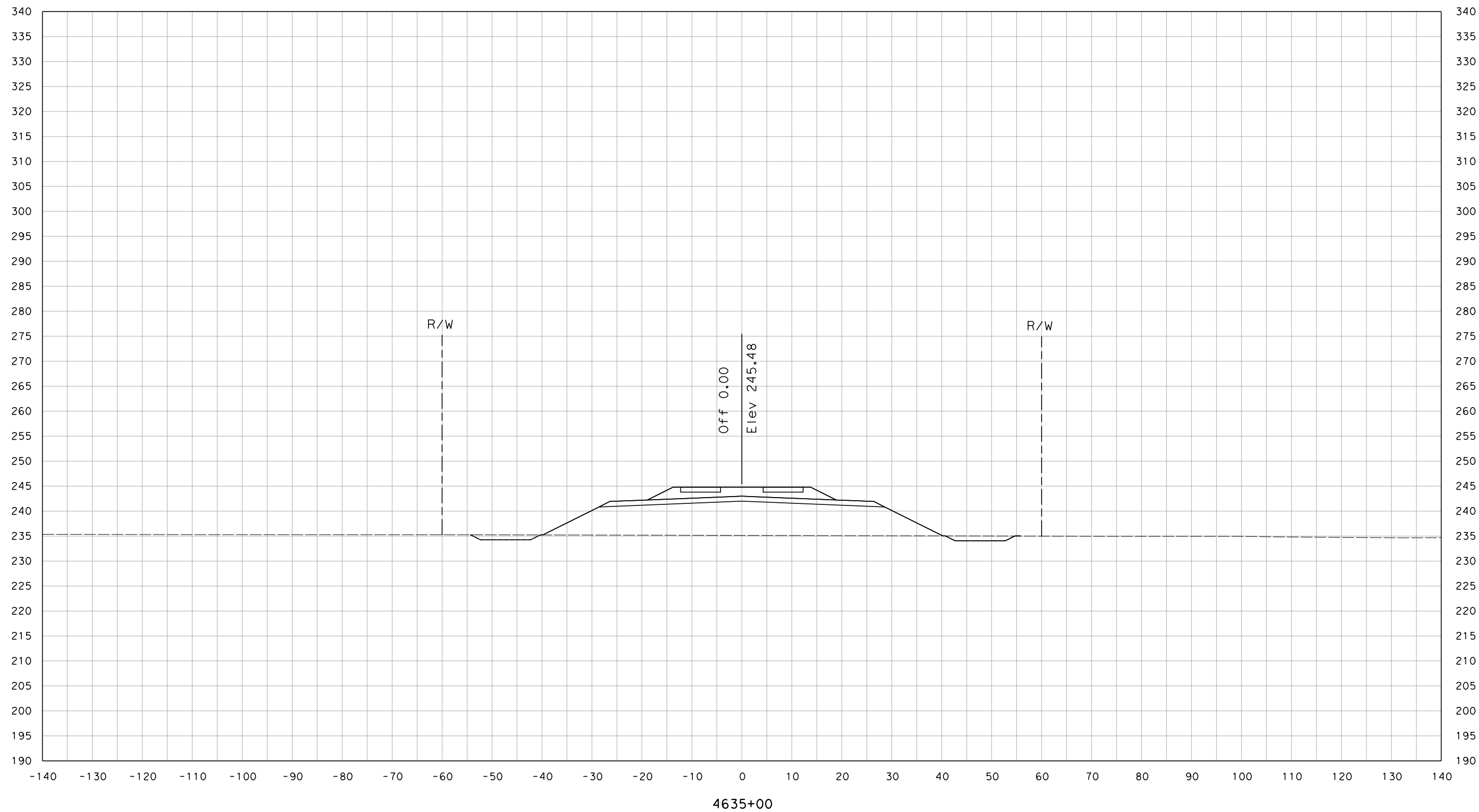
STA. 4620+00 TO STA. 4620+00



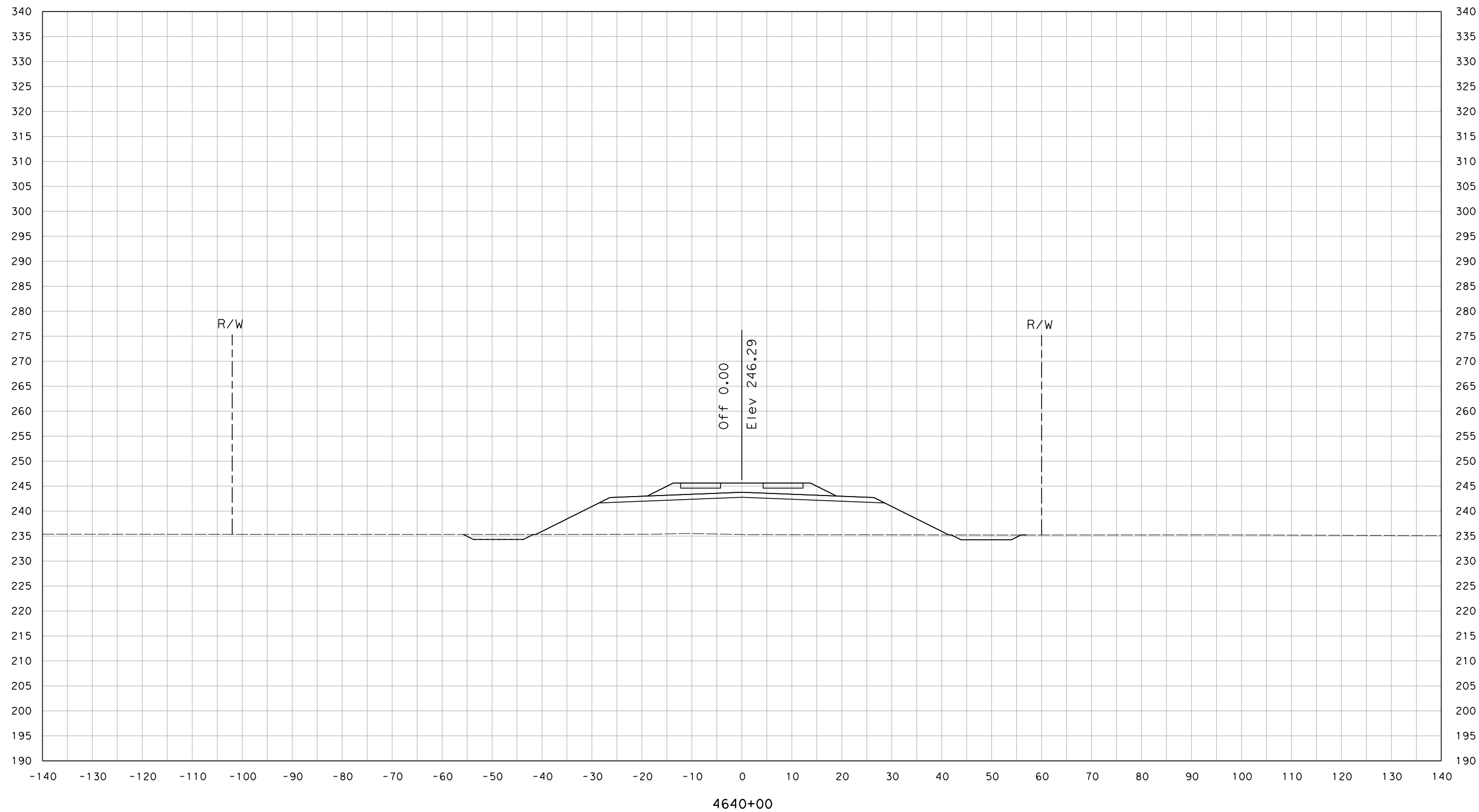
STA. 4625+00 TO STA. 4625+00



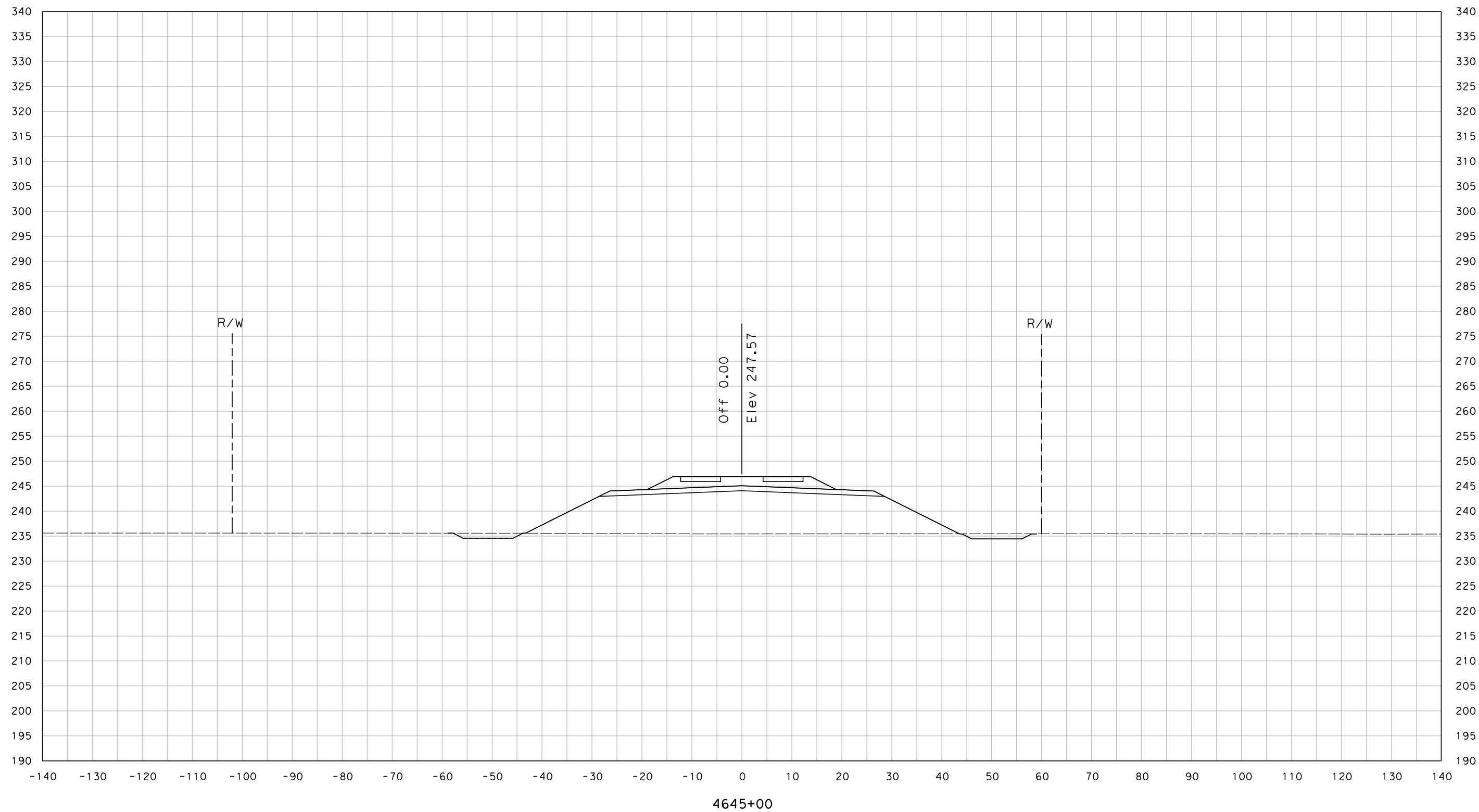
STA. 4630+00 TO STA. 4630+00



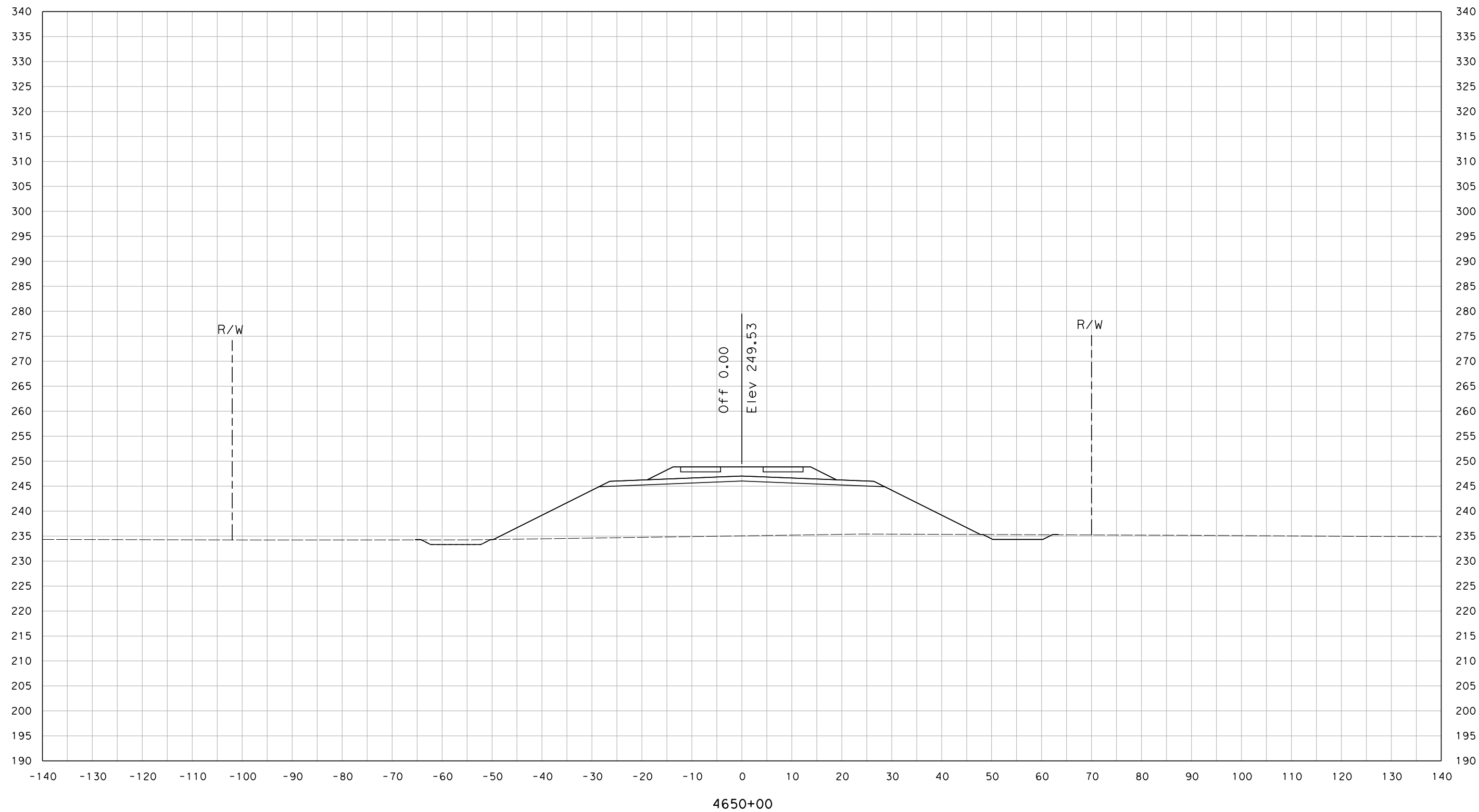
STA. 4635+00 TO STA. 4635+00



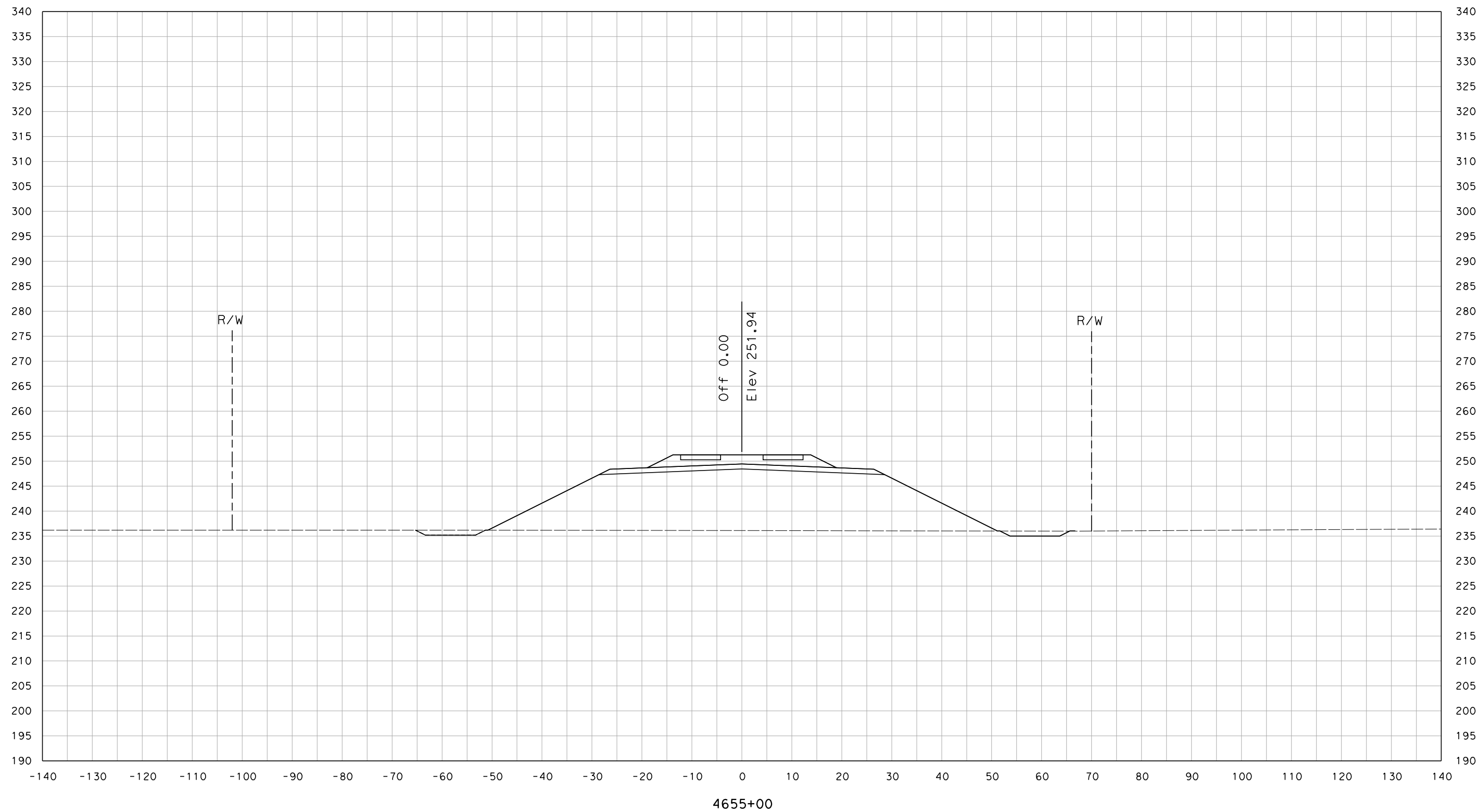
STA. 4640+00 TO STA. 4640+00



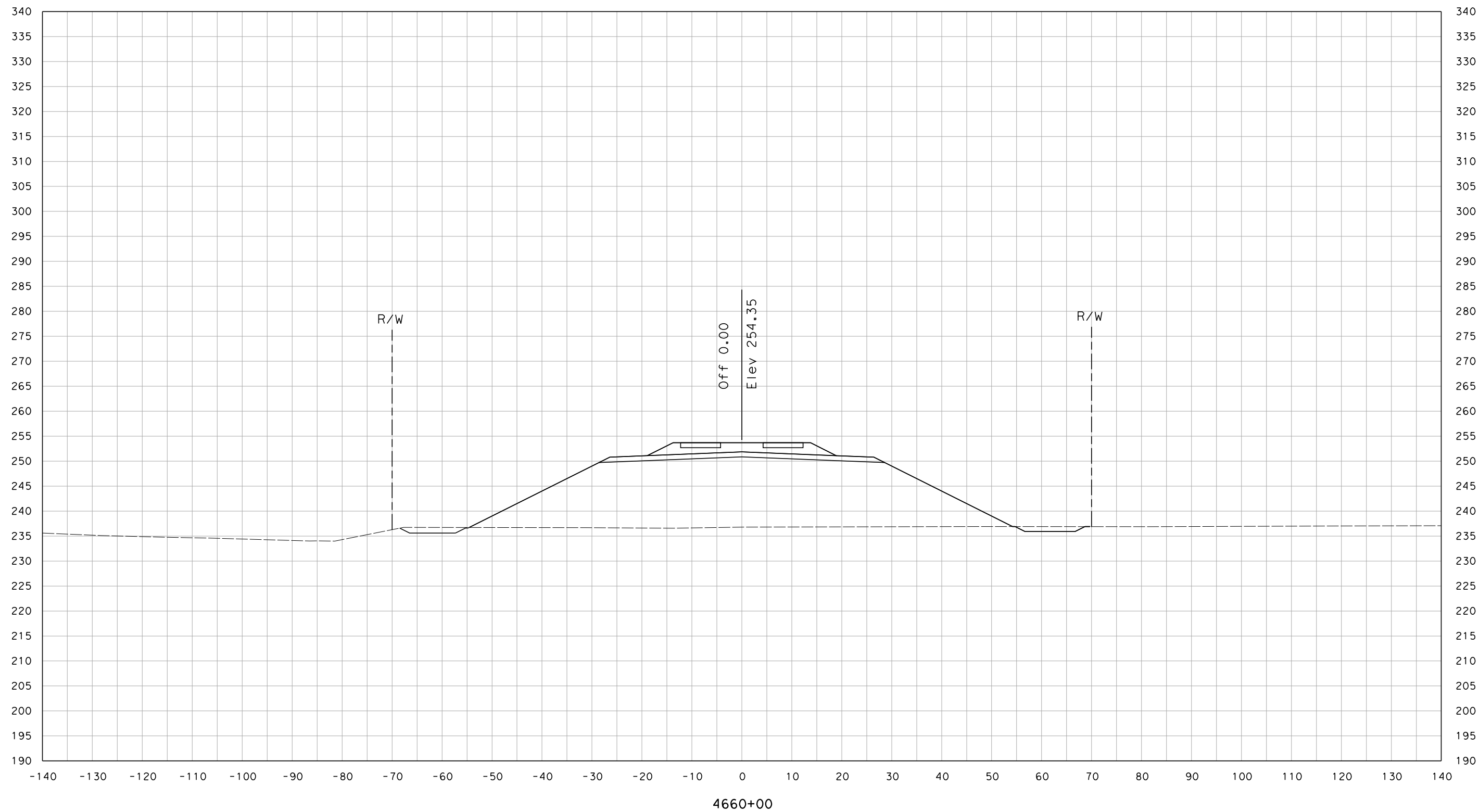
STA. 4645+00 TO STA. 4645+00



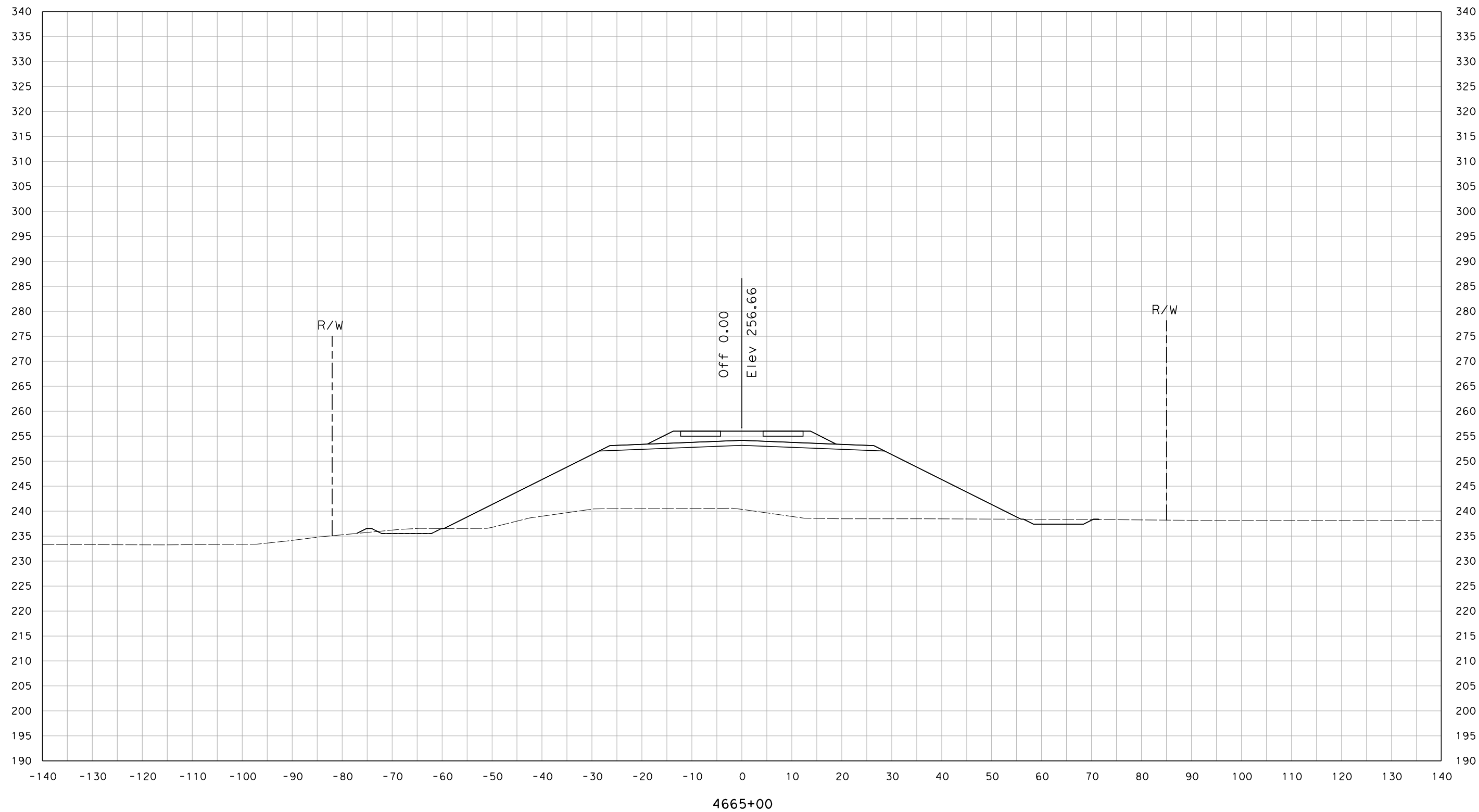
STA. 4650+00 TO STA. 4650+00



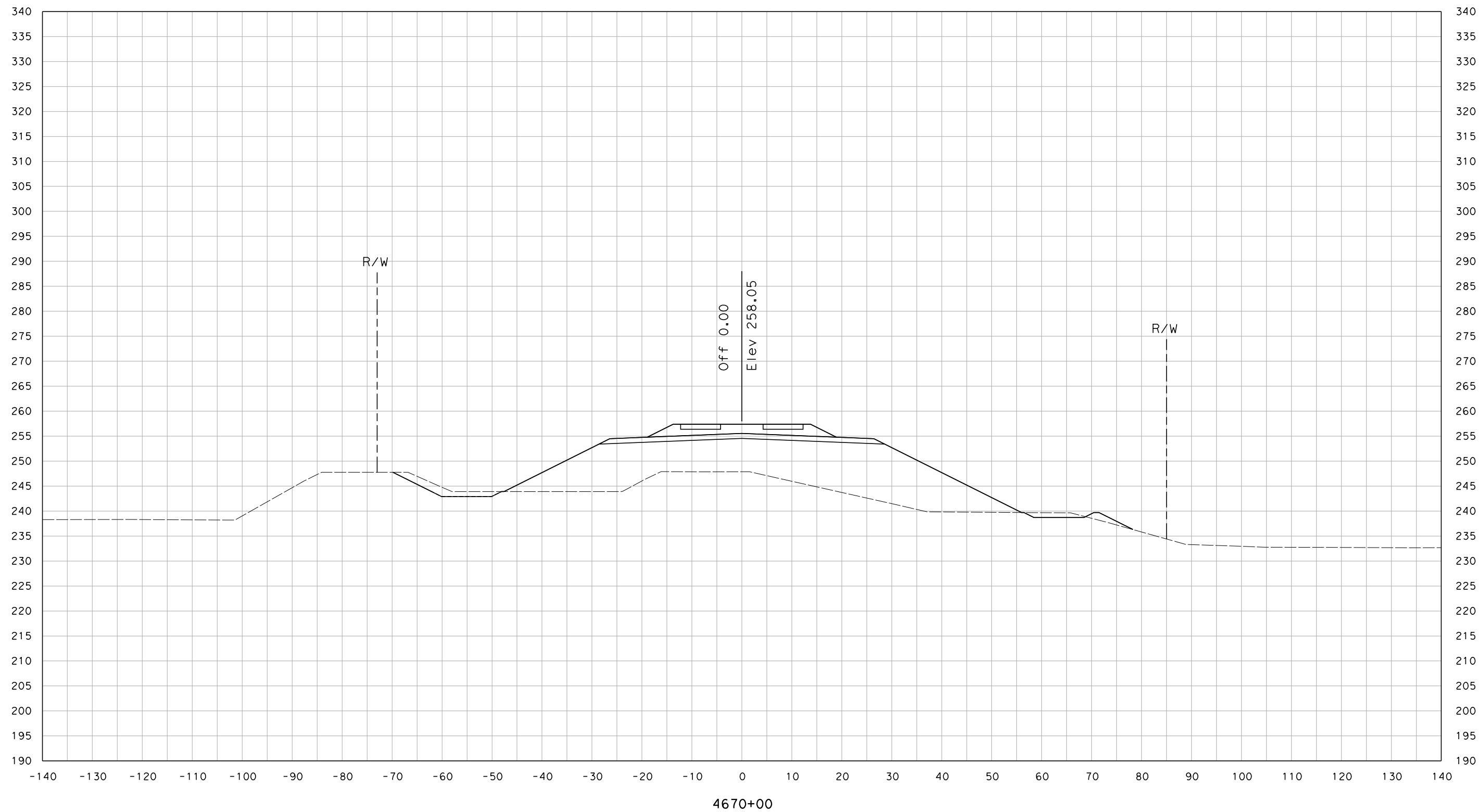
STA. 4655+00 TO STA. 4655+00



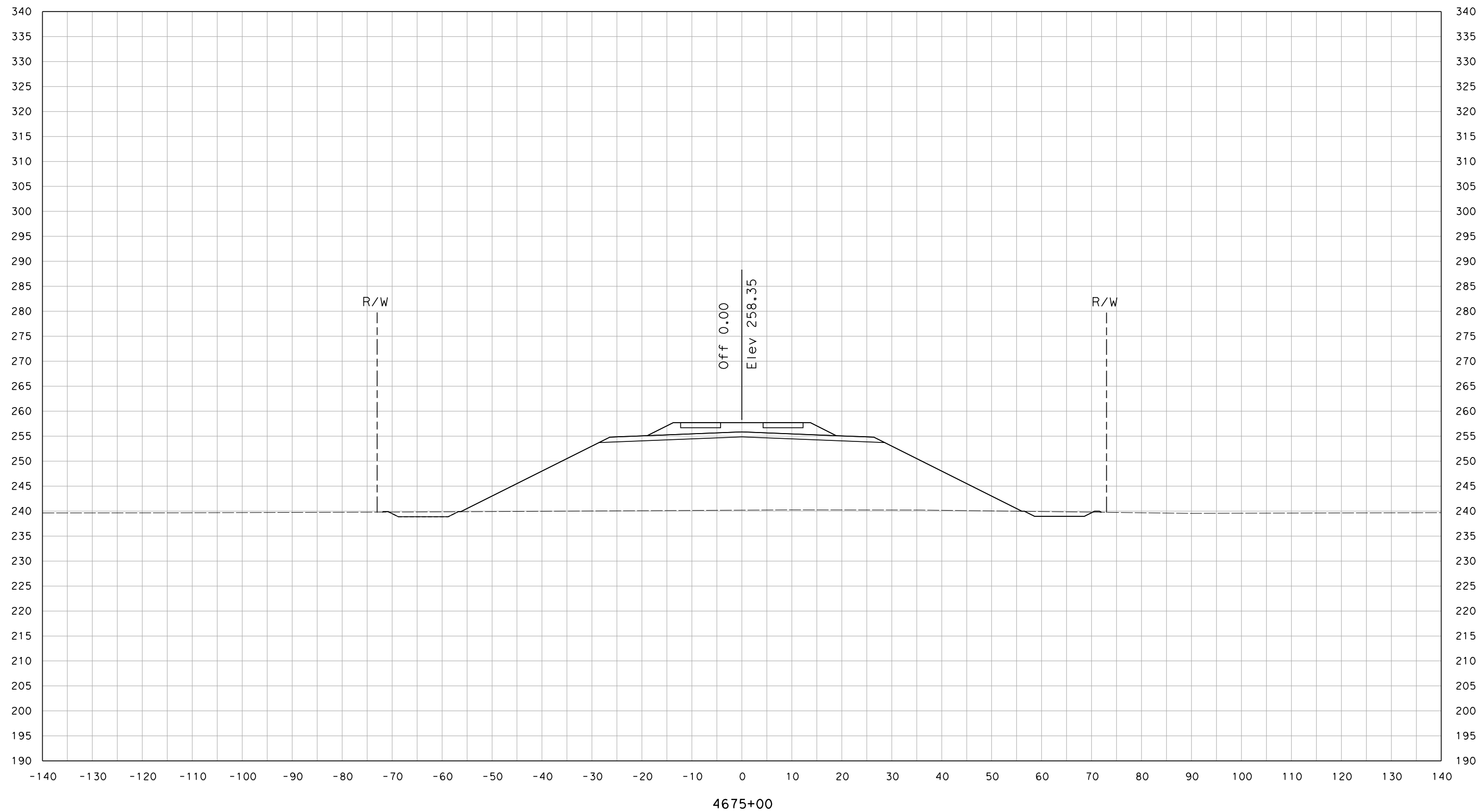
STA. 4660+00 TO STA. 4660+00



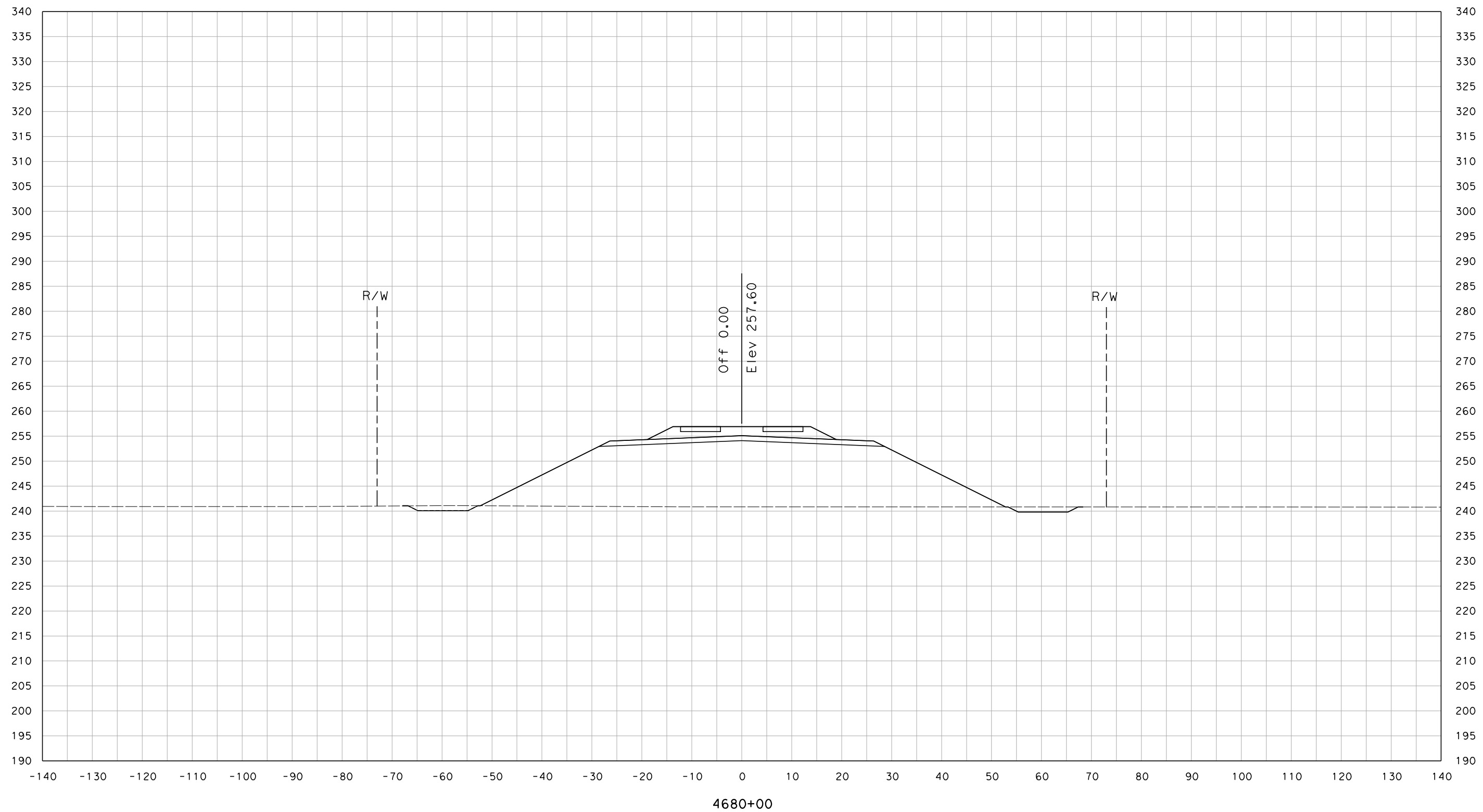
STA. 4665+00 TO STA. 4665+00



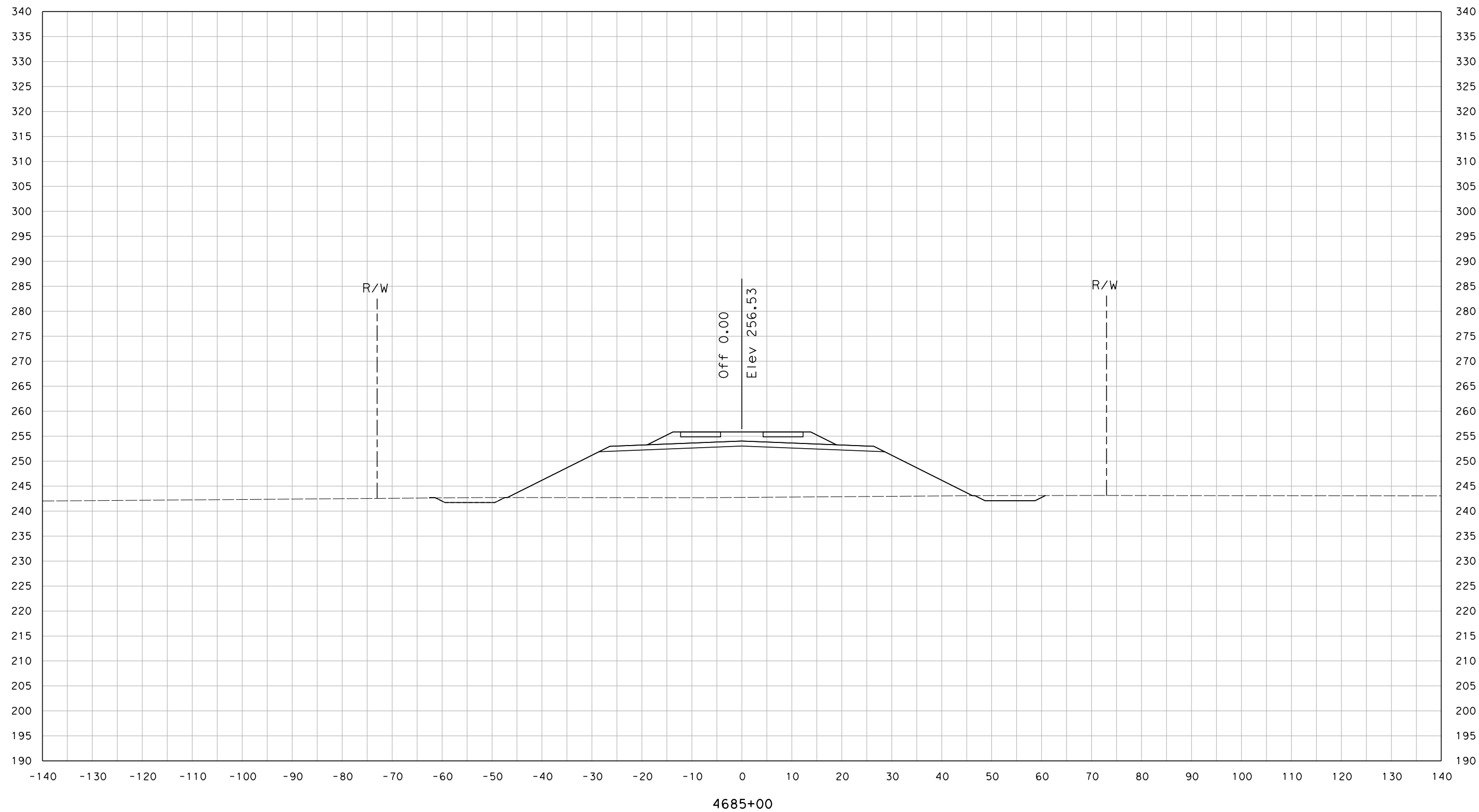
STA. 4670+00 TO STA. 4670+00



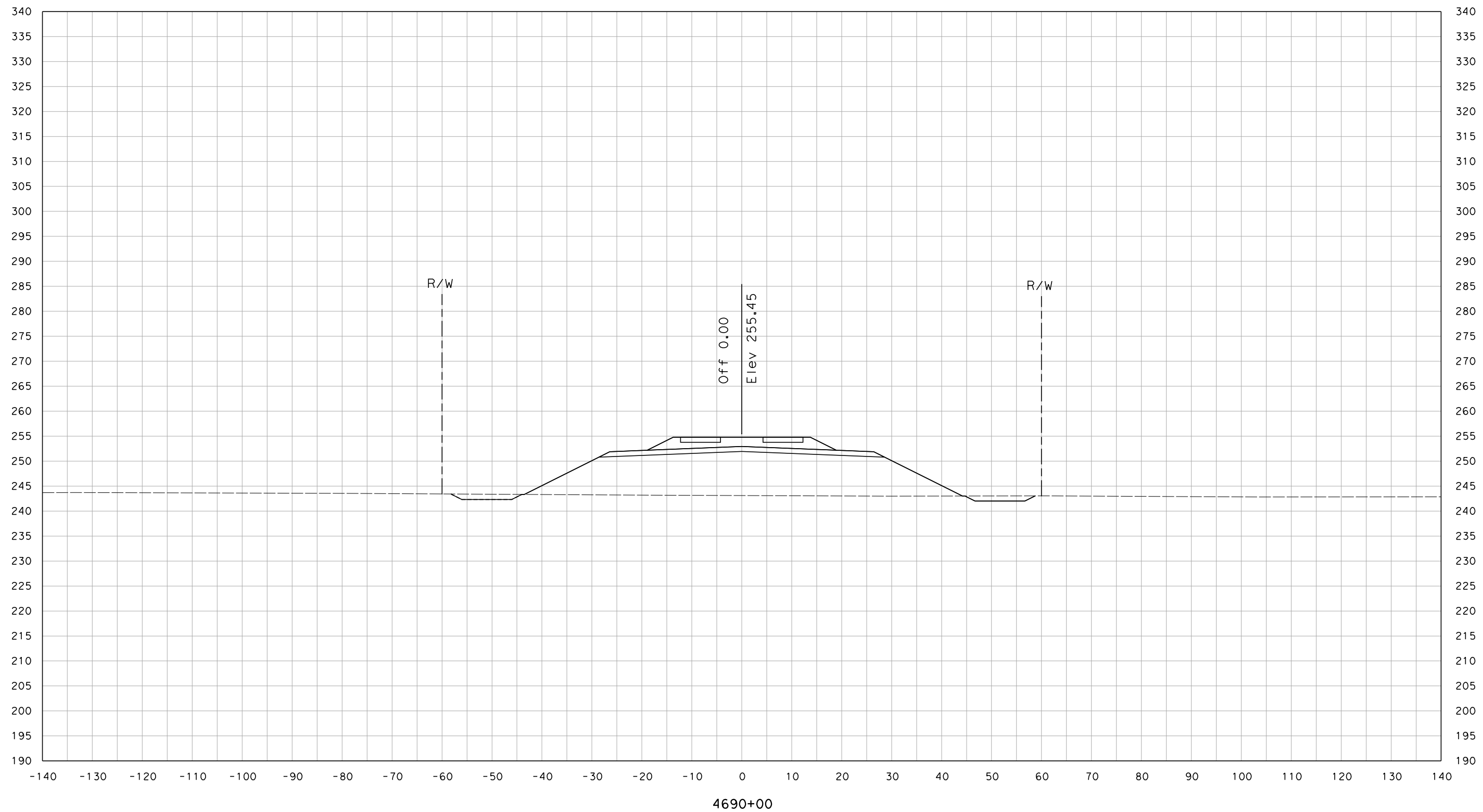
STA. 4675+00 TO STA. 4675+00



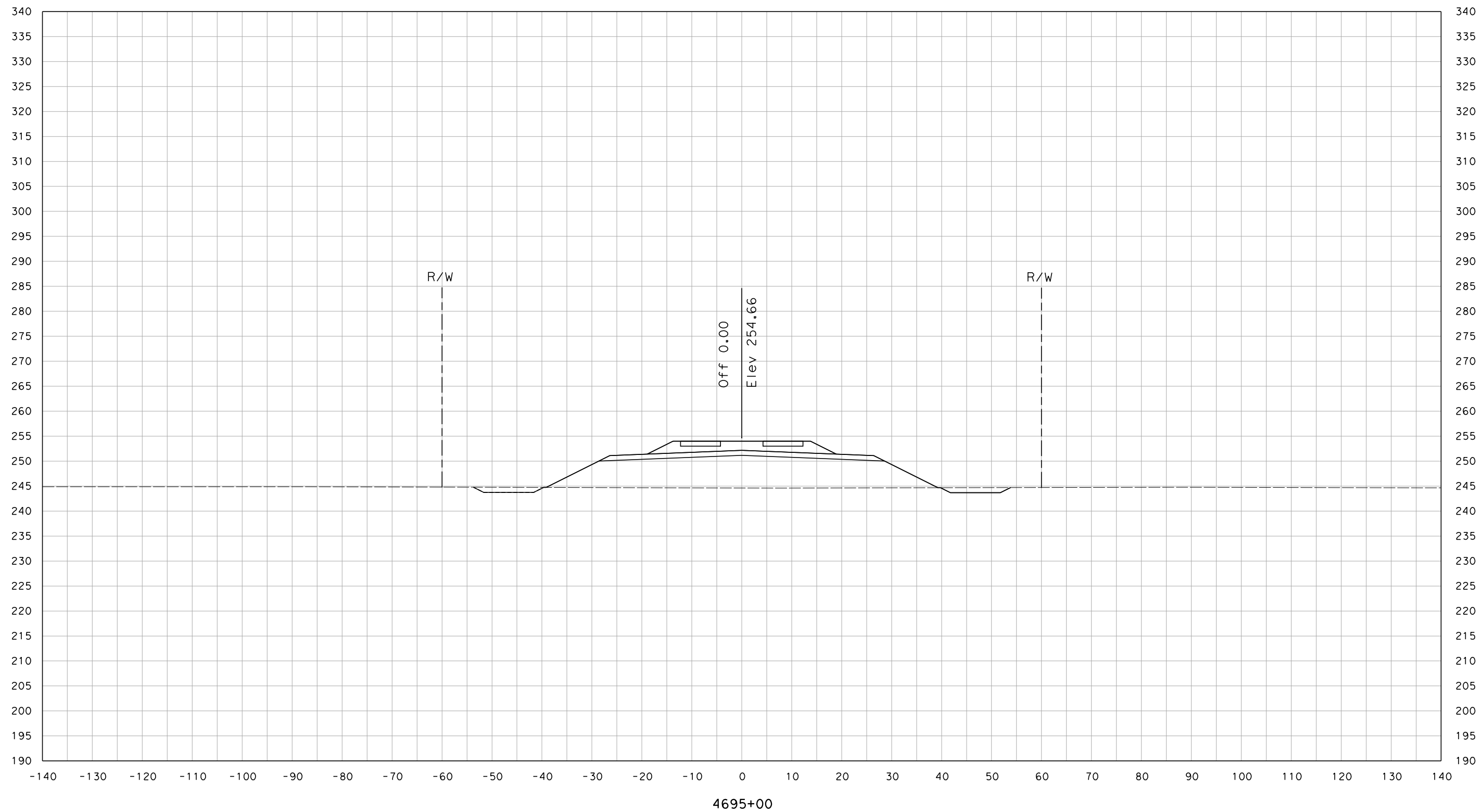
STA. 4680+00 TO STA. 4680+00

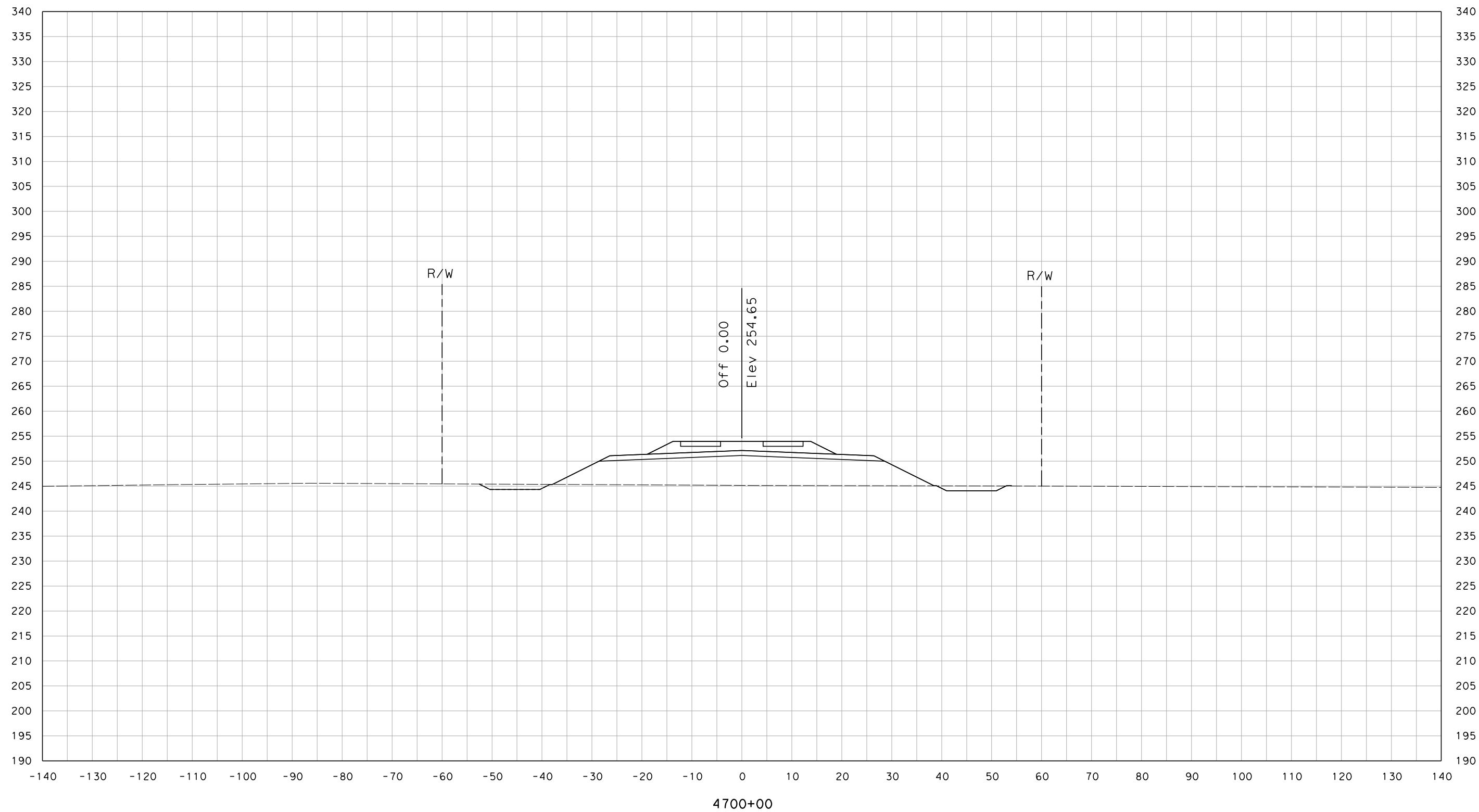


STA. 4685+00 TO STA. 4685+00

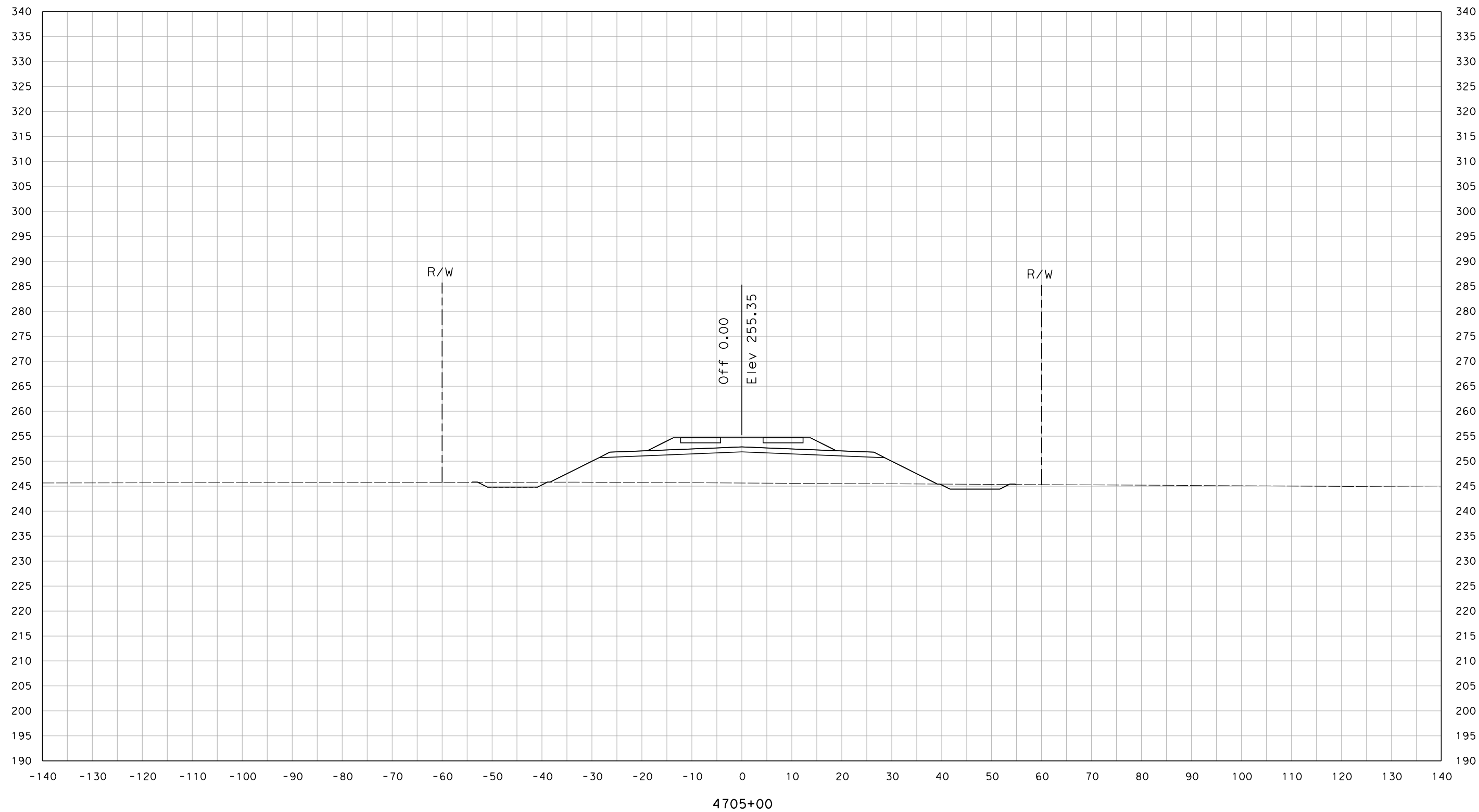


STA. 4690+00 TO STA. 4690+00

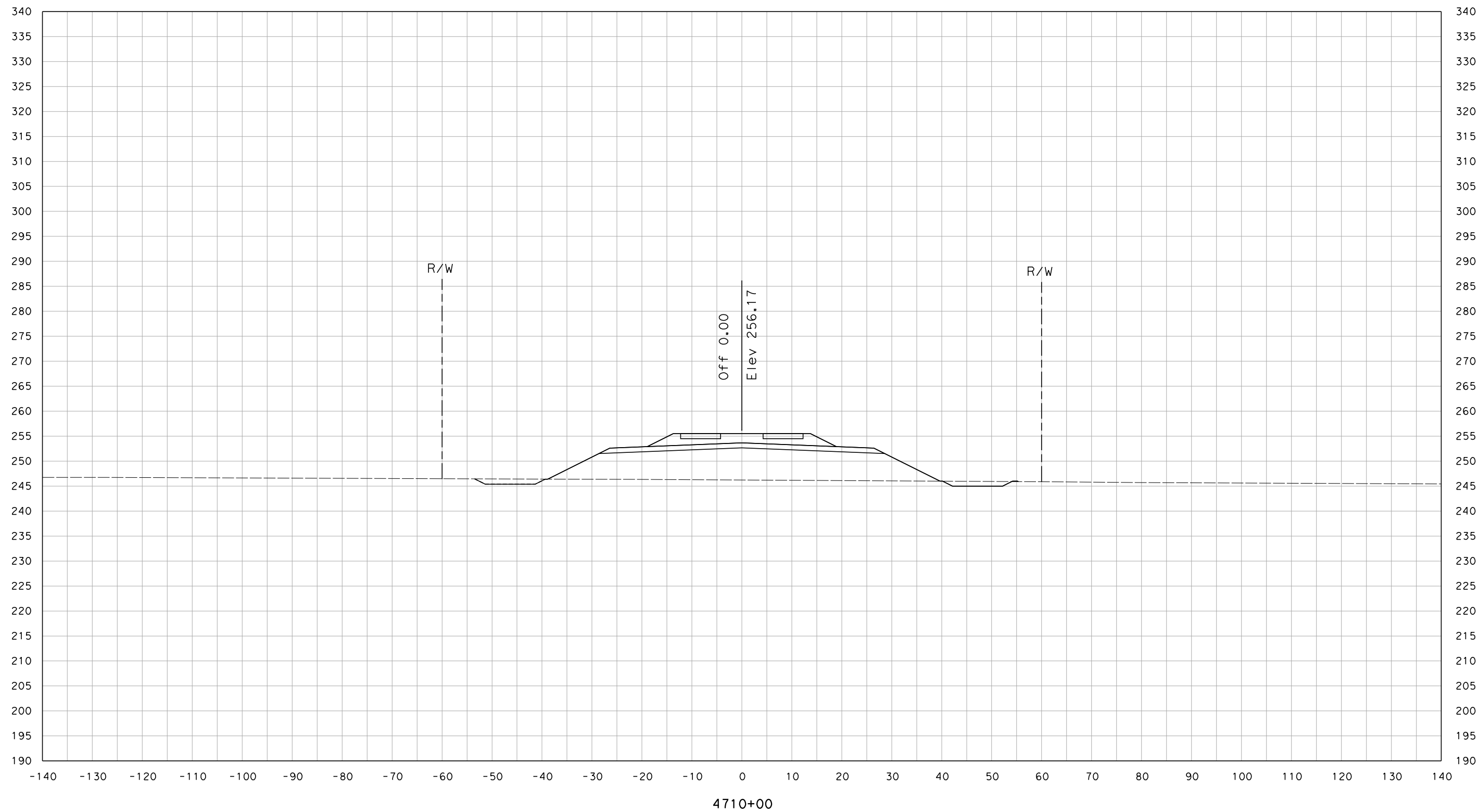




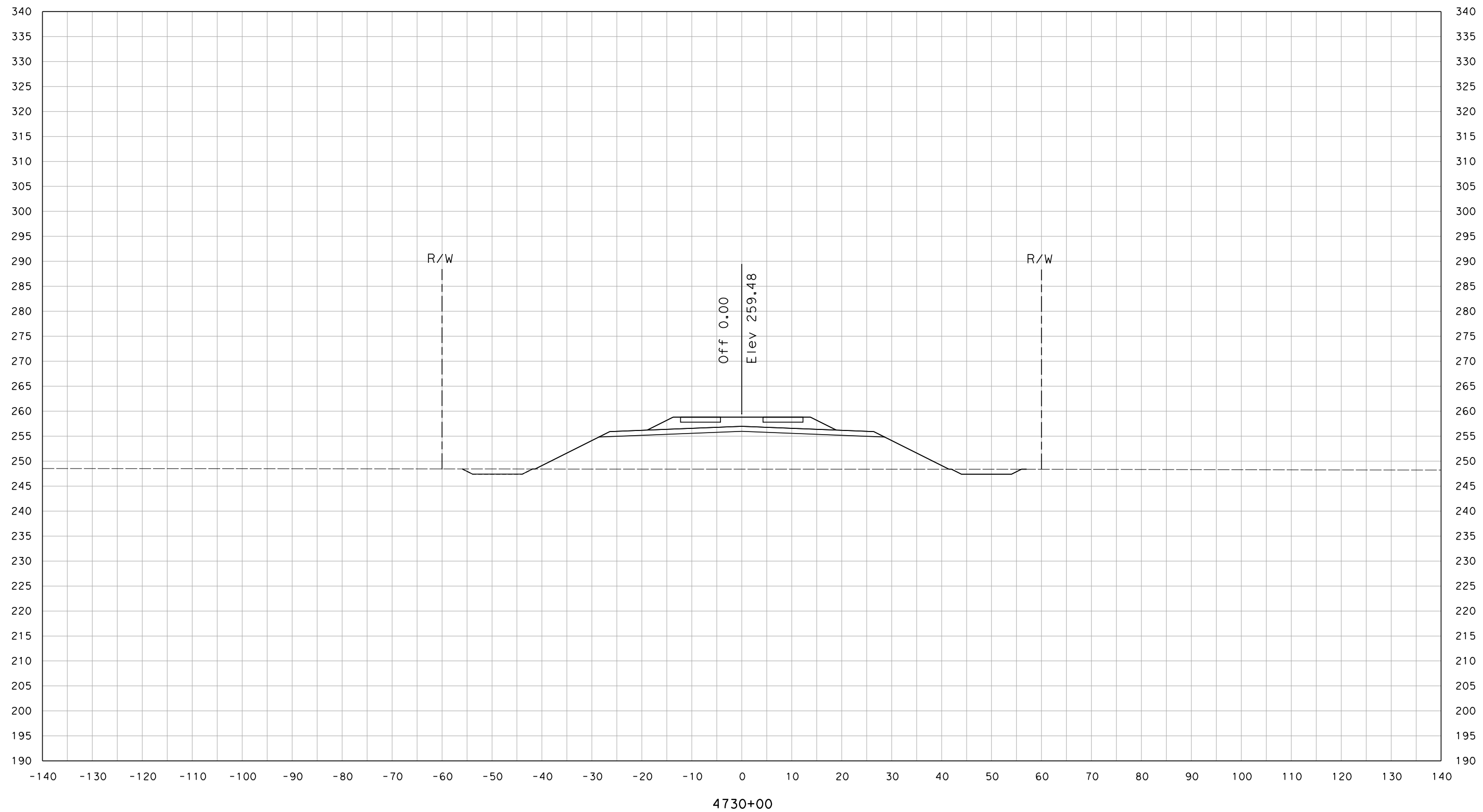
STA. 4700+00 TO STA. 4700+00



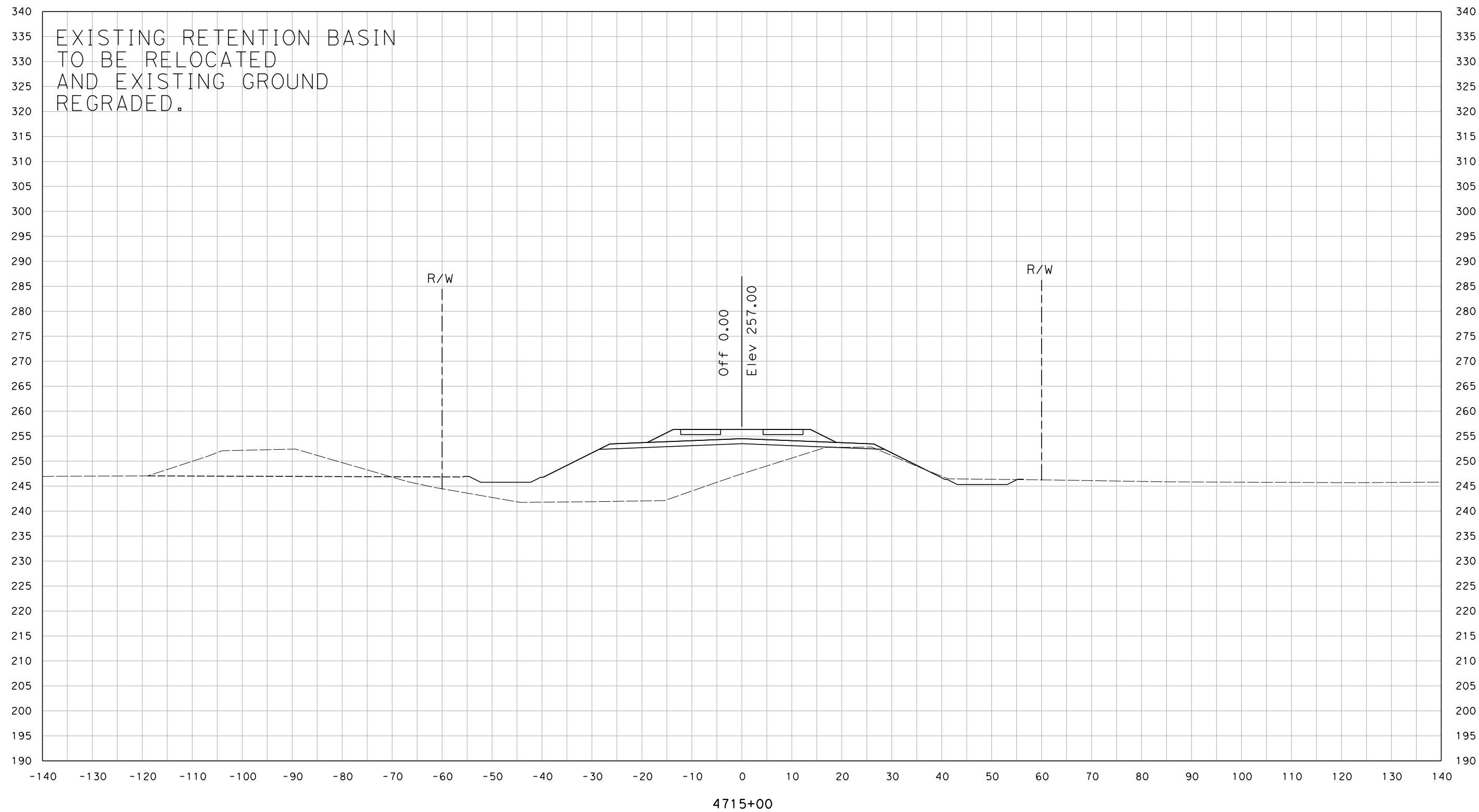
STA. 4705+00 TO STA. 4705+00



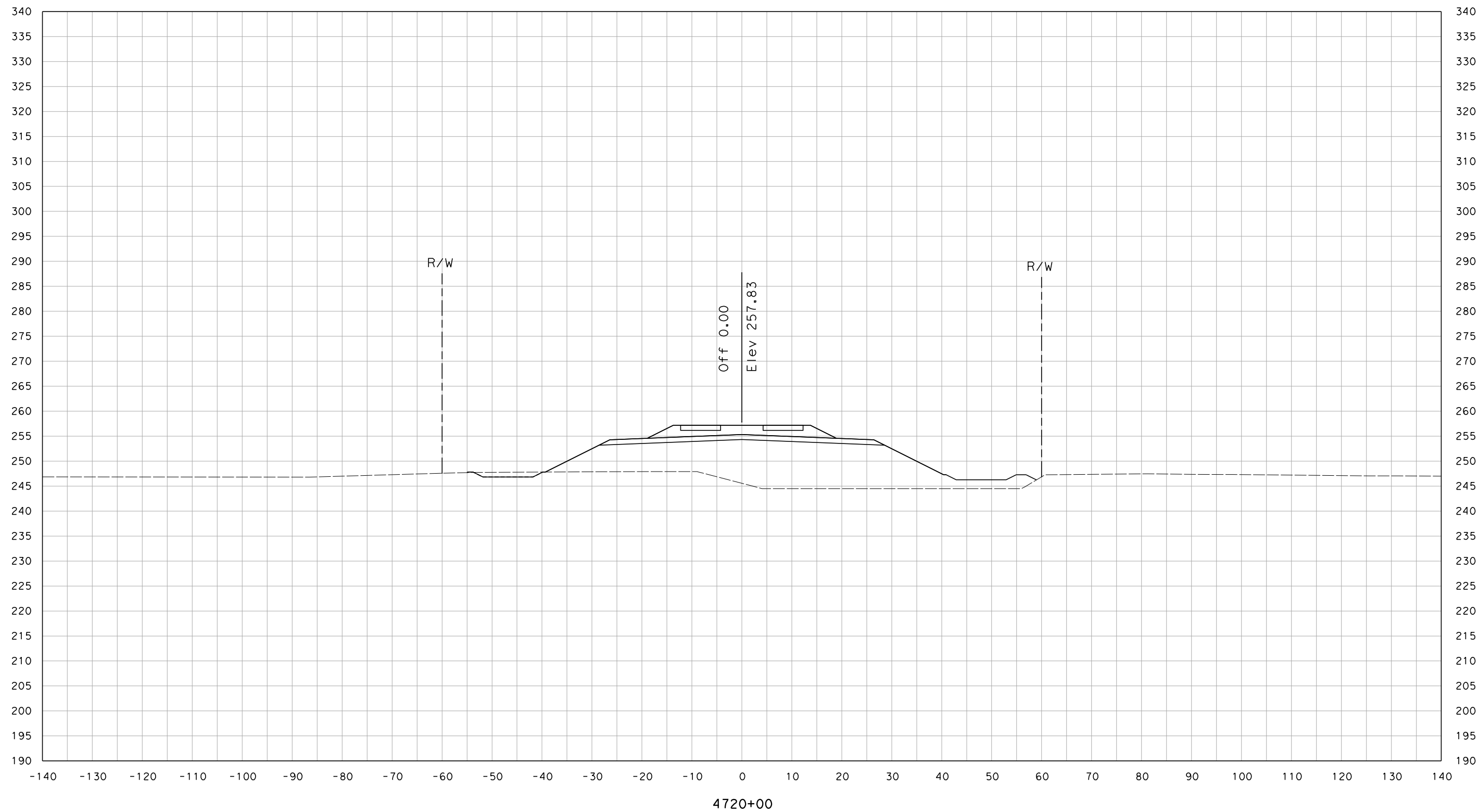
STA. 4710+00 TO STA. 4710+00



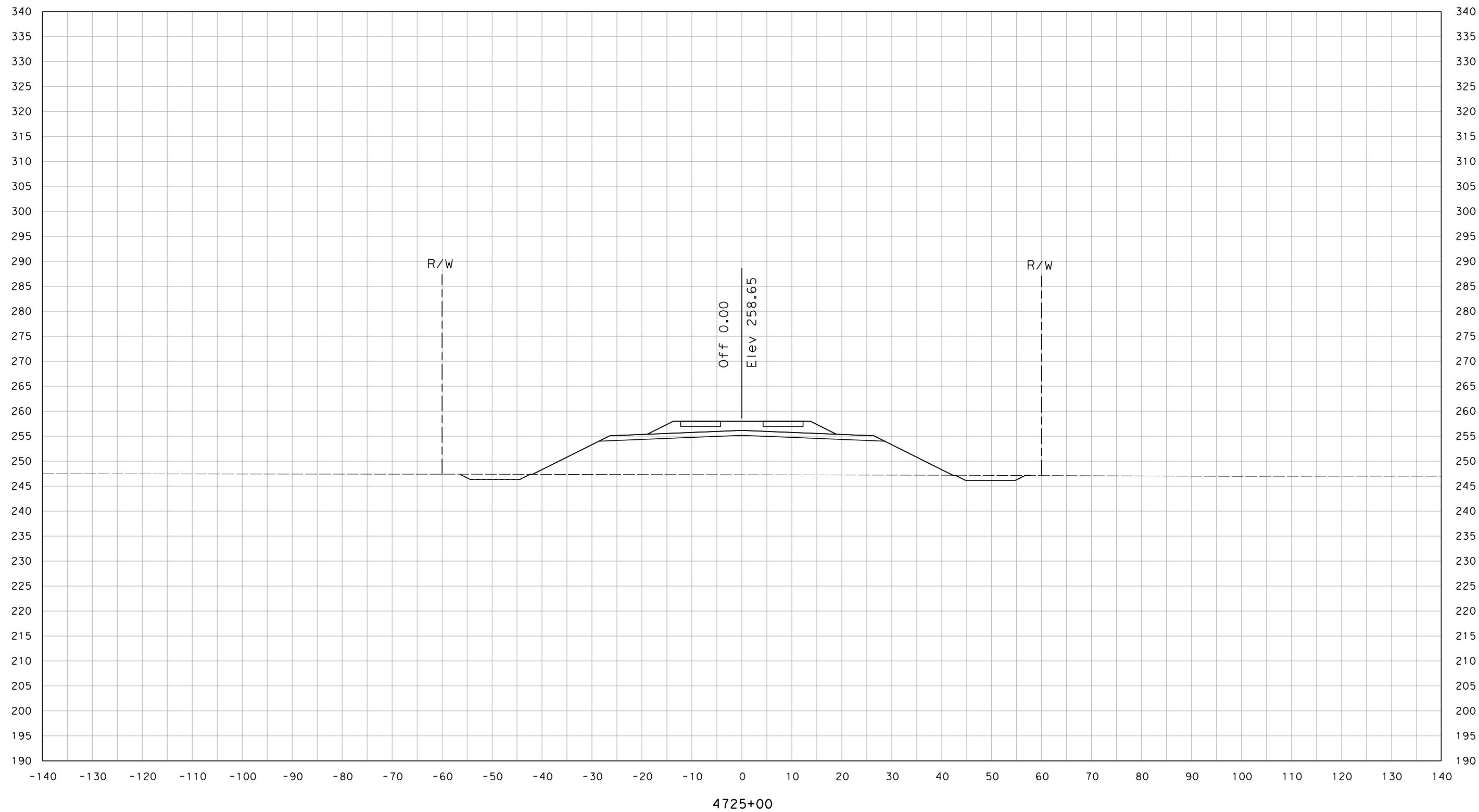
STA. 4730+00 TO STA. 4730+00



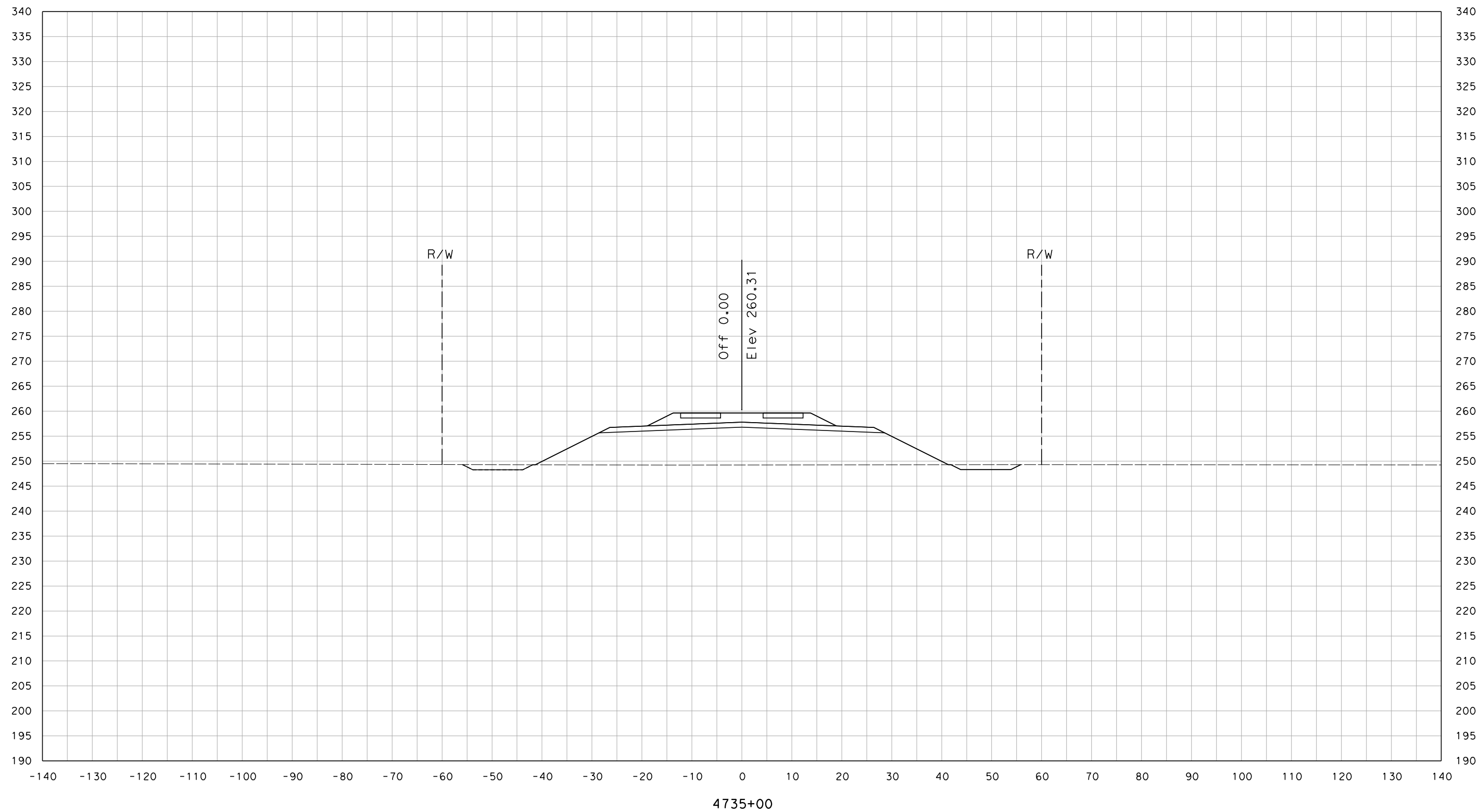
STA. 4715+00 TO STA. 4715+00



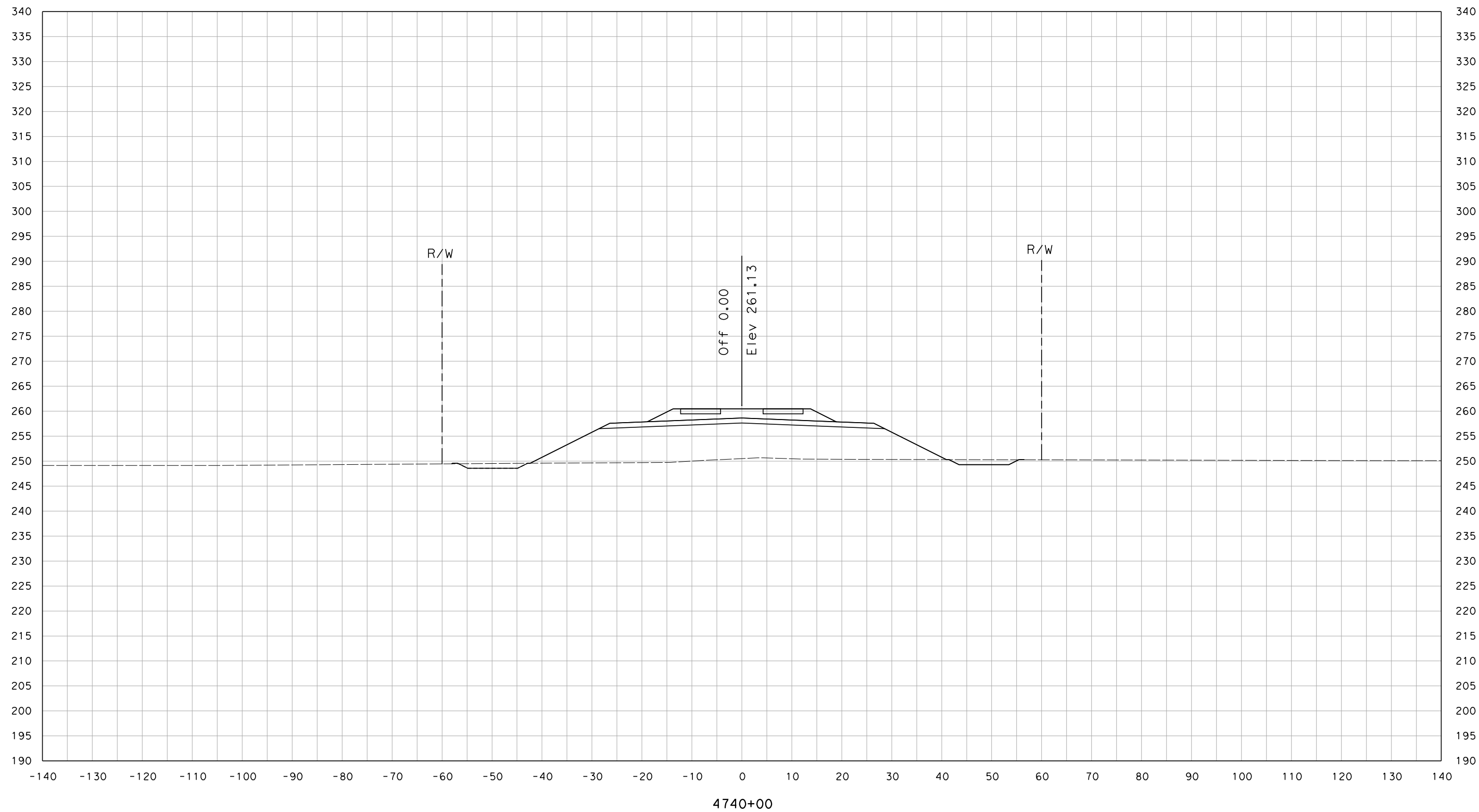
STA. 4720+00 TO STA. 4720+00



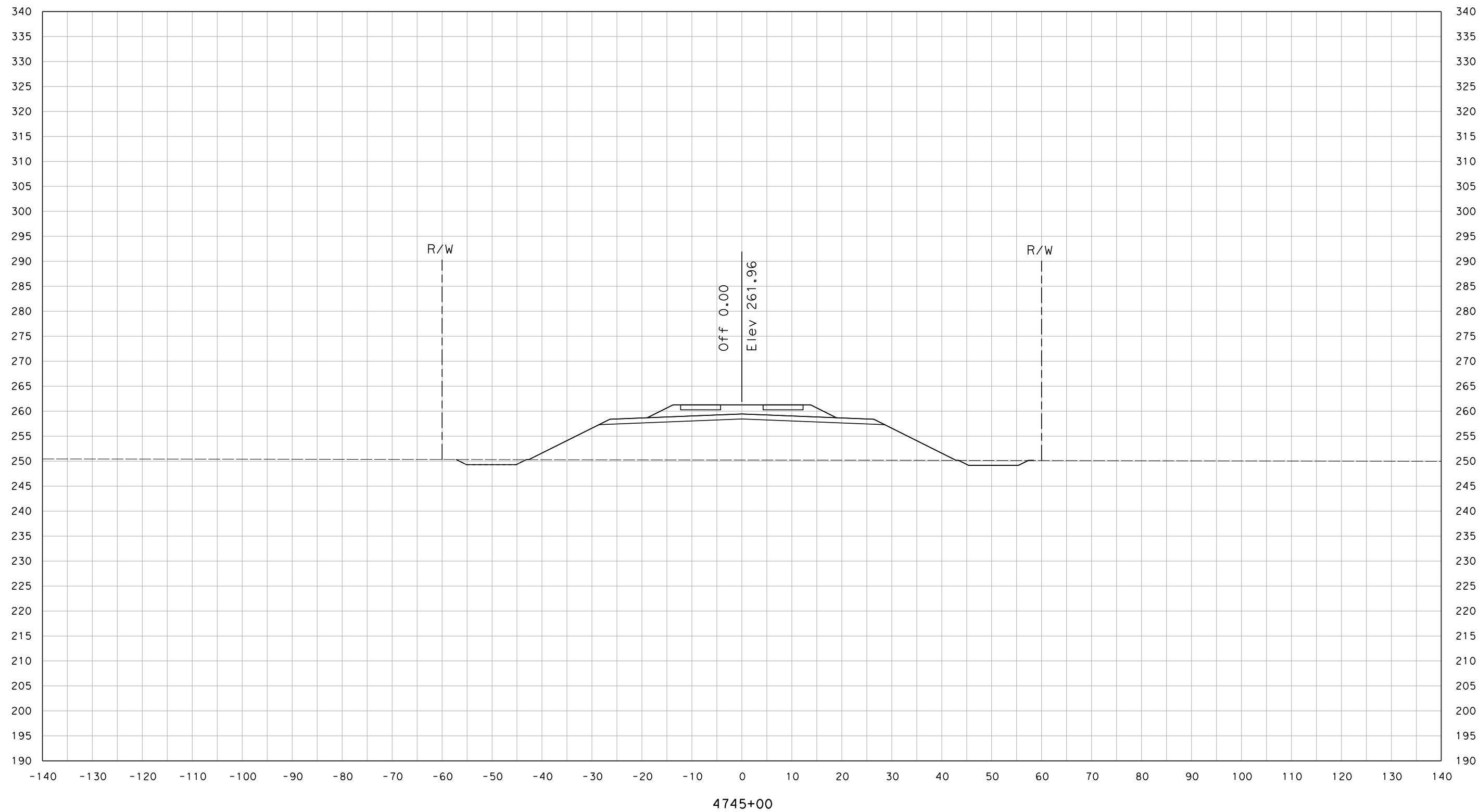
STA. 4725+00 TO STA. 4725+00



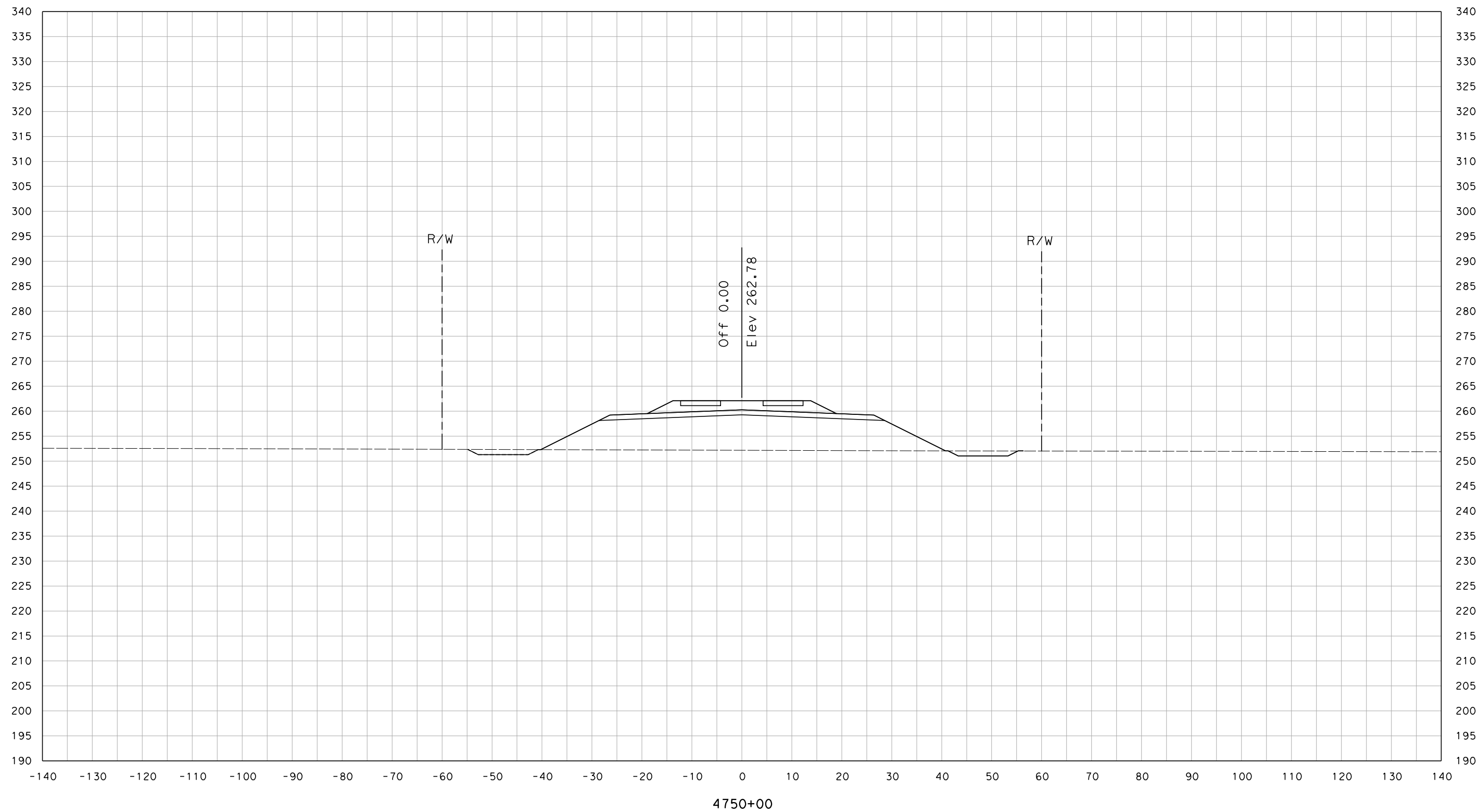
STA. 4735+00 TO STA. 4735+00



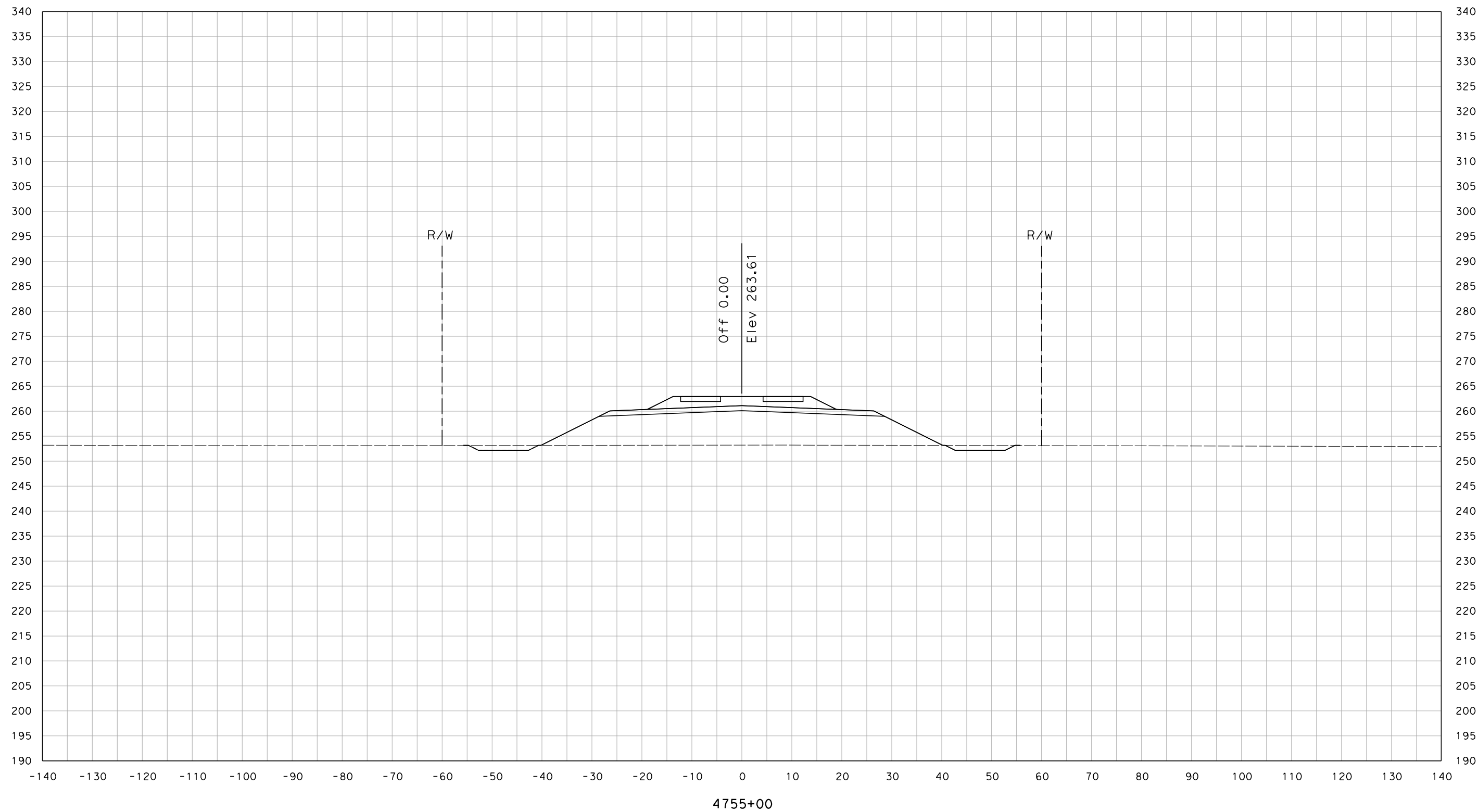
STA. 4740+00 TO STA. 4740+00



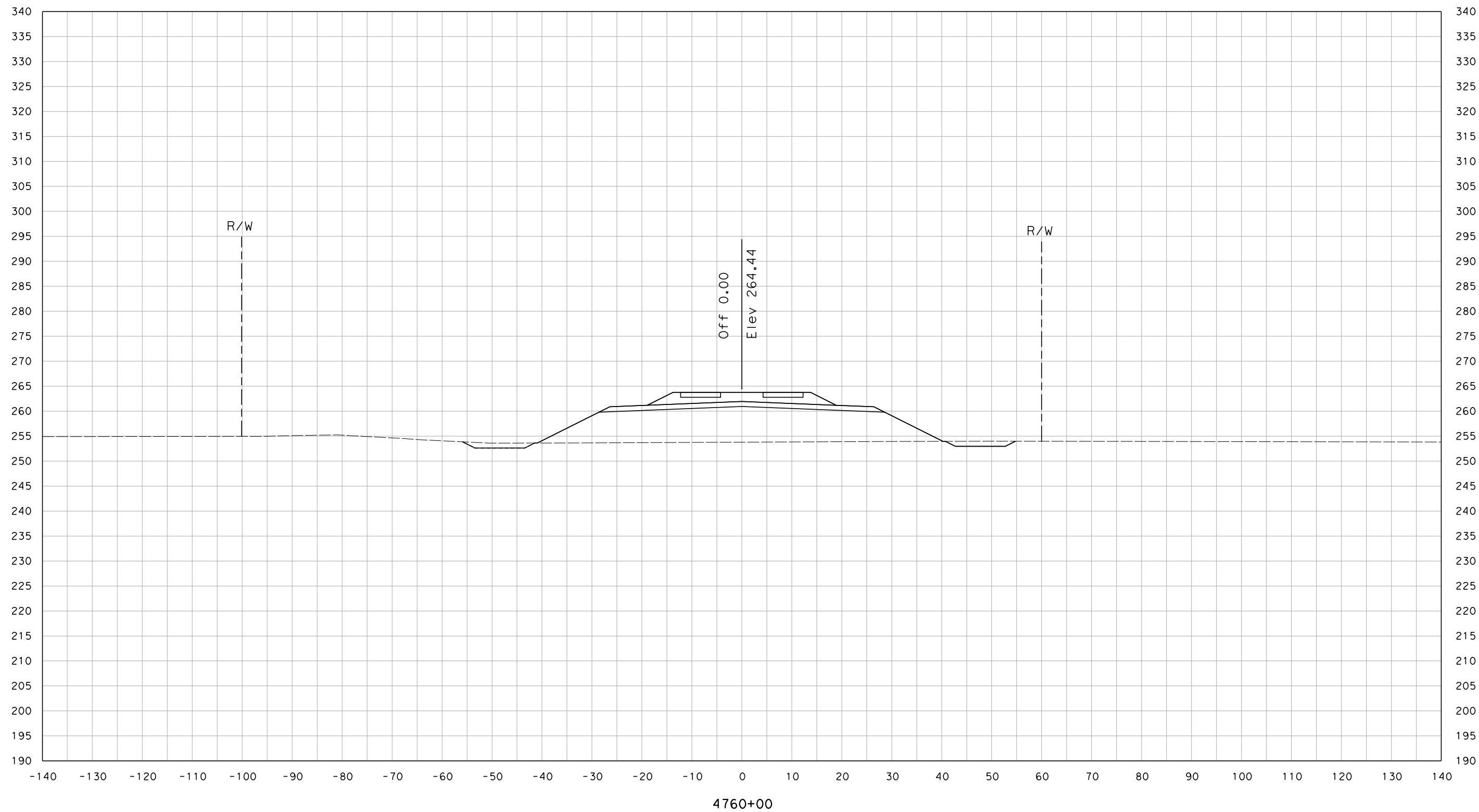
STA. 4745+00 TO STA. 4745+00



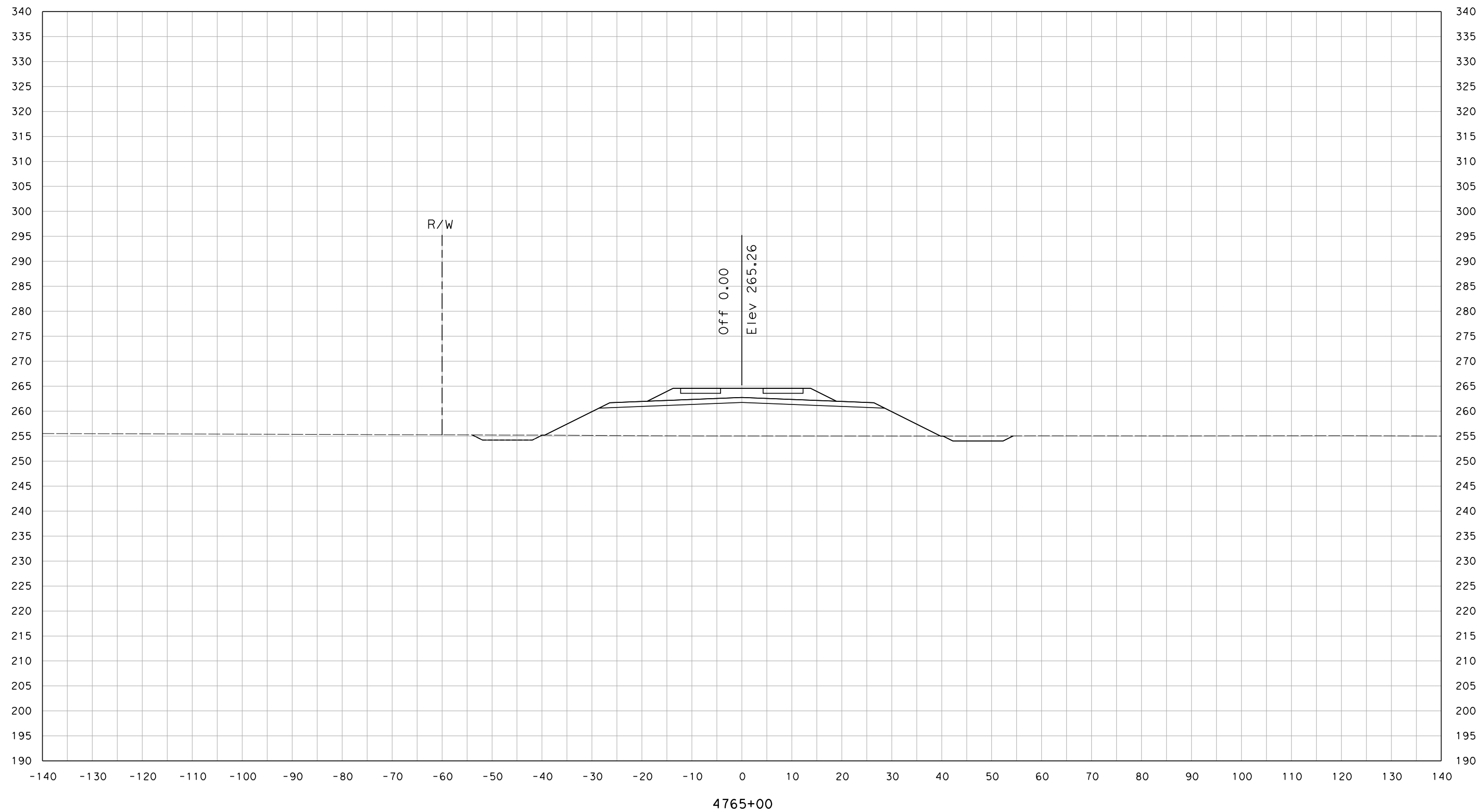
STA. 4750+00 TO STA. 4750+00



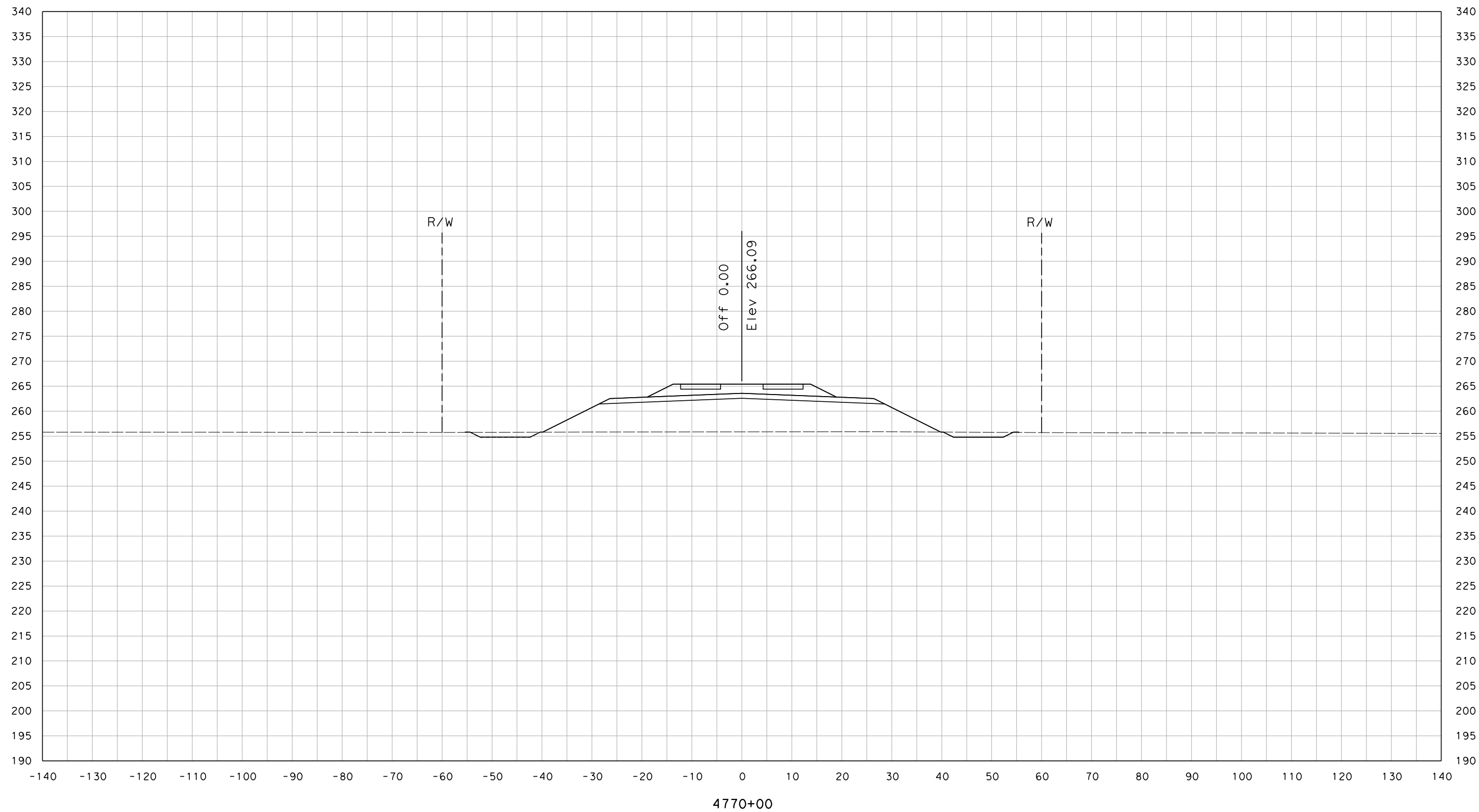
STA. 4755+00 TO STA. 4755+00



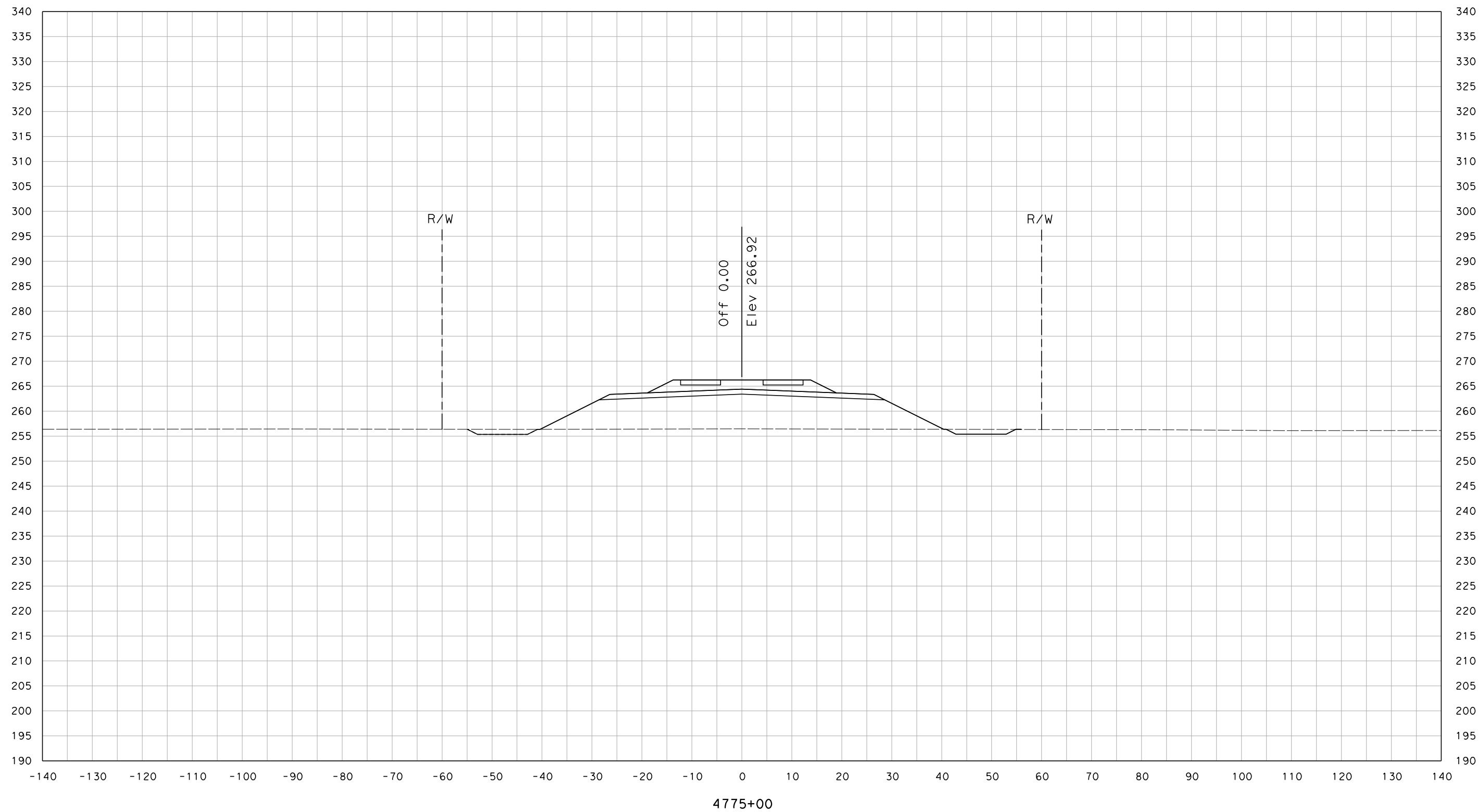
STA. 4760+00 TO STA. 4760+00



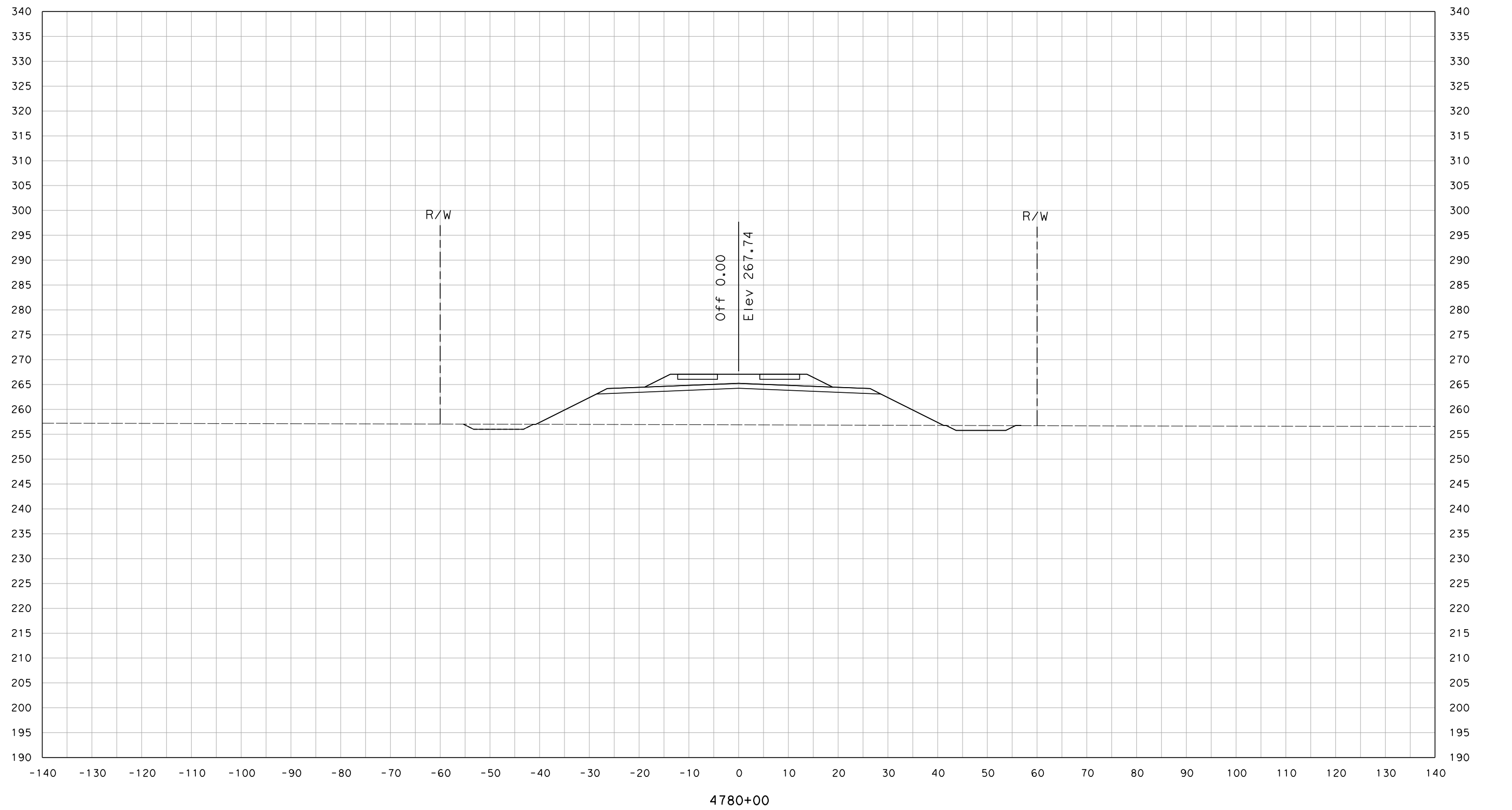
STA. 4765+00 TO STA. 4765+00

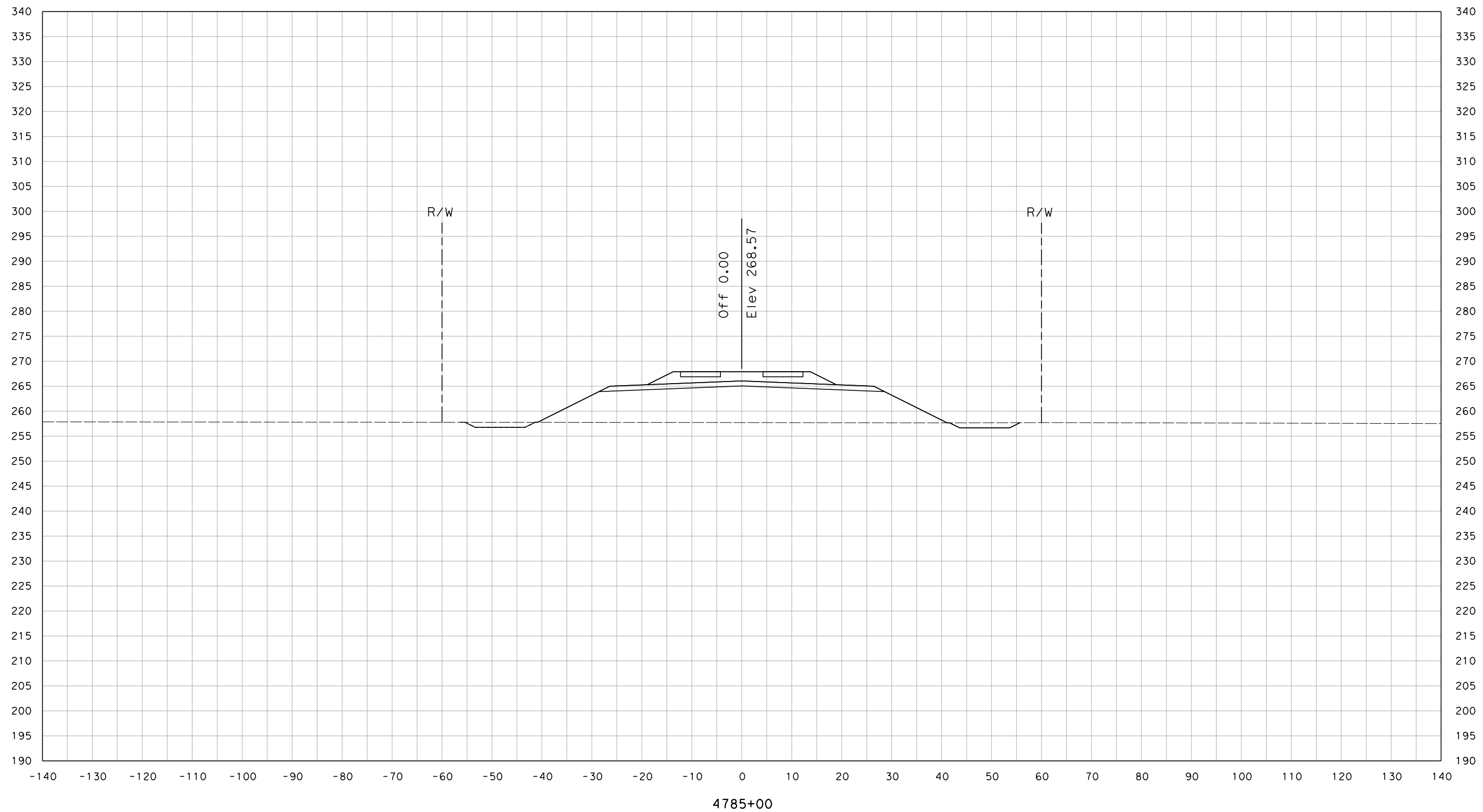


STA. 4770+00 TO STA. 4770+00

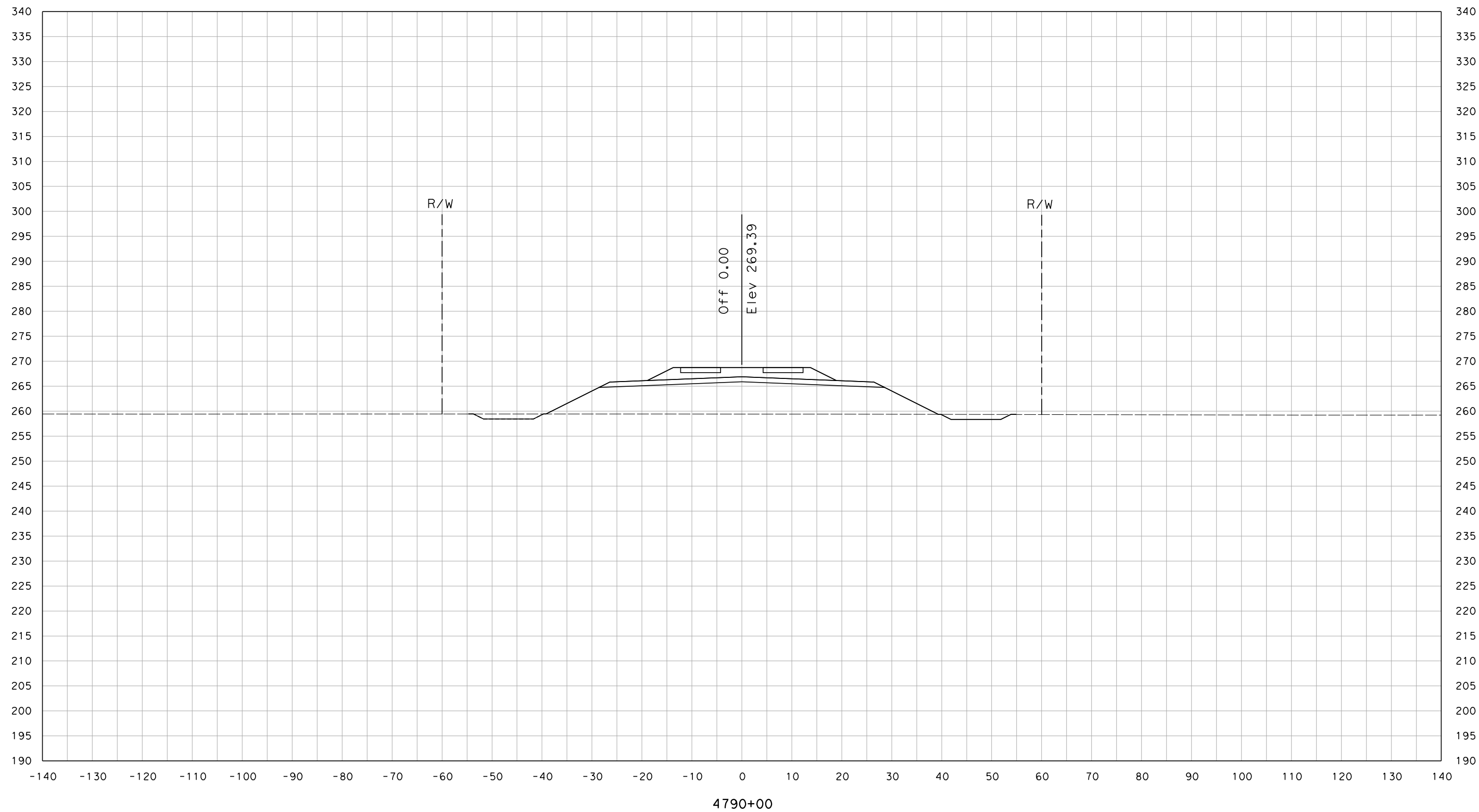


STA. 4775+00 TO STA. 4775+00

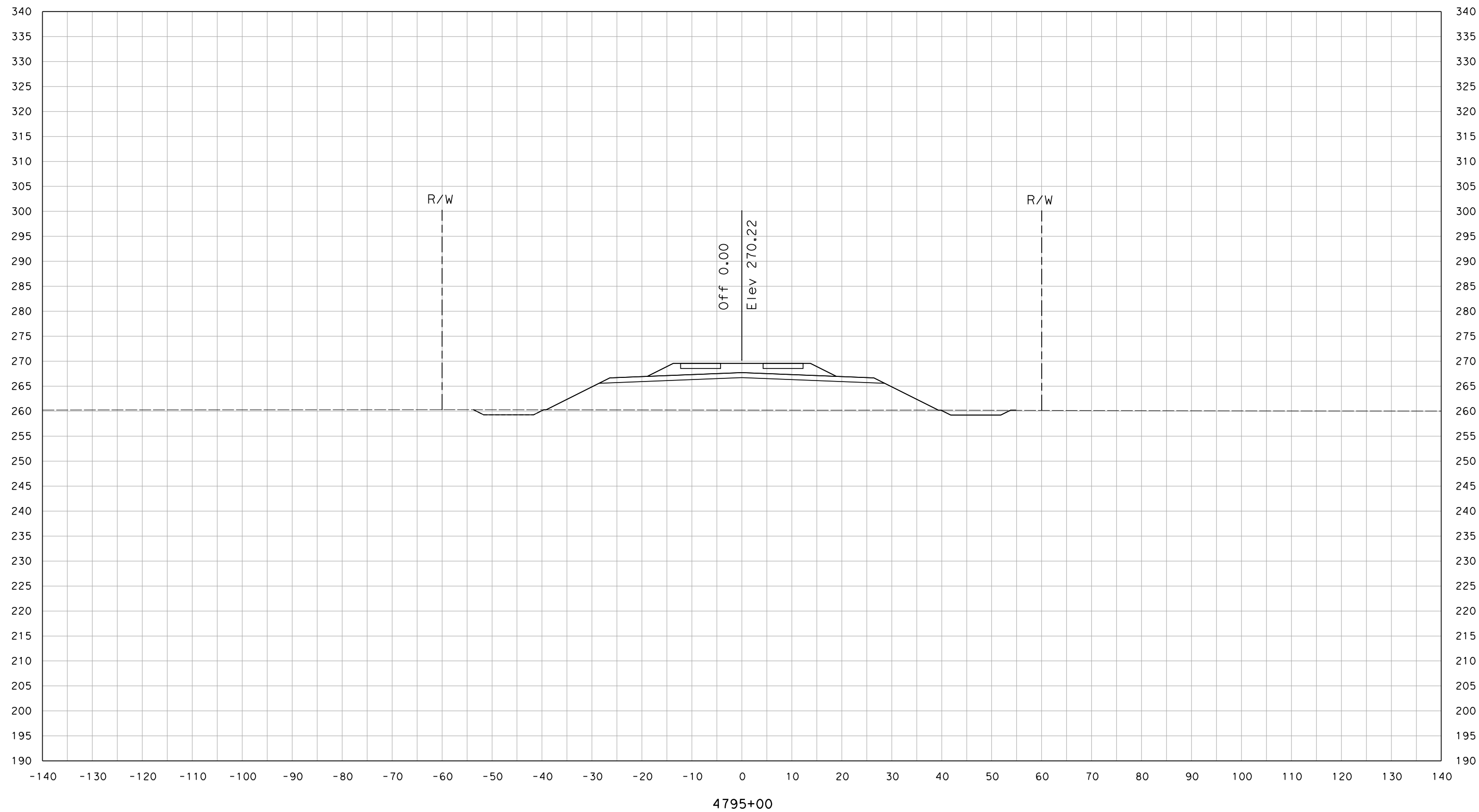




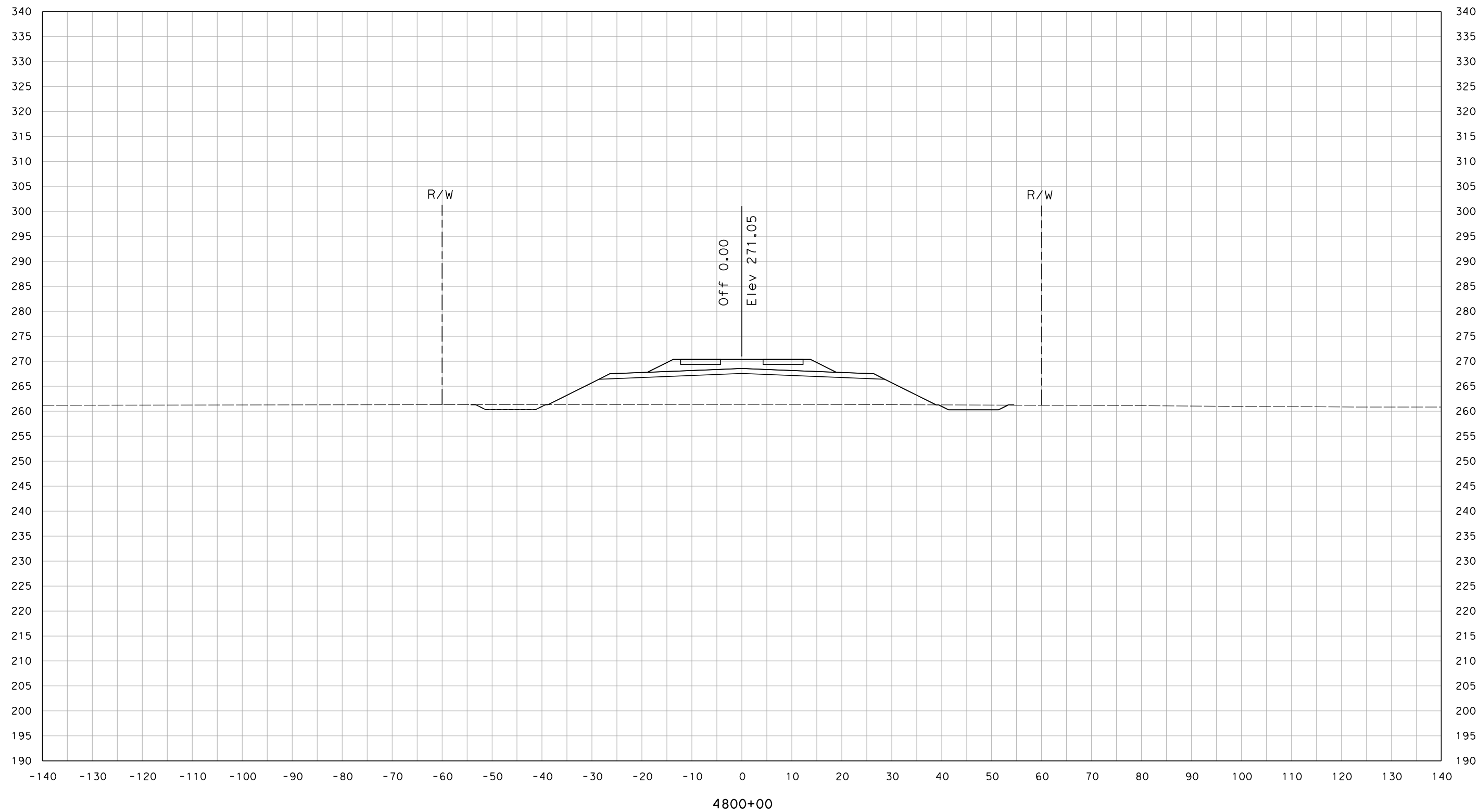
STA. 4785+00 TO STA. 4785+00



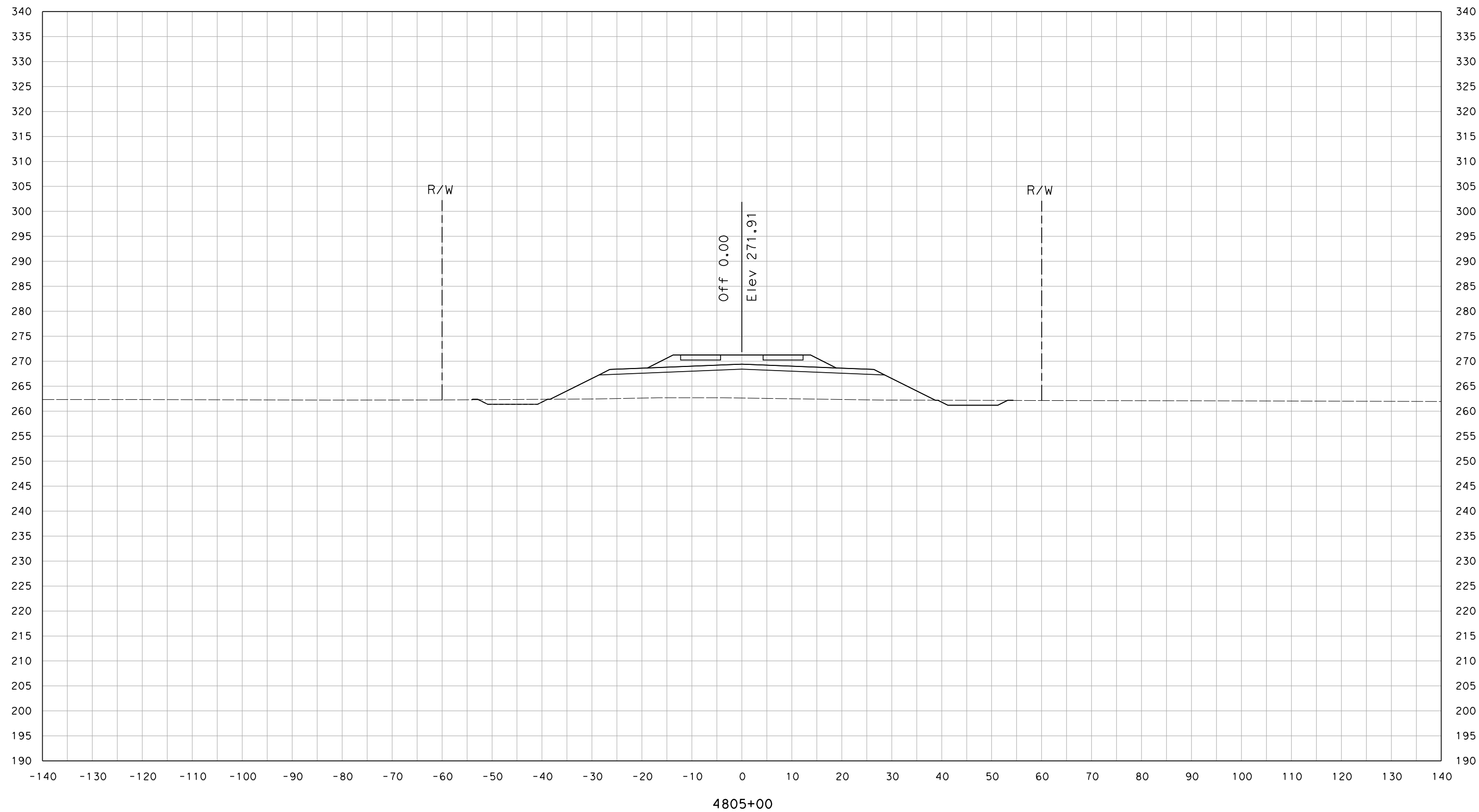
STA. 4790+00 TO STA. 4790+00



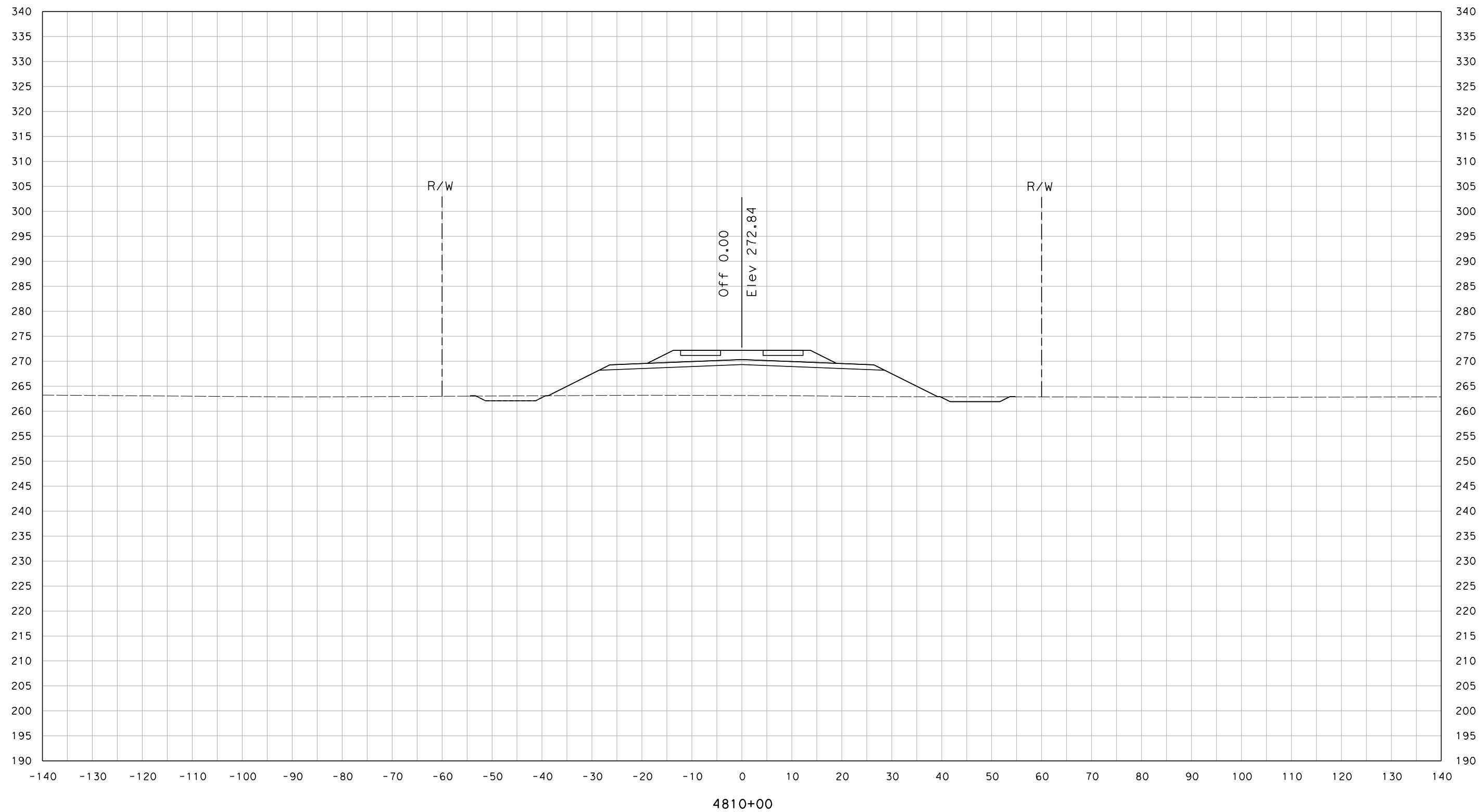
STA. 4795+00 TO STA. 4795+00



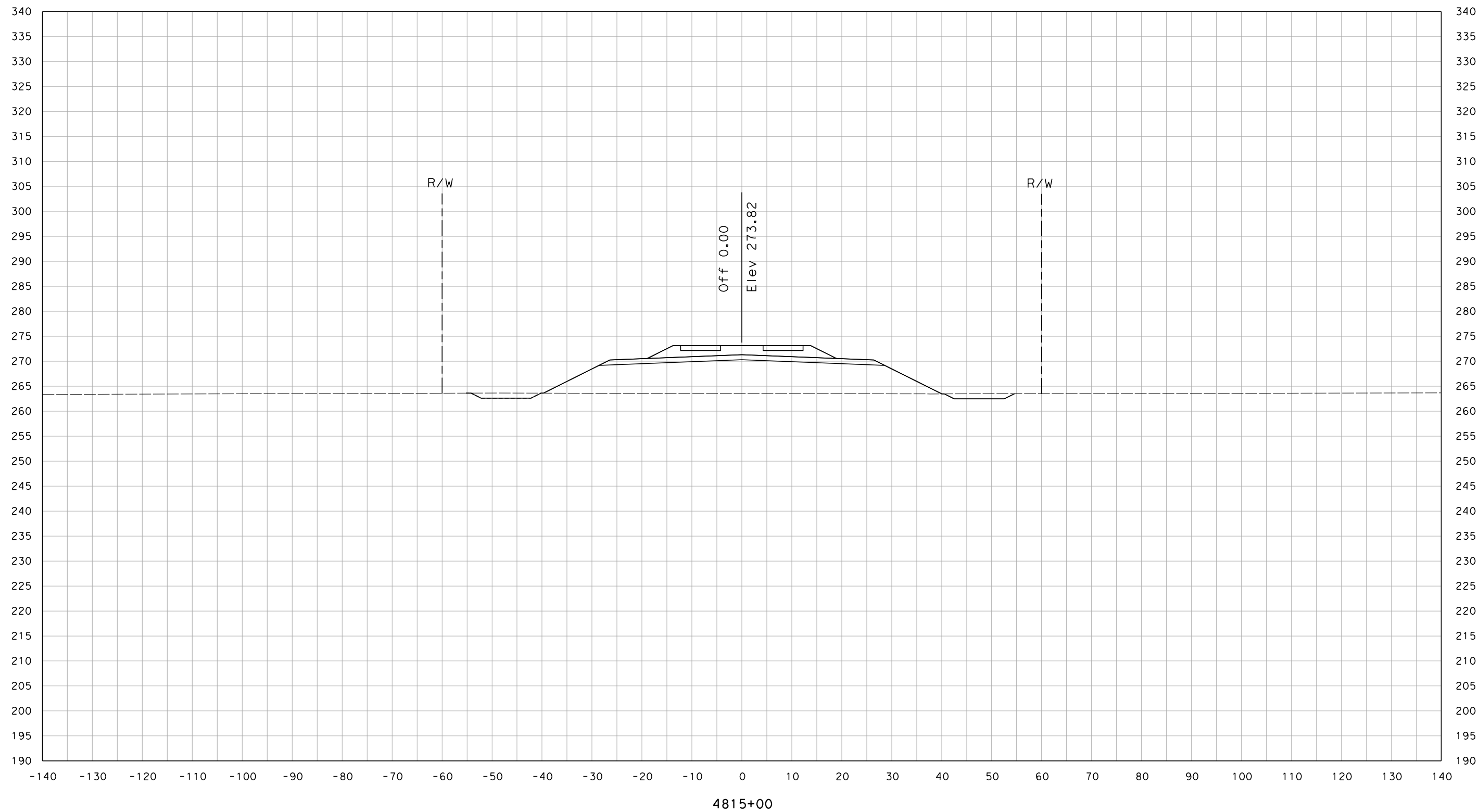
STA. 4800+00 TO STA. 4800+00



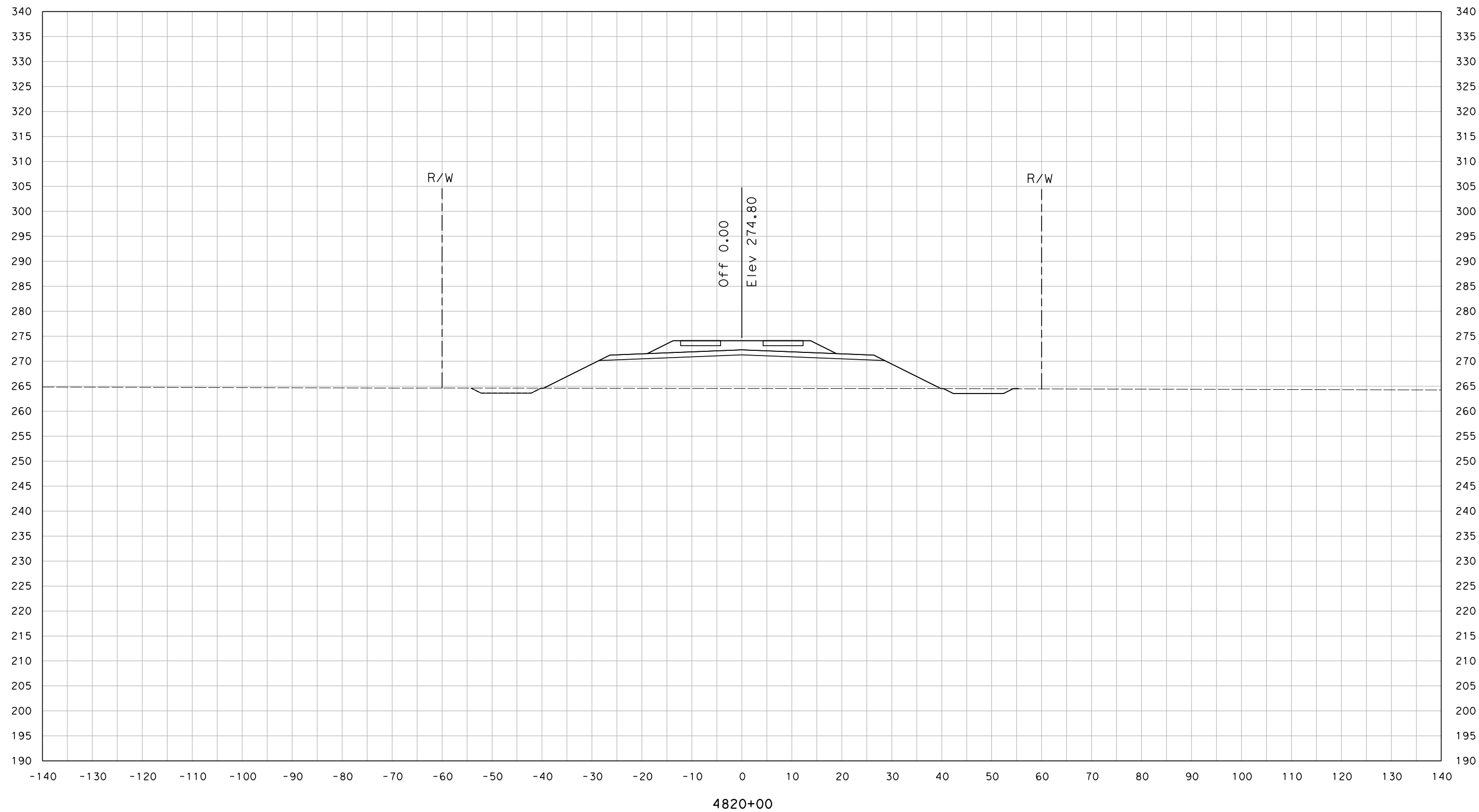
STA. 4805+00 TO STA. 4805+00



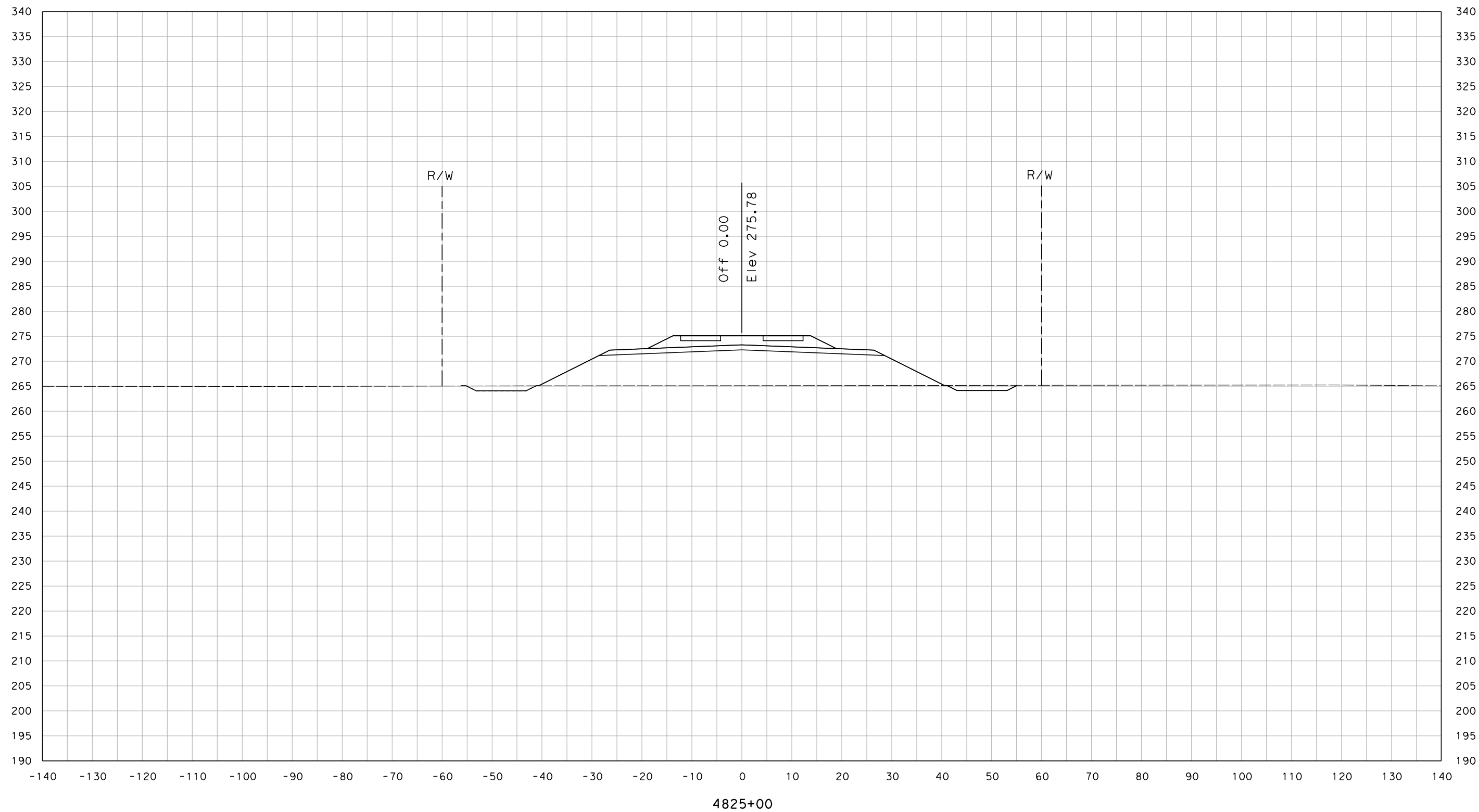
STA. 4810+00 TO STA. 4810+00



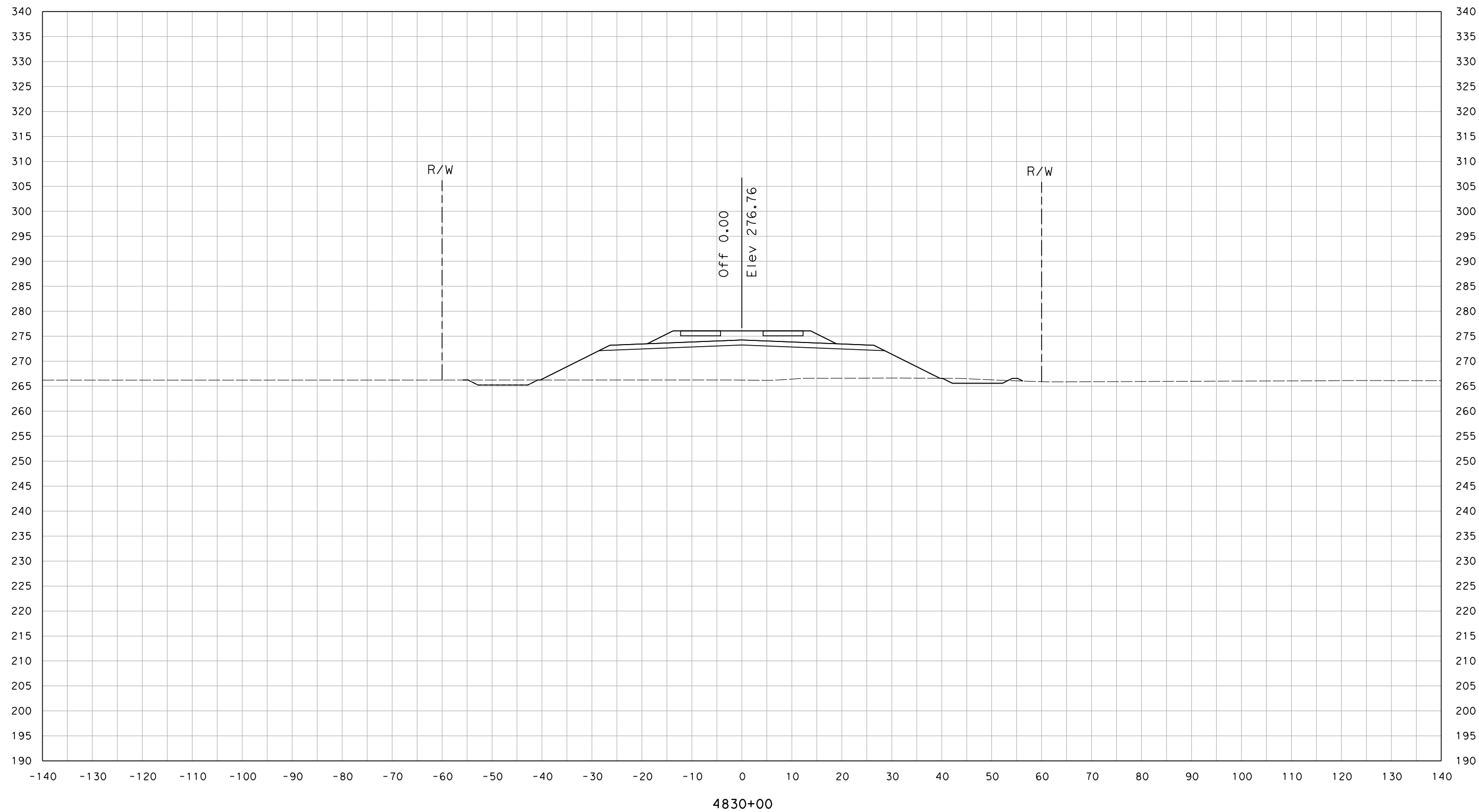
STA. 4815+00 TO STA. 4815+00



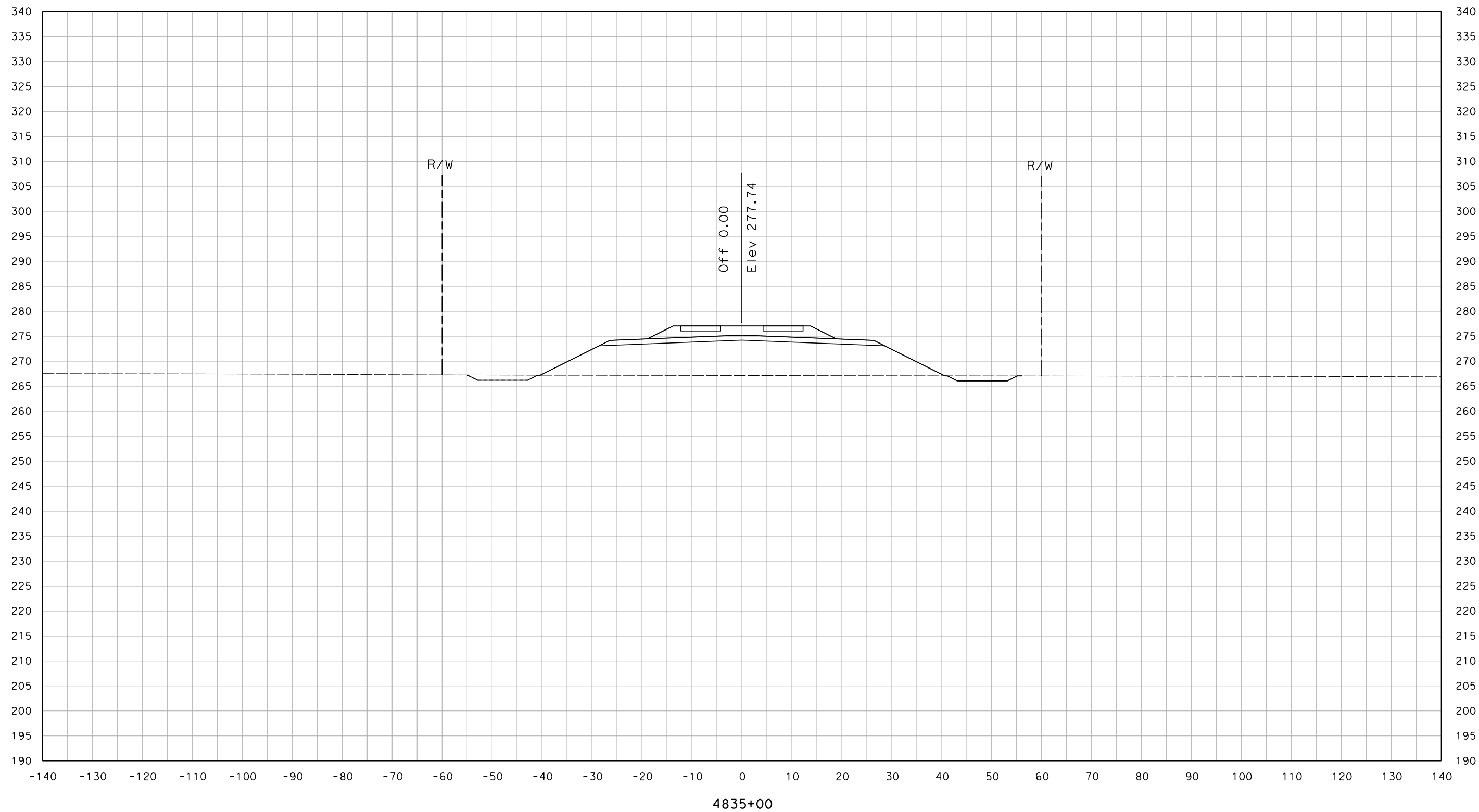
STA. 4820+00 TO STA. 4820+00



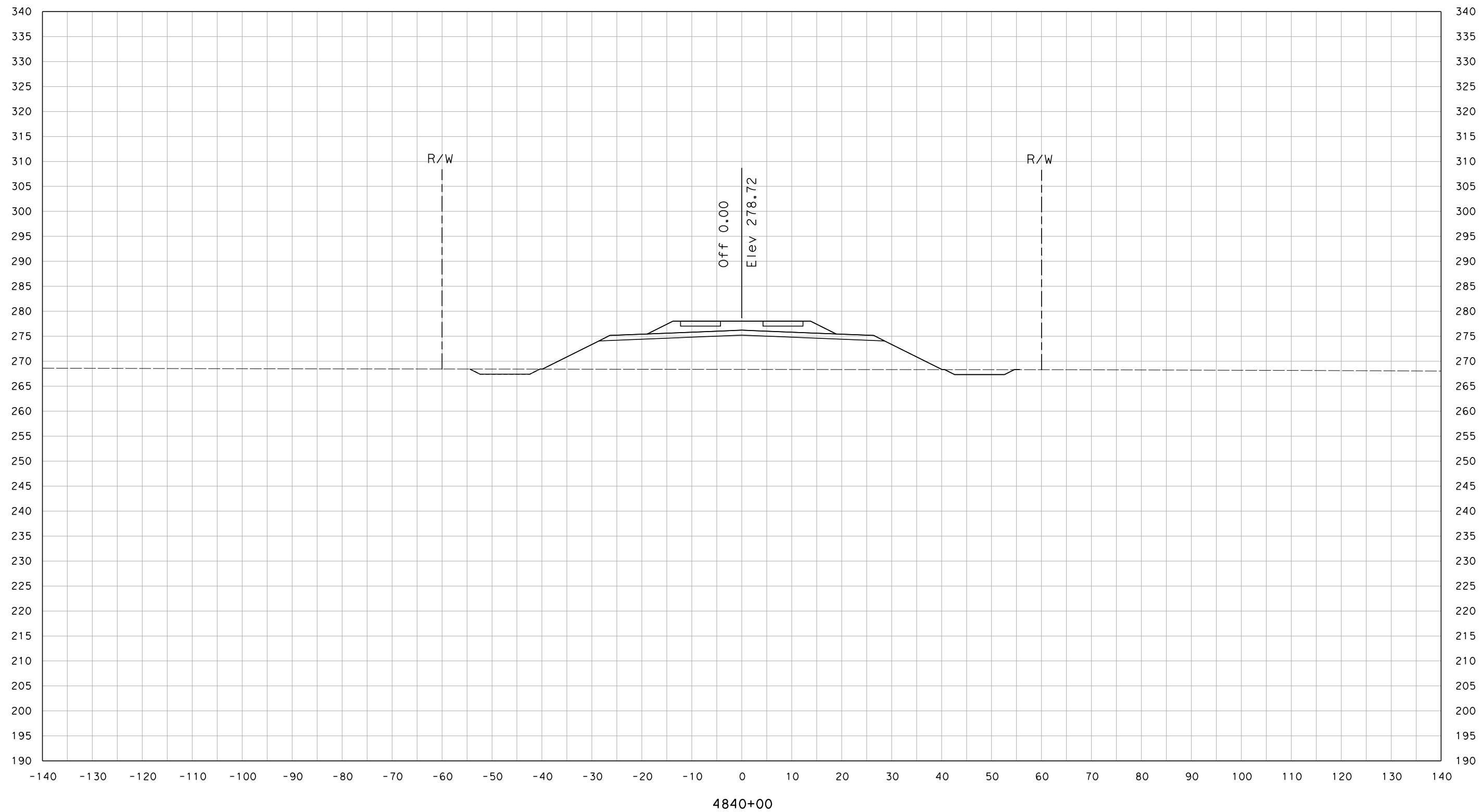
STA. 4825+00 TO STA. 4825+00



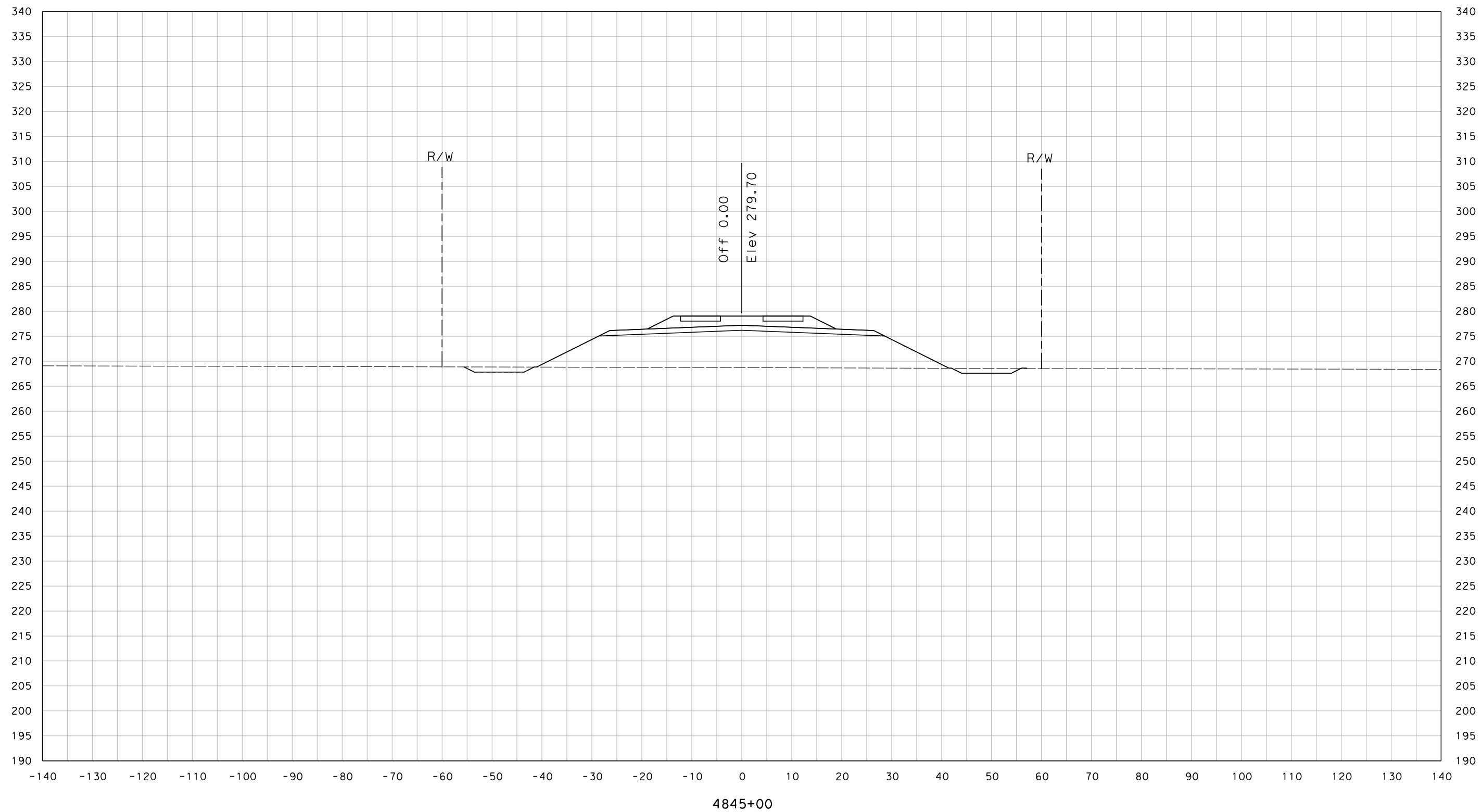
STA. 4830+00 TO STA. 4830+00



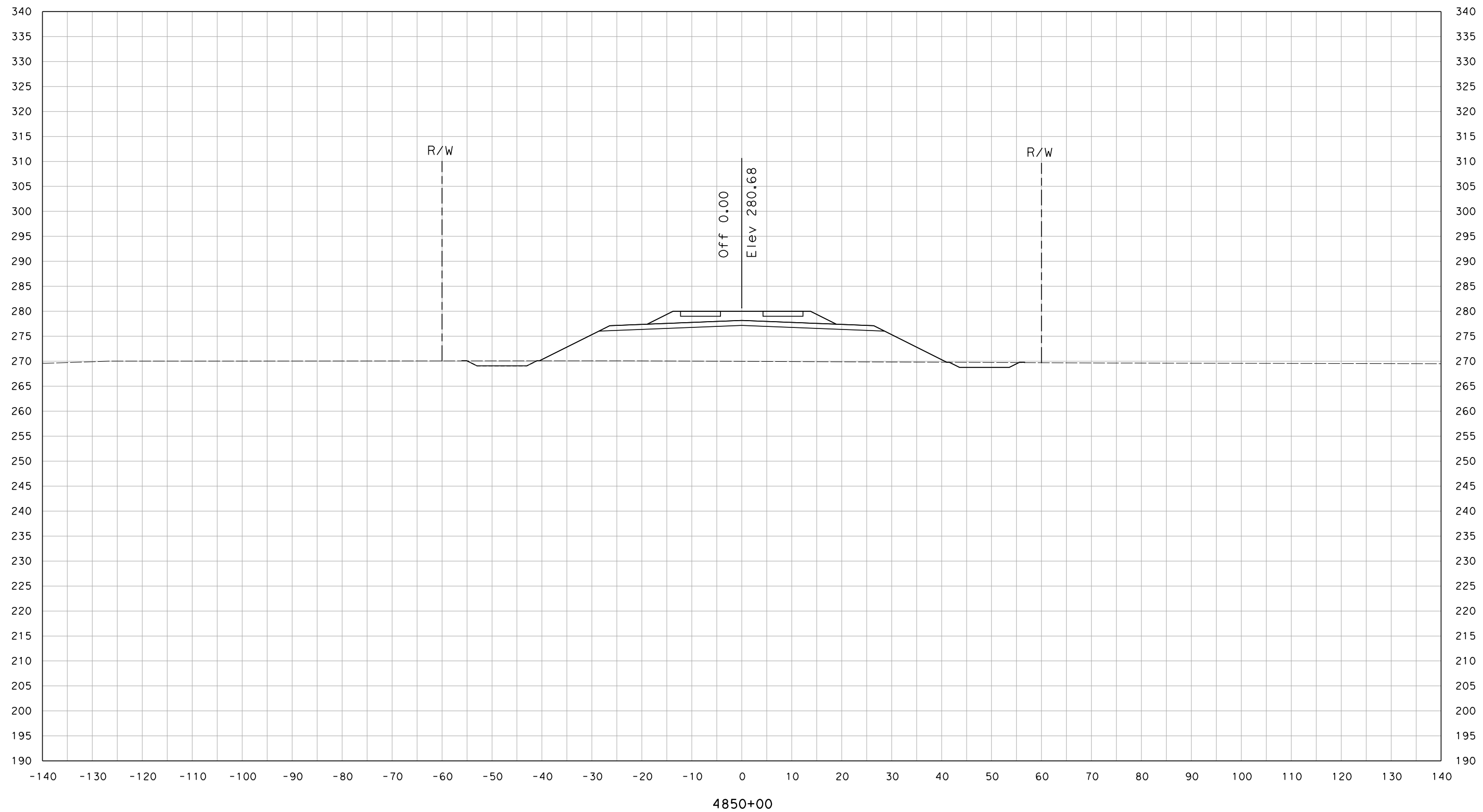
STA. 4835+00 TO STA. 4835+00



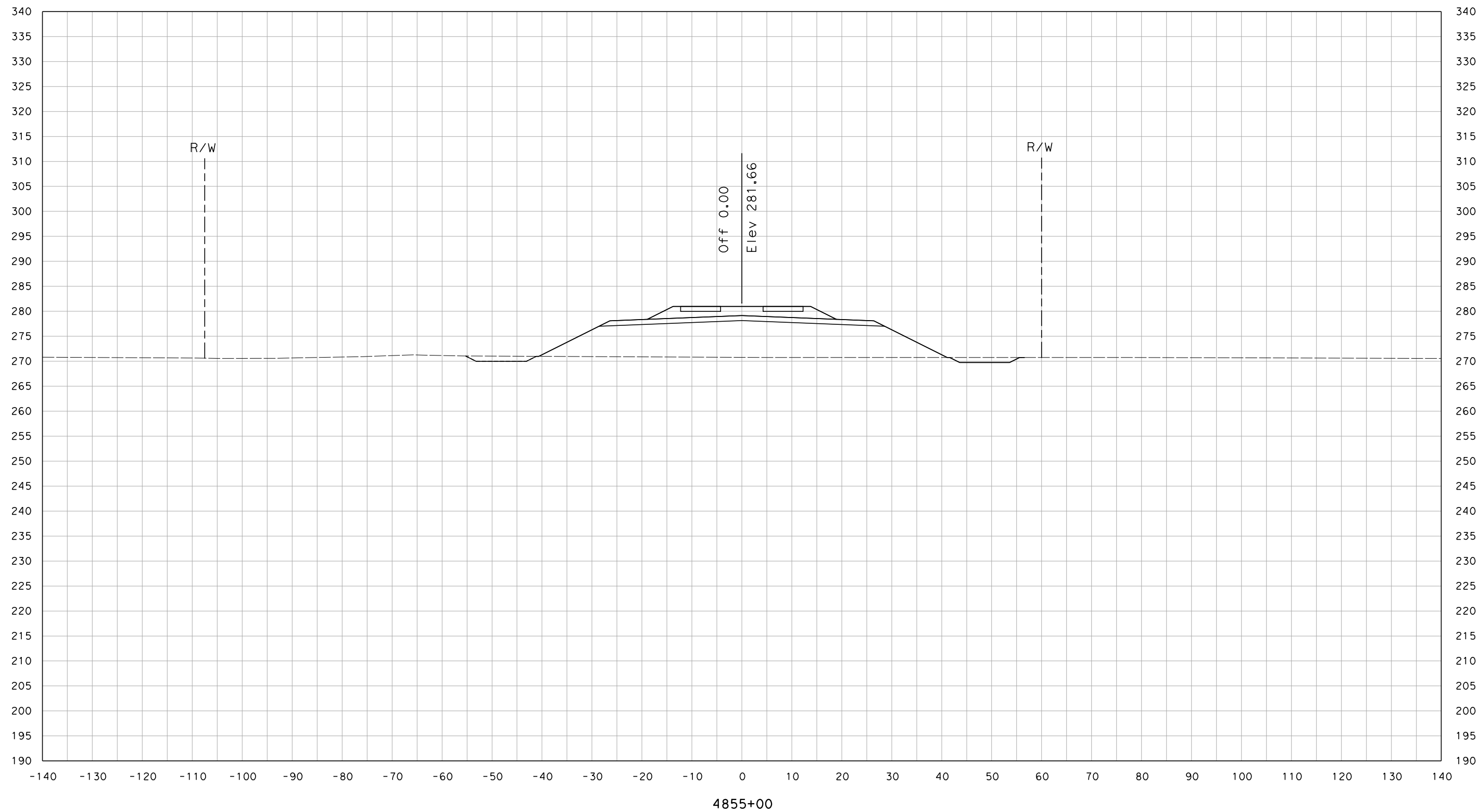
STA. 4840+00 TO STA. 4840+00



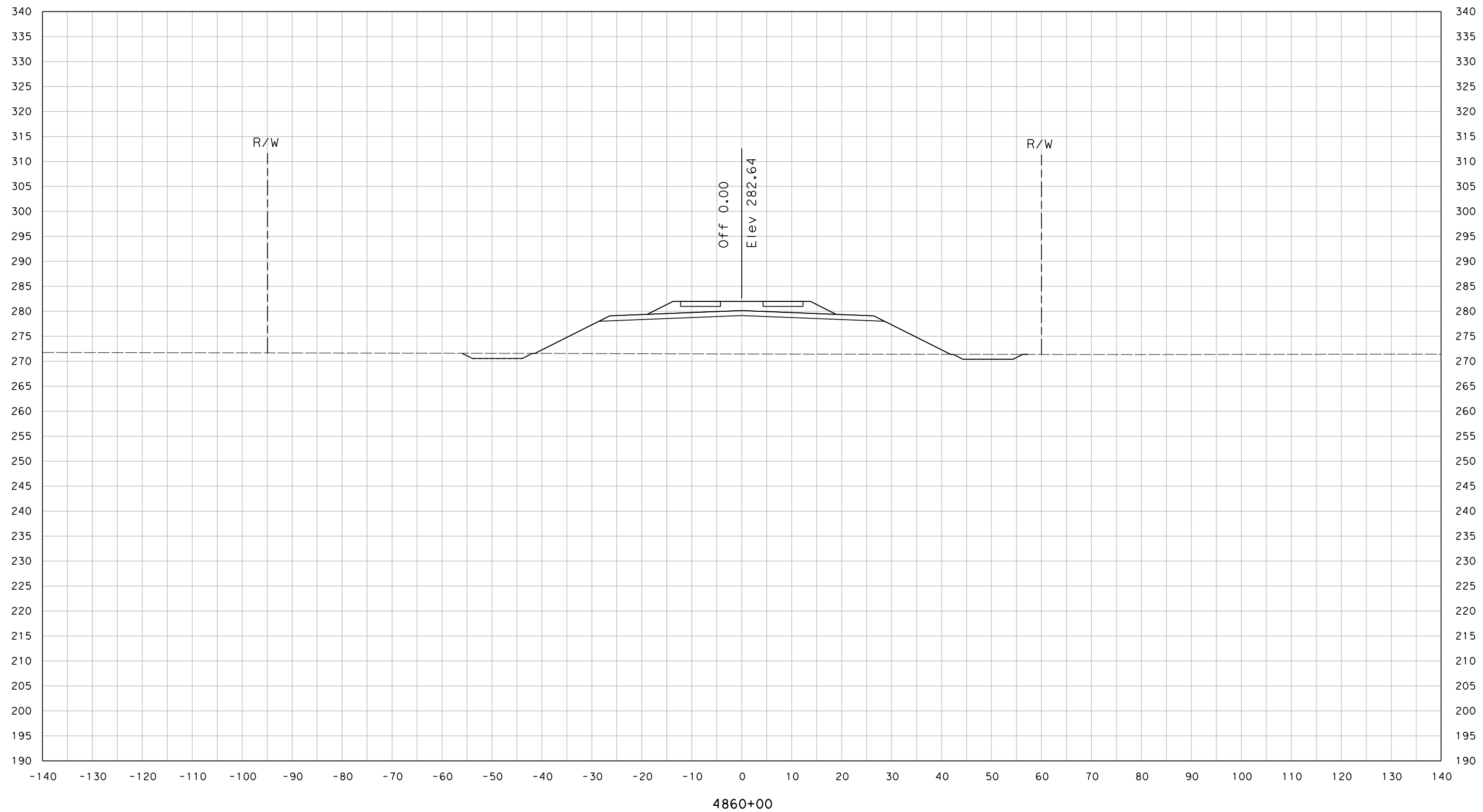
STA. 4845+00 TO STA. 4845+00



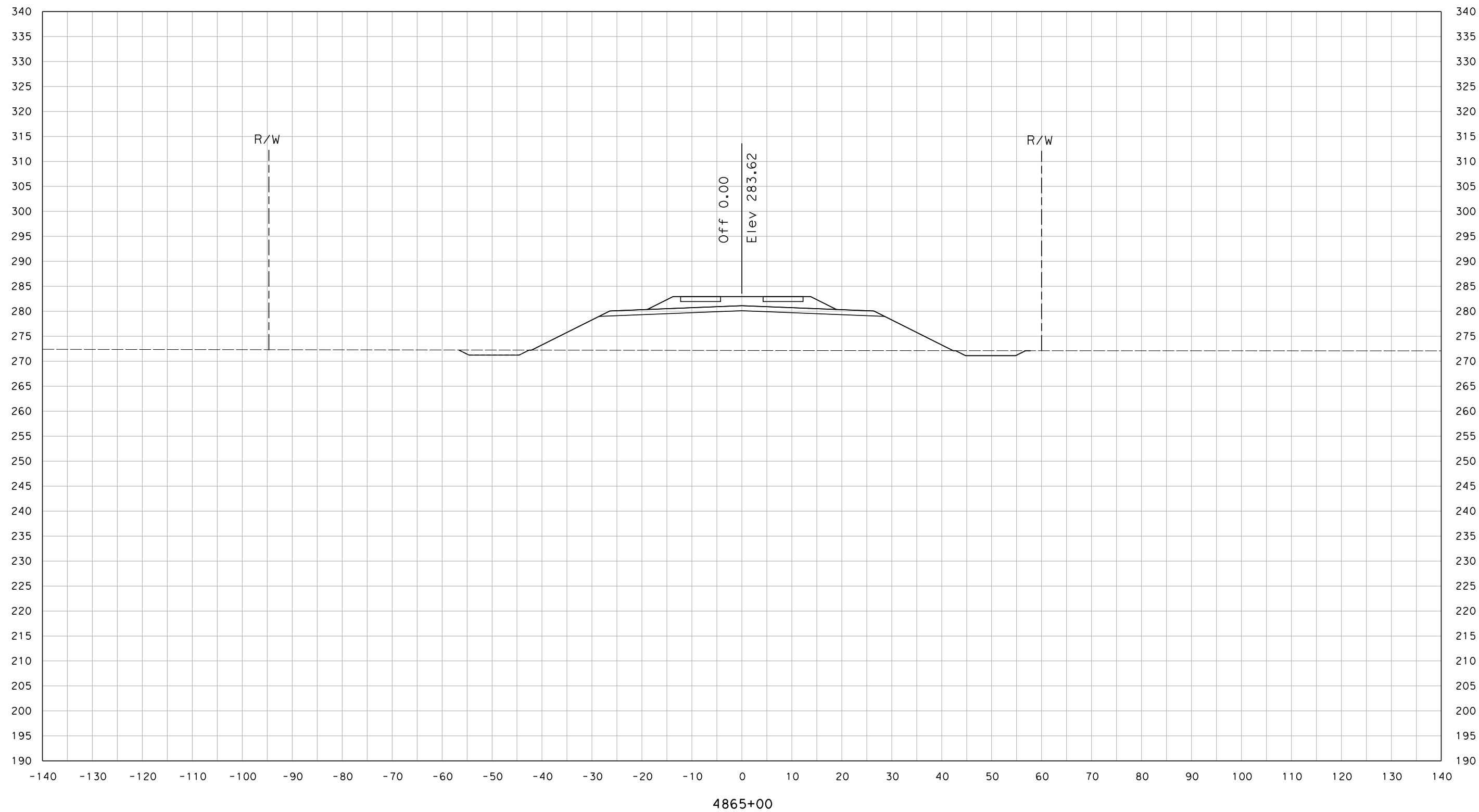
STA. 4850+00 TO STA. 4850+00



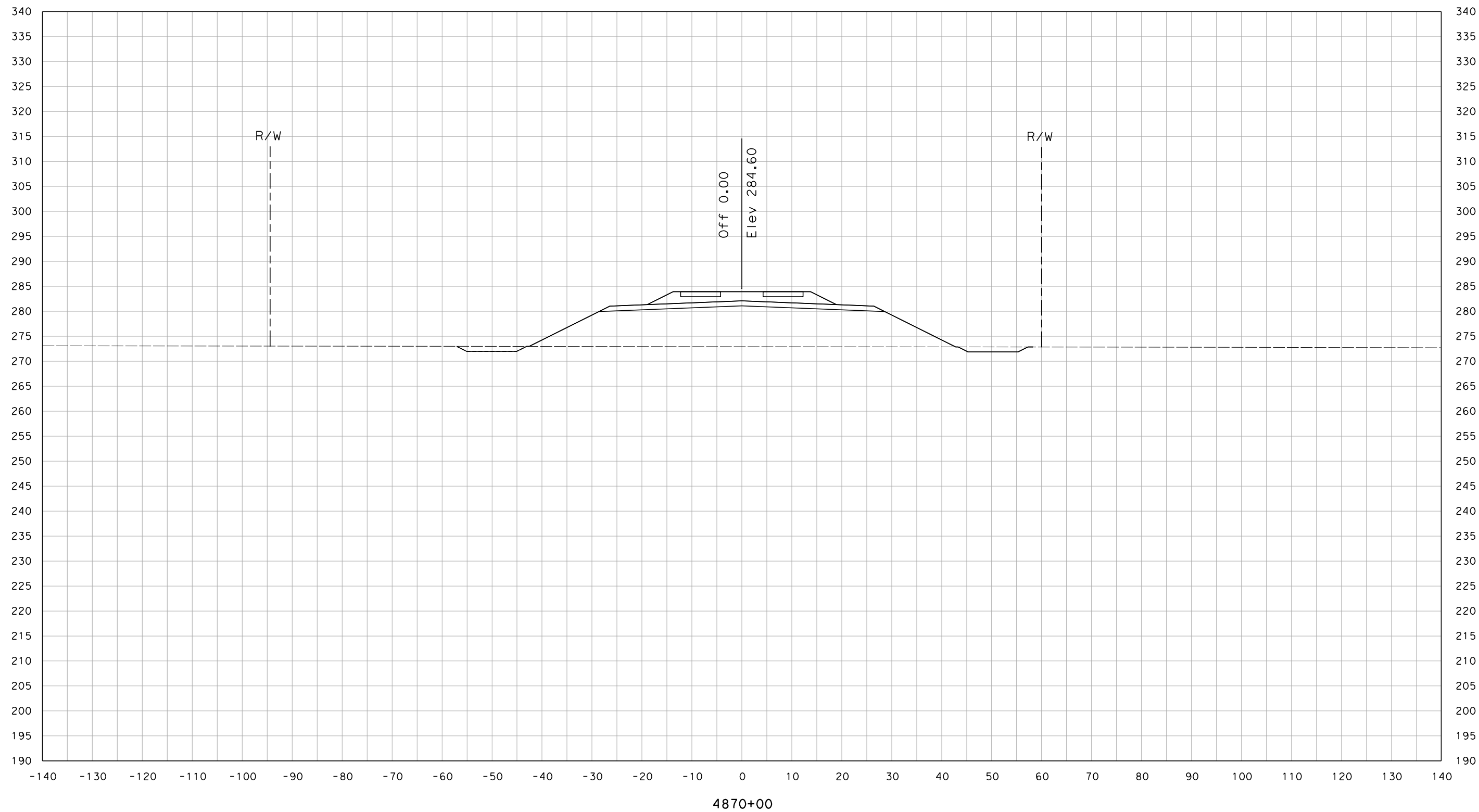
STA. 4855+00 TO STA. 4855+00



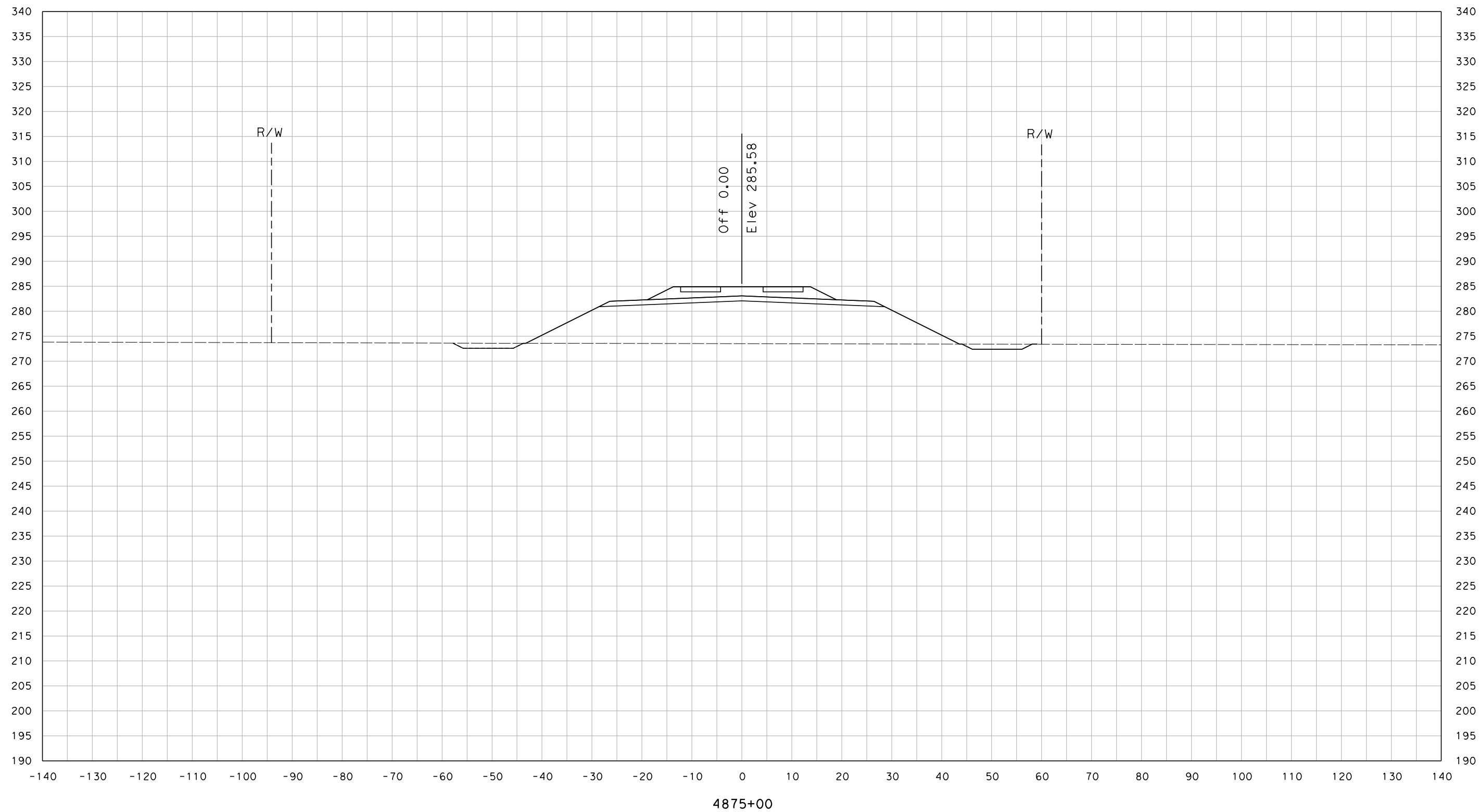
STA. 4860+00 TO STA. 4860+00



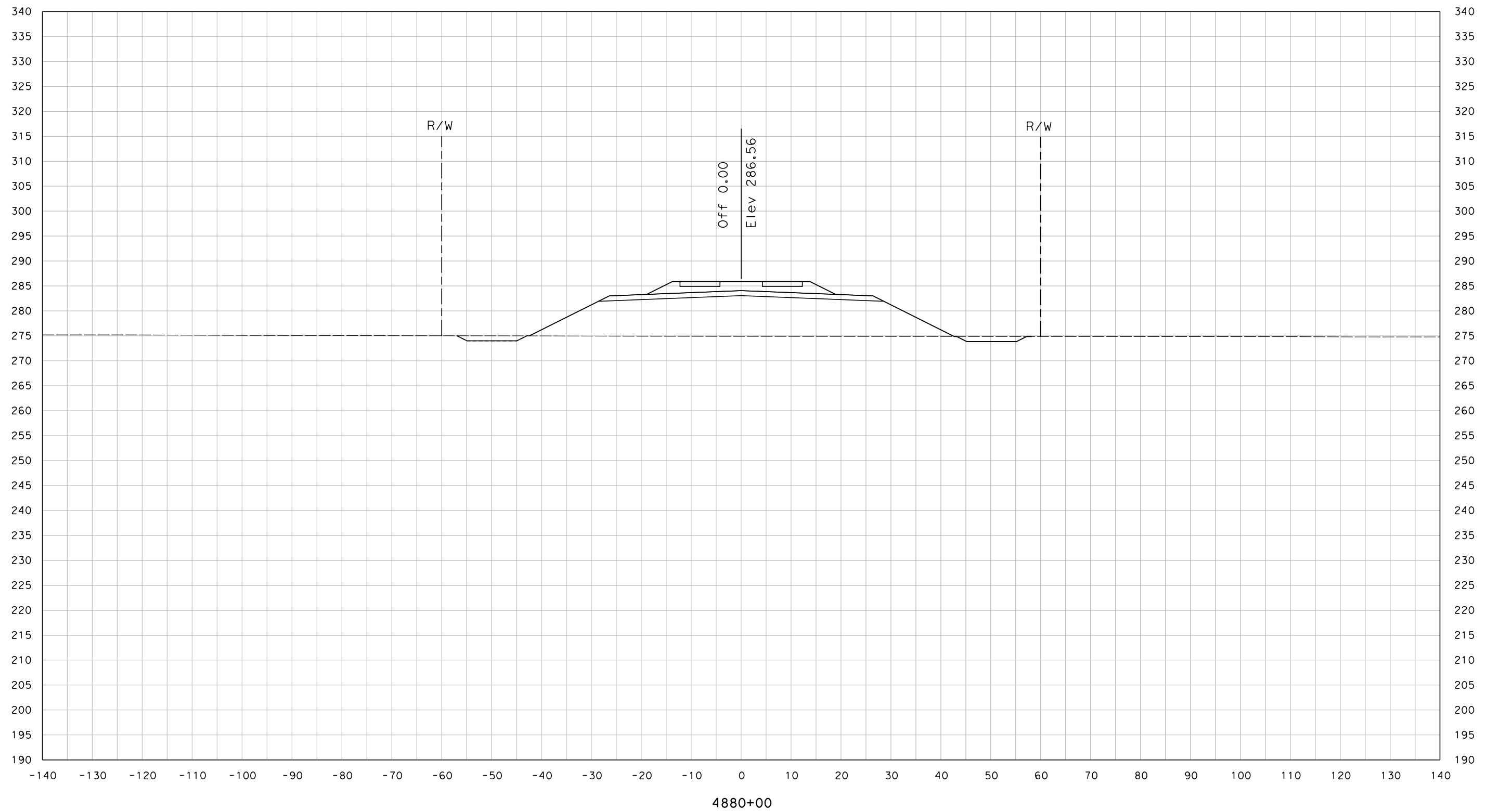
STA. 4865+00 TO STA. 4865+00



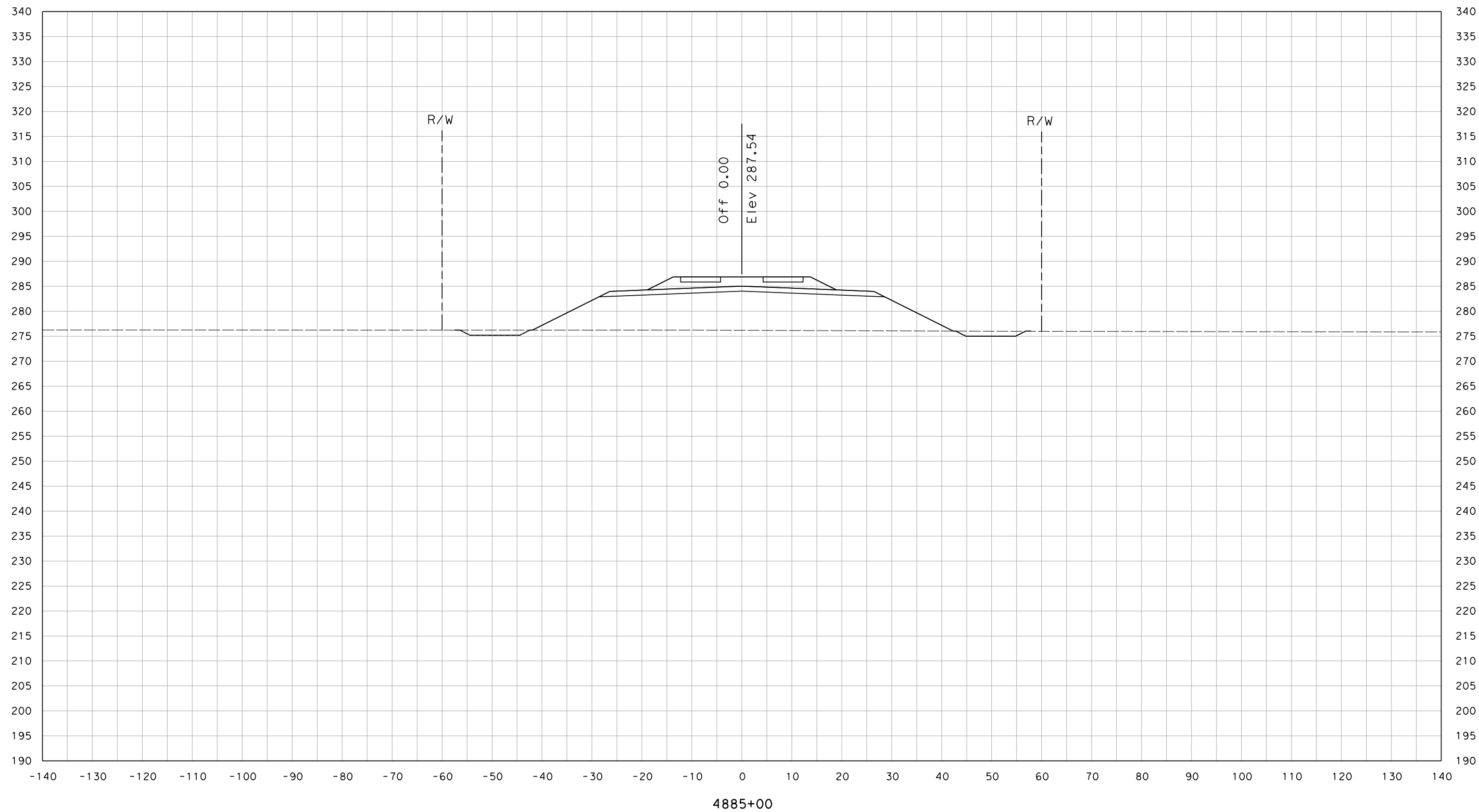
STA. 4870+00 TO STA. 4870+00



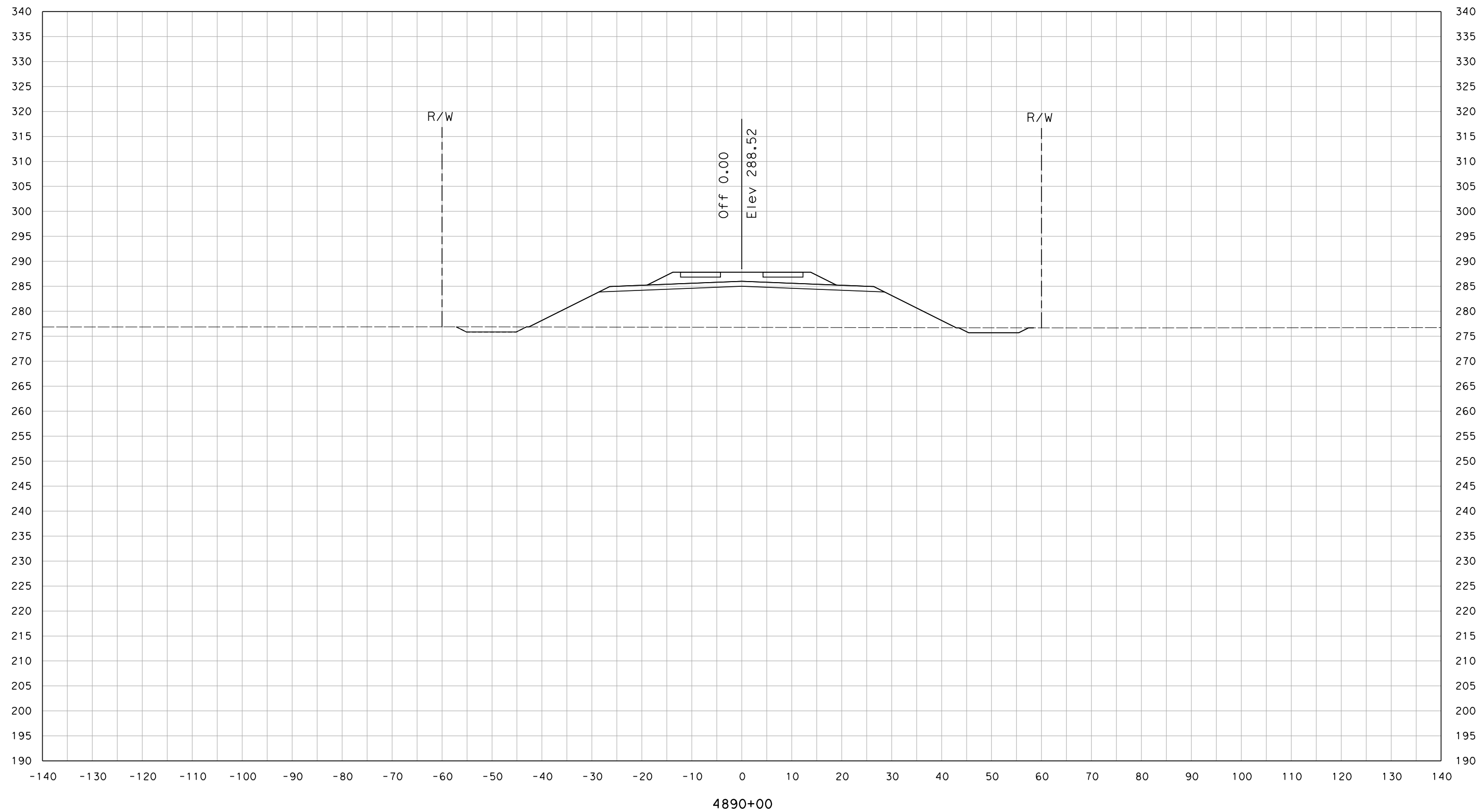
STA. 4875+00 TO STA. 4875+00



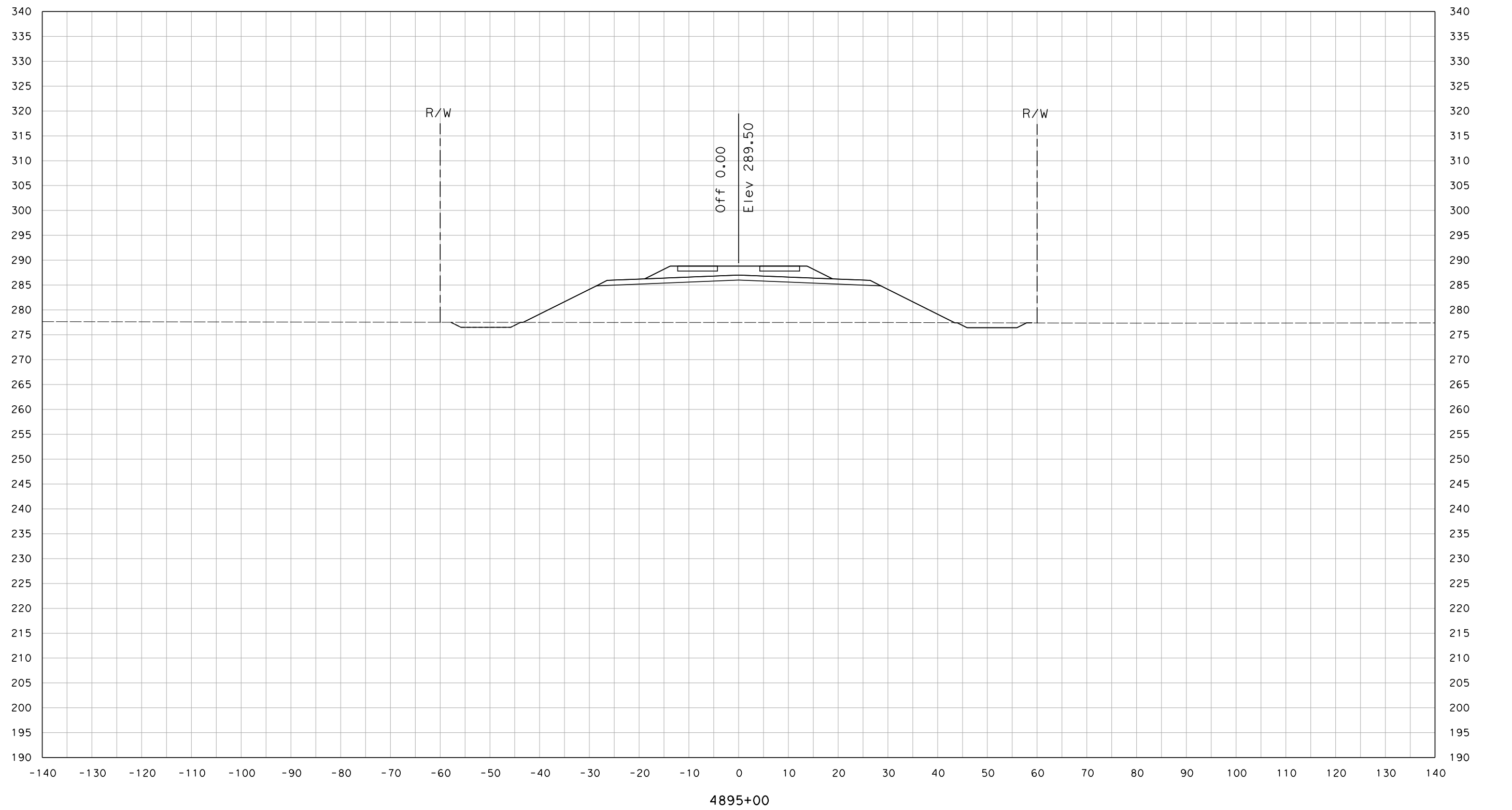
STA. 4880+00 TO STA. 4880+00

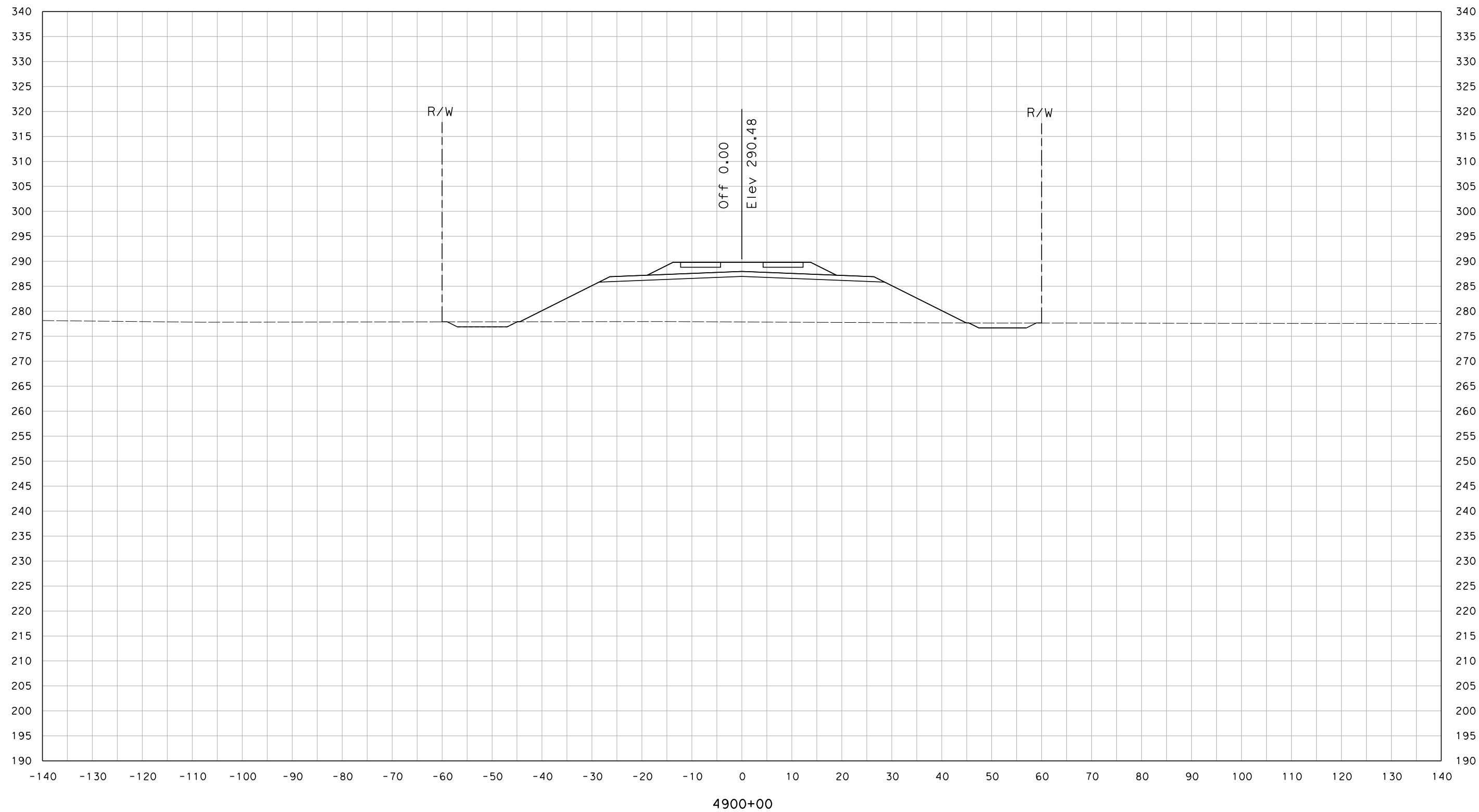


STA. 4885+00 TO STA. 4885+00

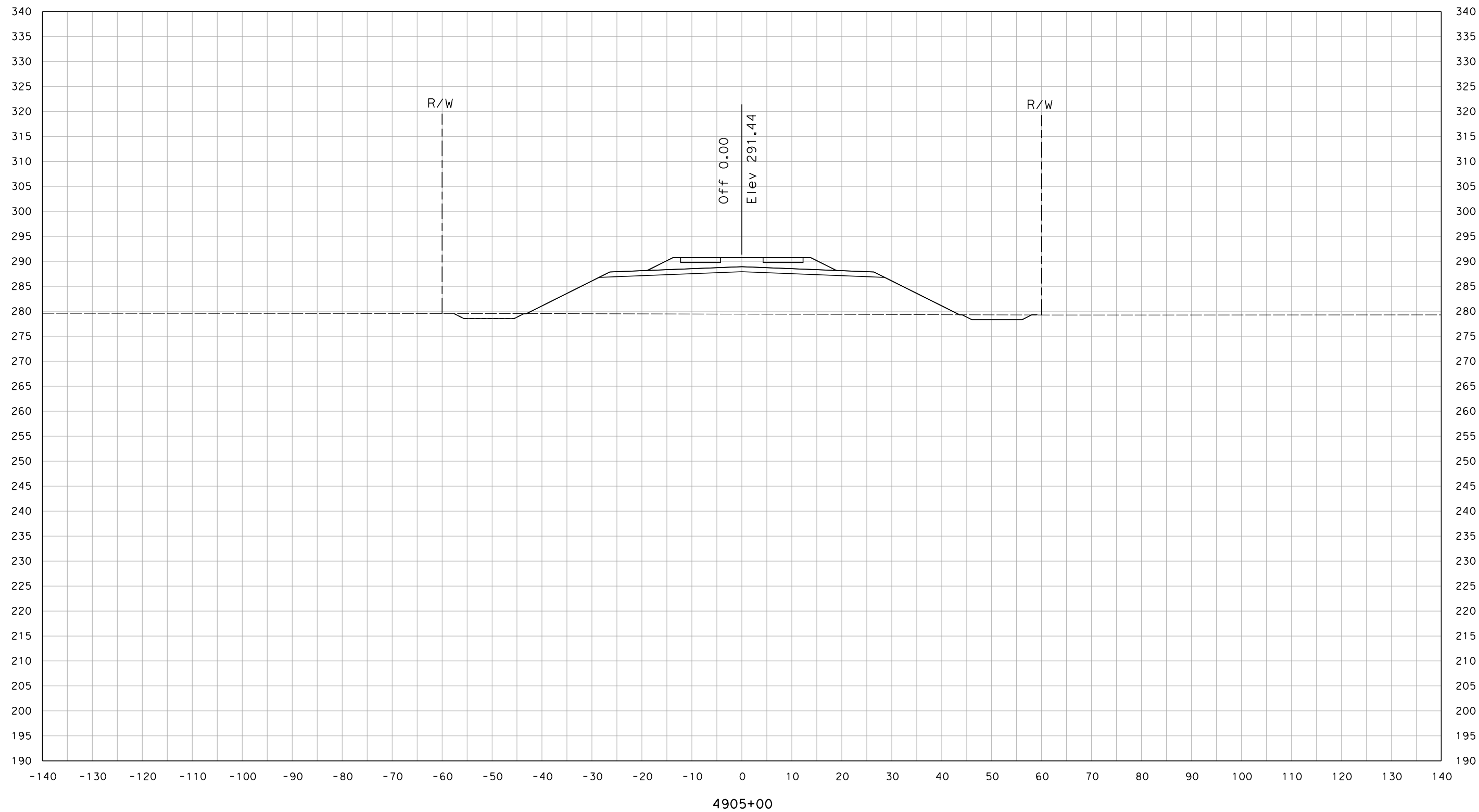


STA. 4890+00 TO STA. 4890+00

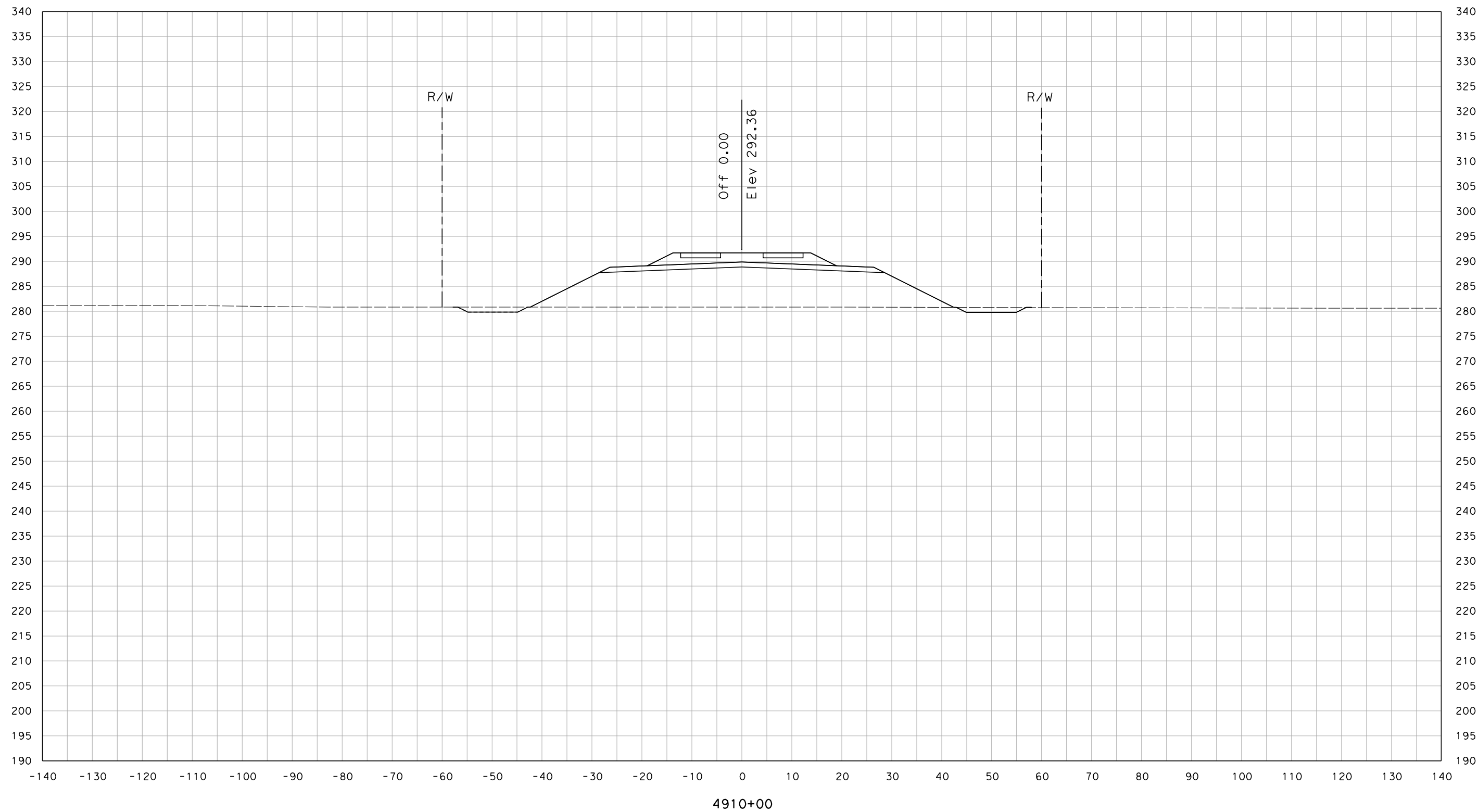




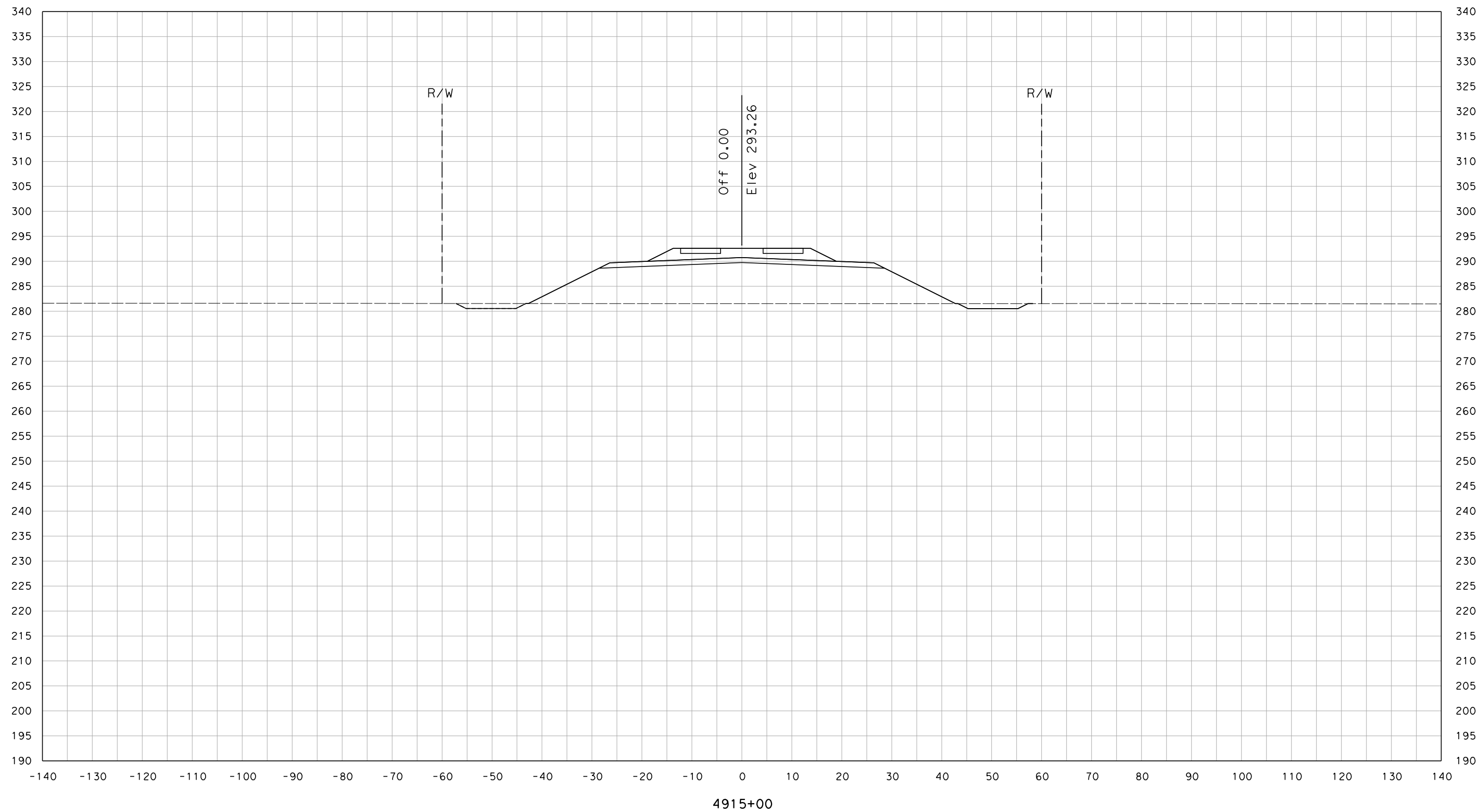
STA. 4900+00 TO STA. 4900+00



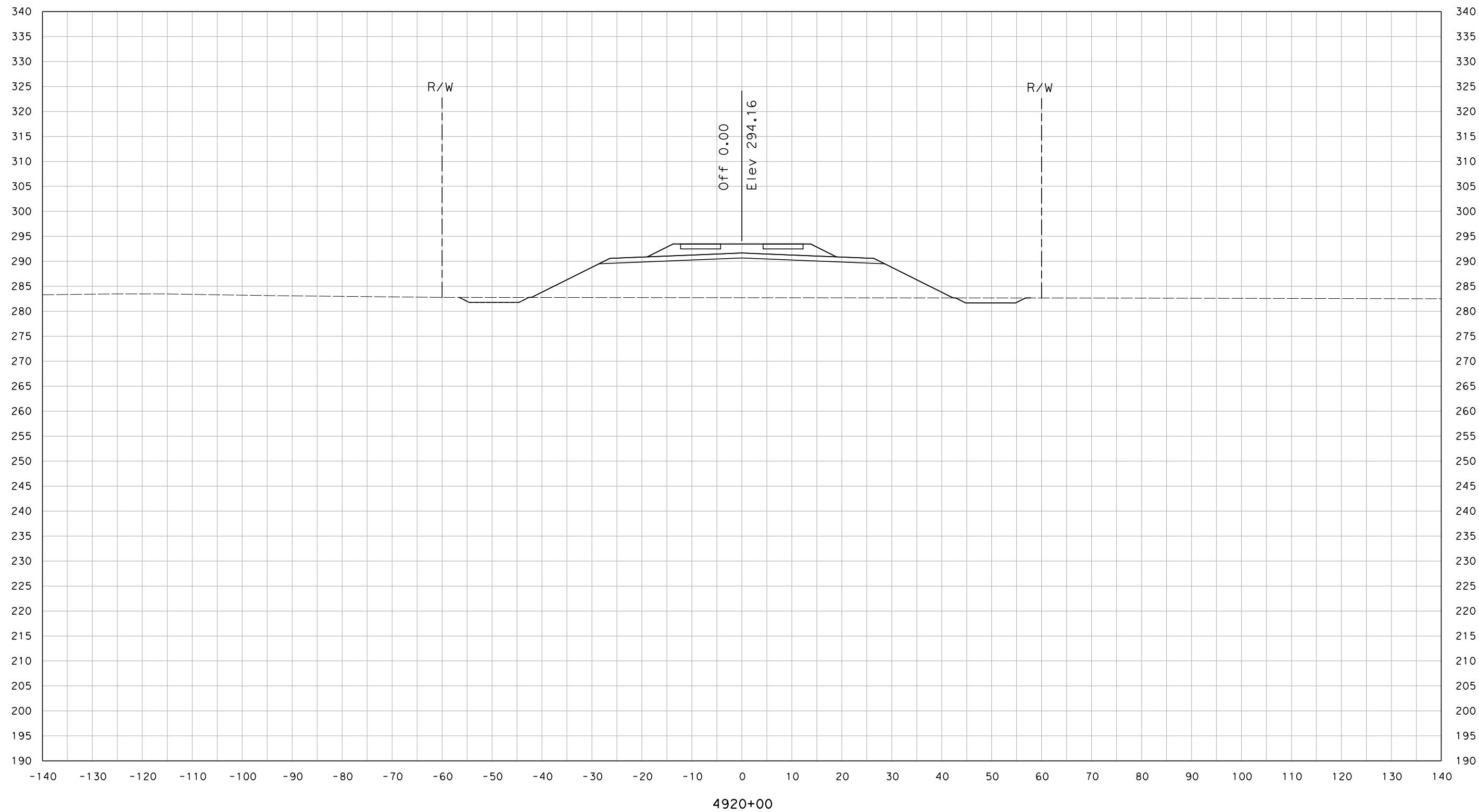
STA. 4905+00 TO STA. 4905+00



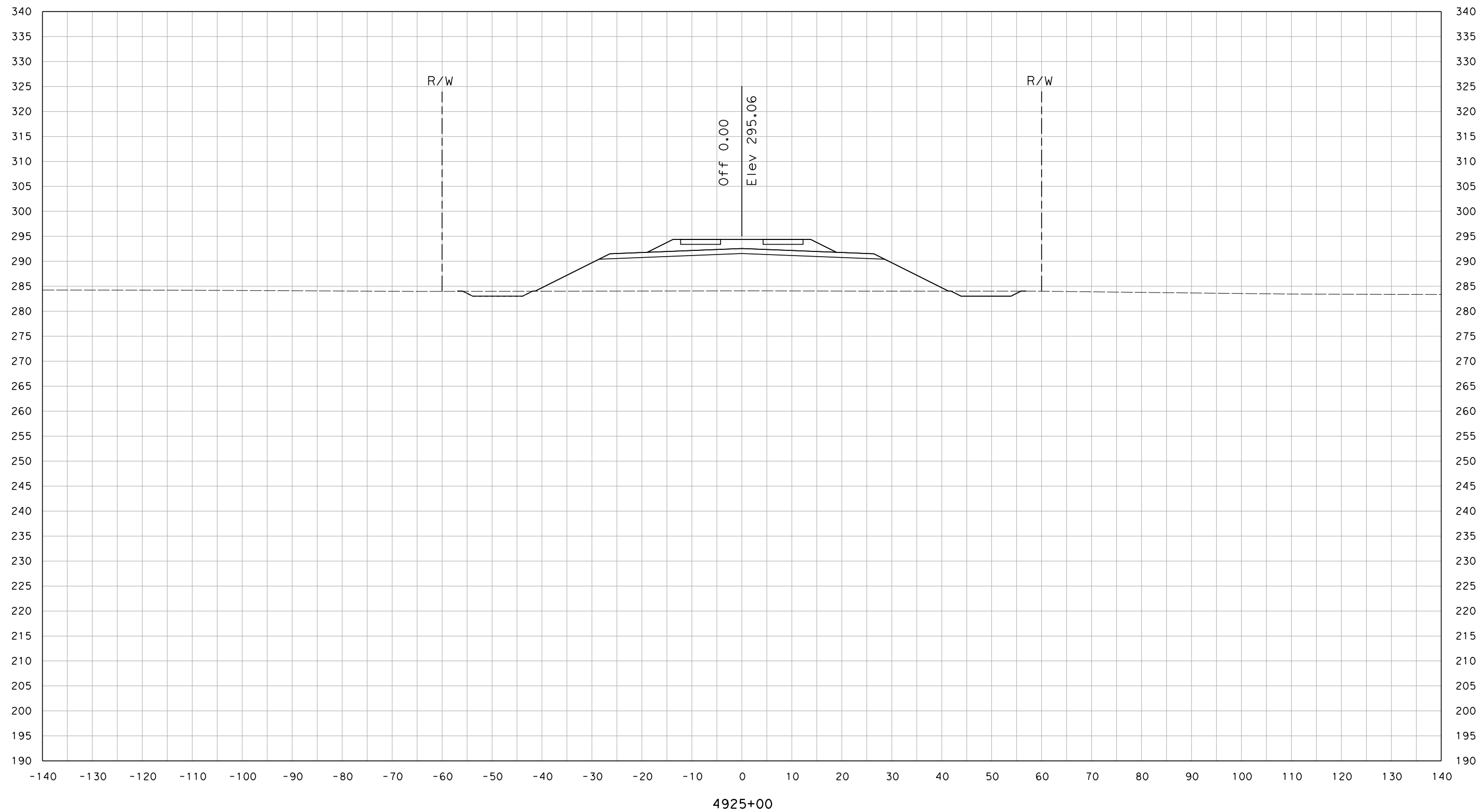
STA. 4910+00 TO STA. 4910+00



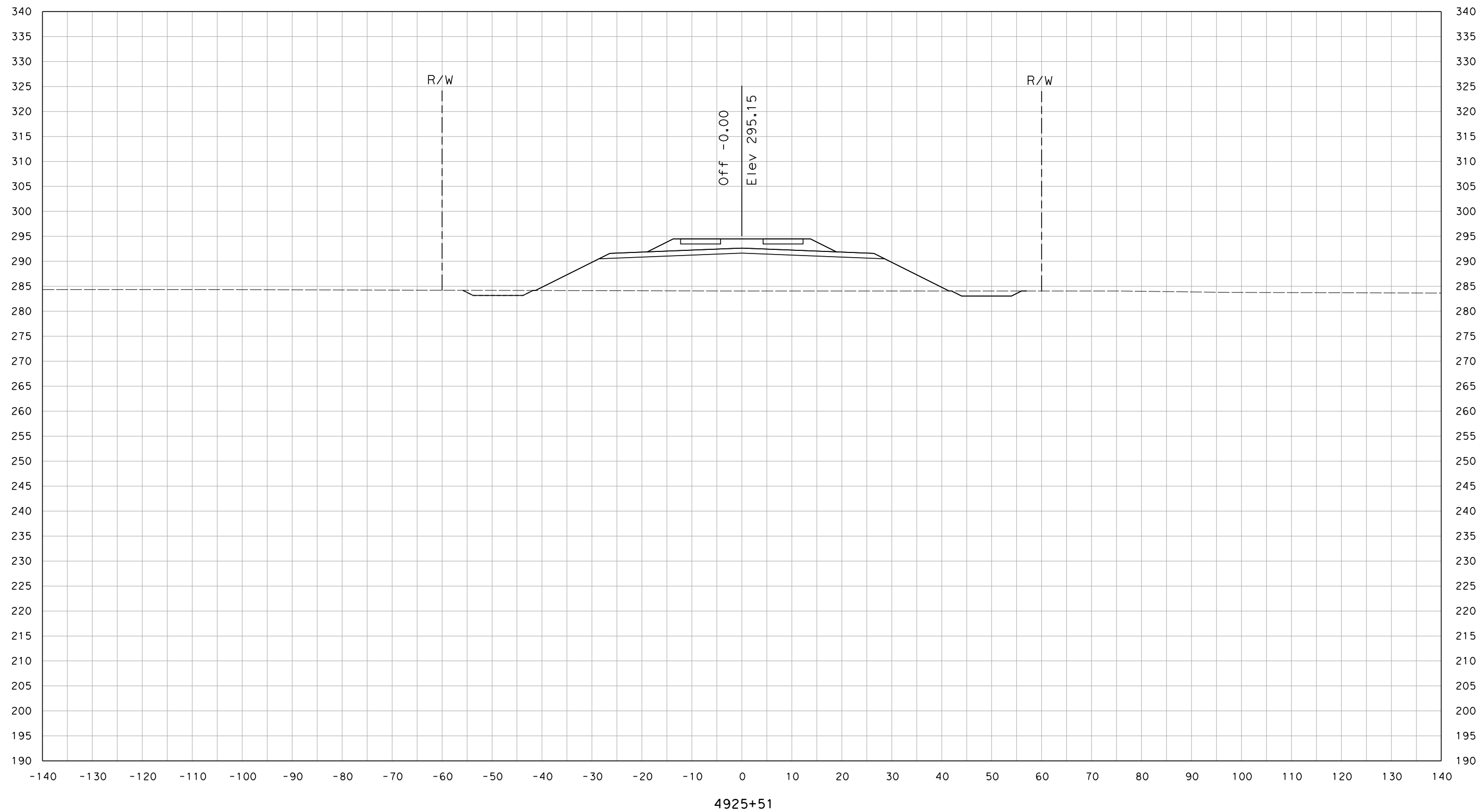
STA. 4915+00 TO STA. 4915+00



STA. 4920+00 TO STA. 4920+00



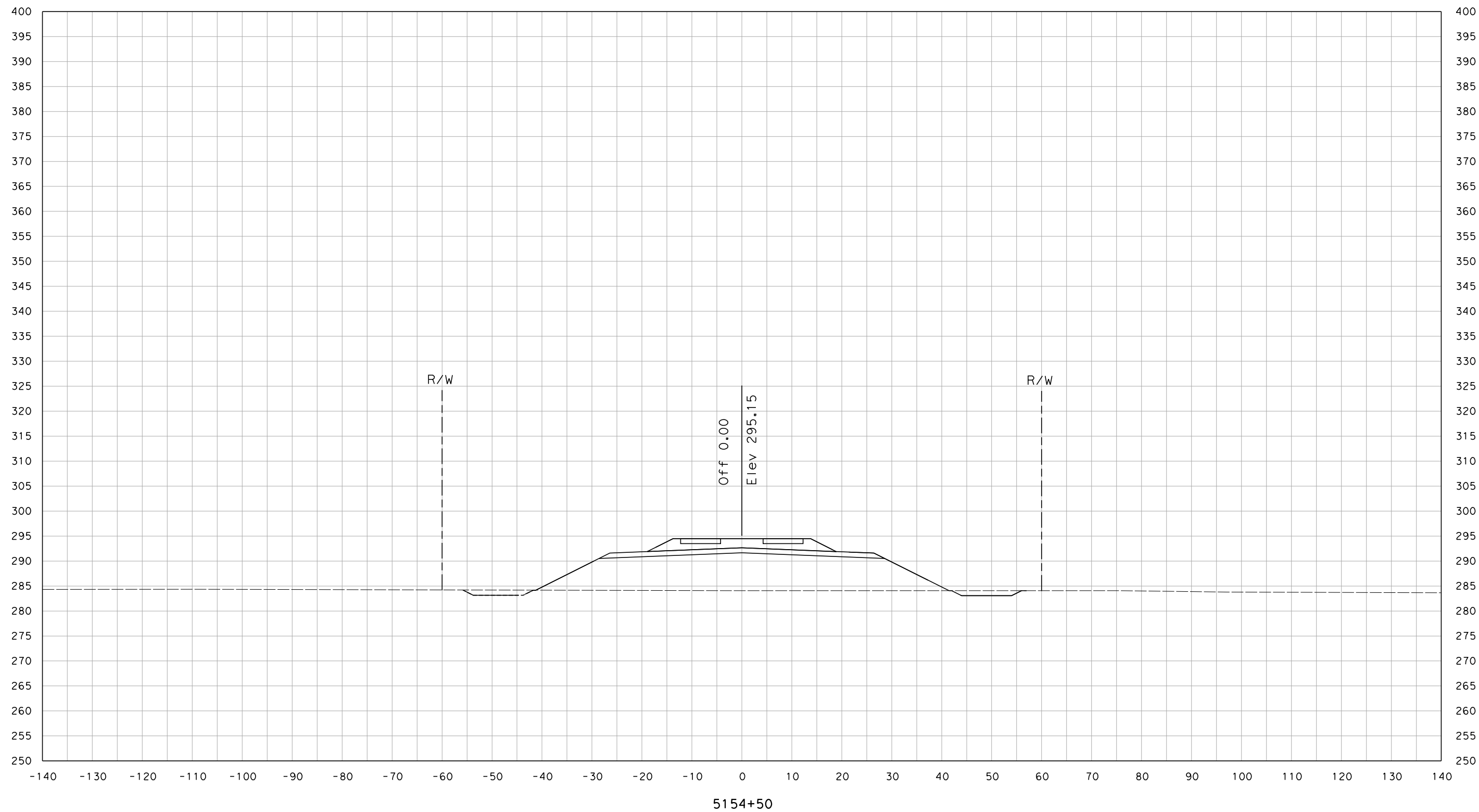
STA. 4925+00 TO STA. 4925+00



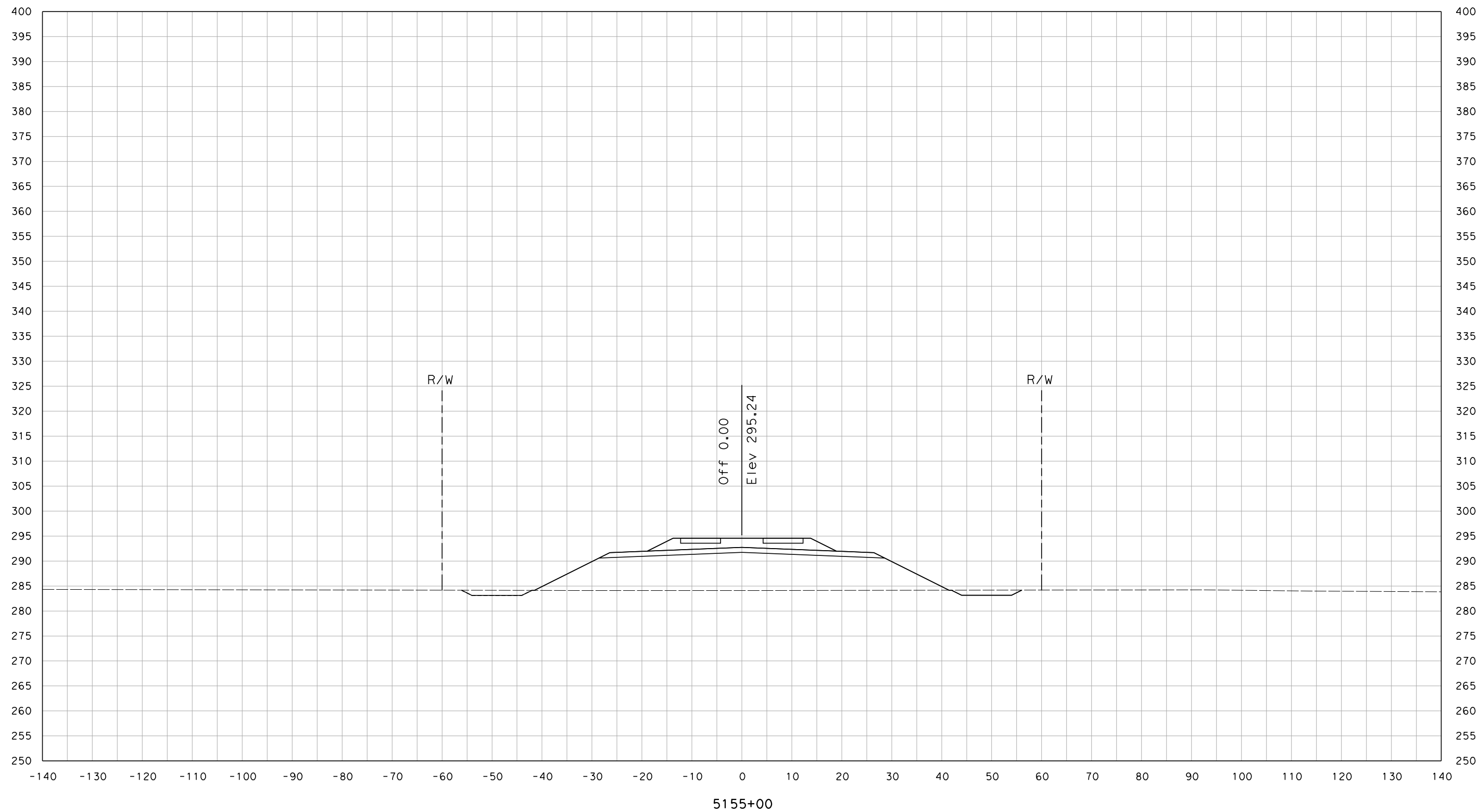
STA. 4925+51 TO STA. 4925+51

ALIGNMENT

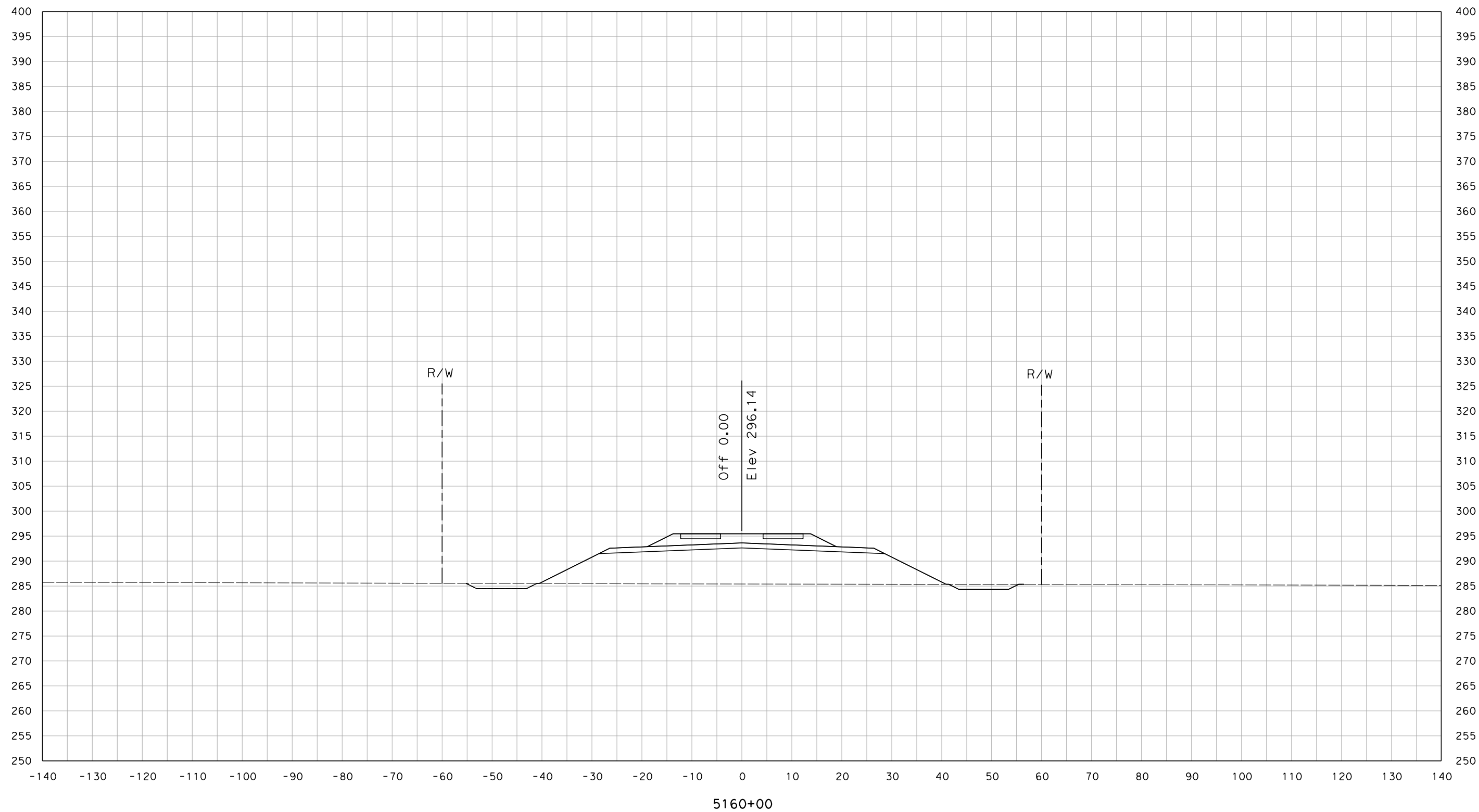
"L1"



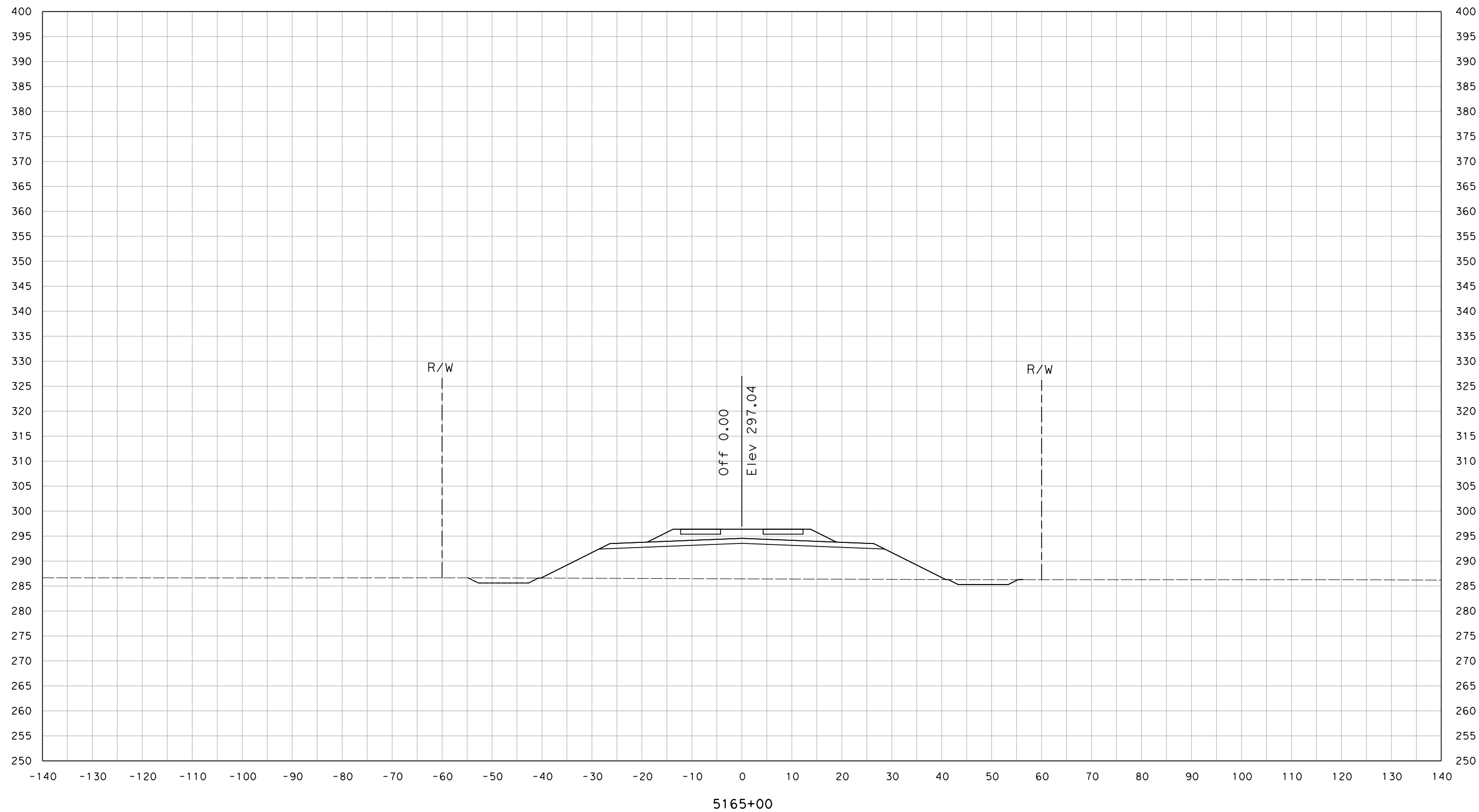
STA. 5154+50 TO STA. 5154+50



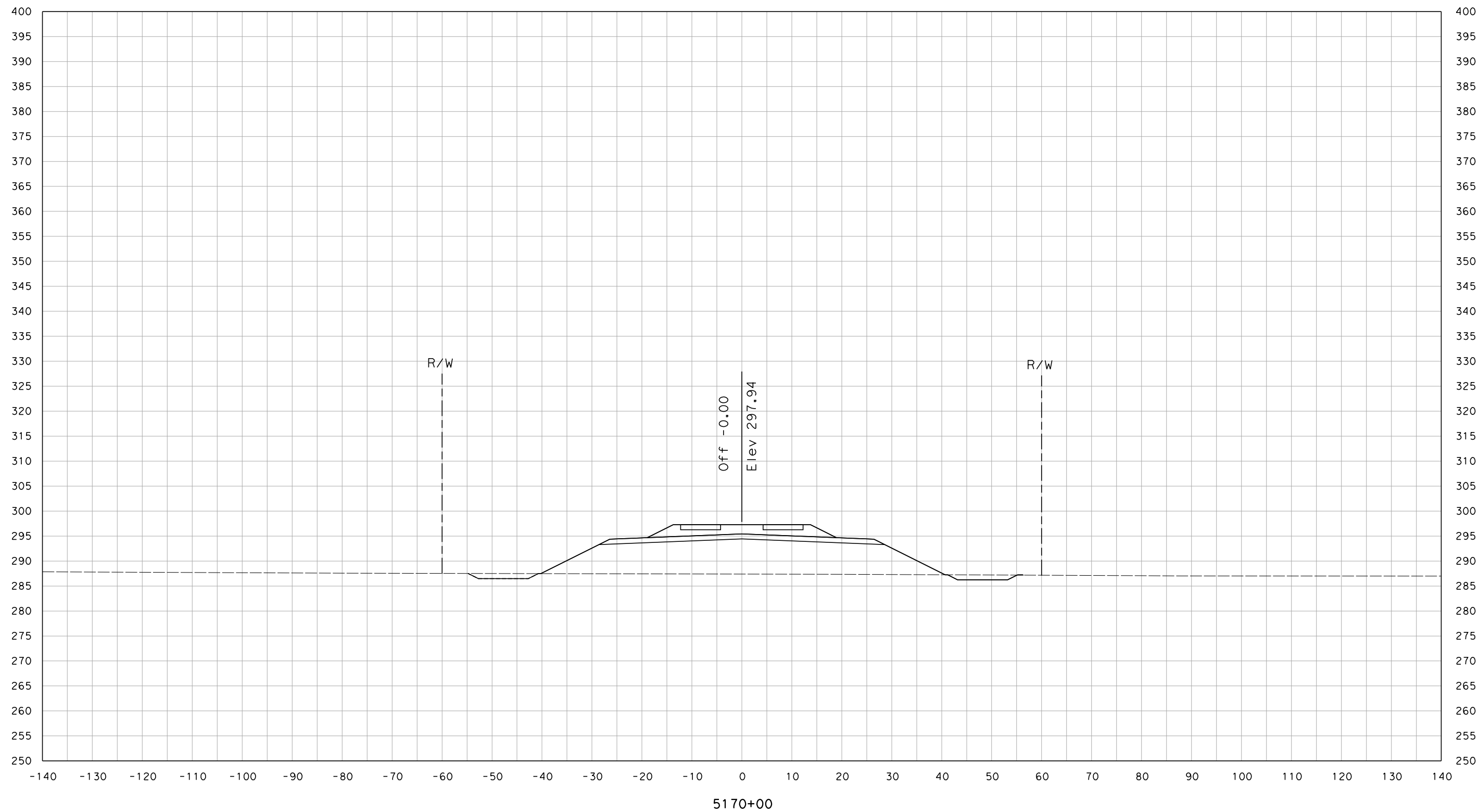
STA. 5155+00 TO STA. 5155+00



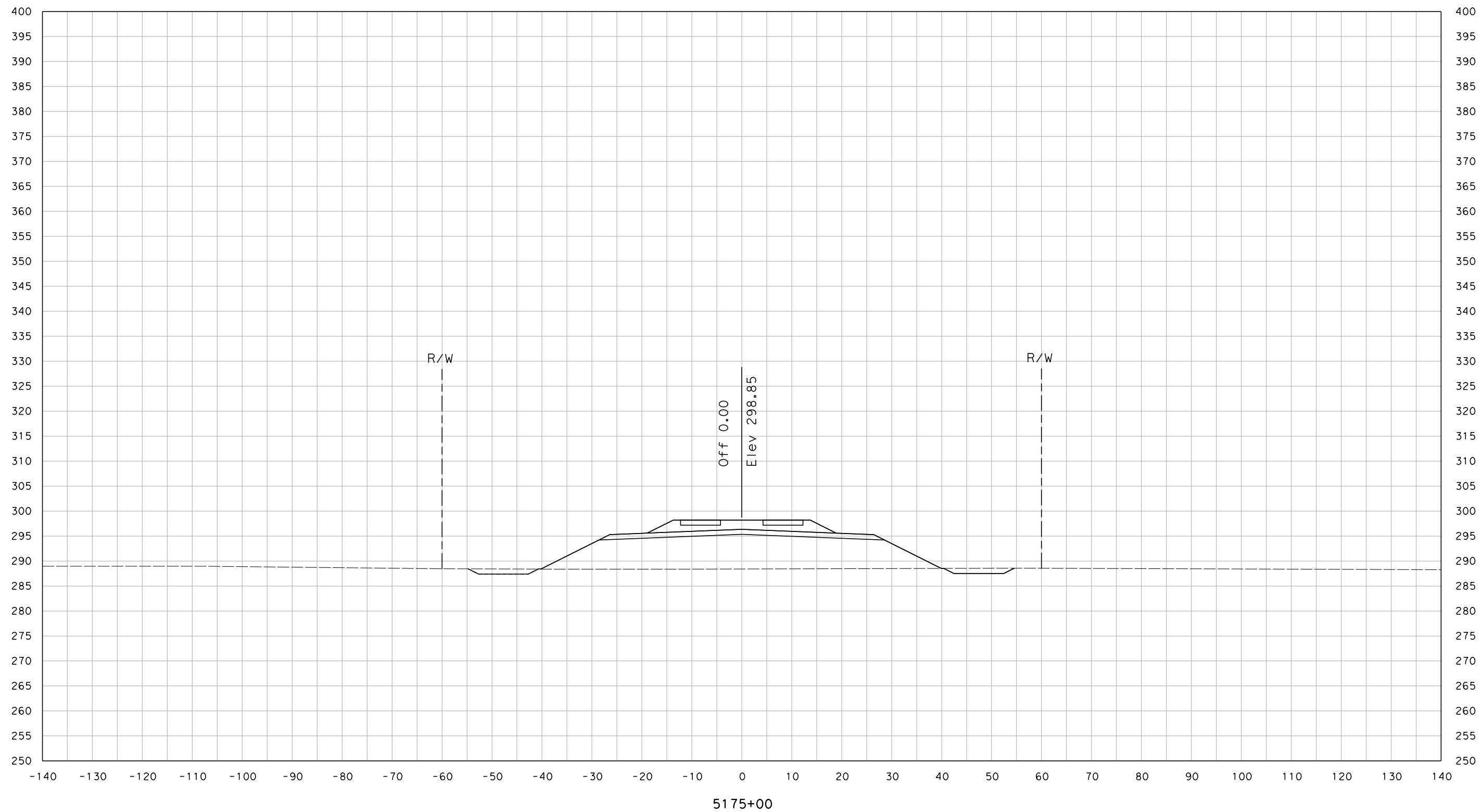
STA. 5160+00 TO STA. 5160+00



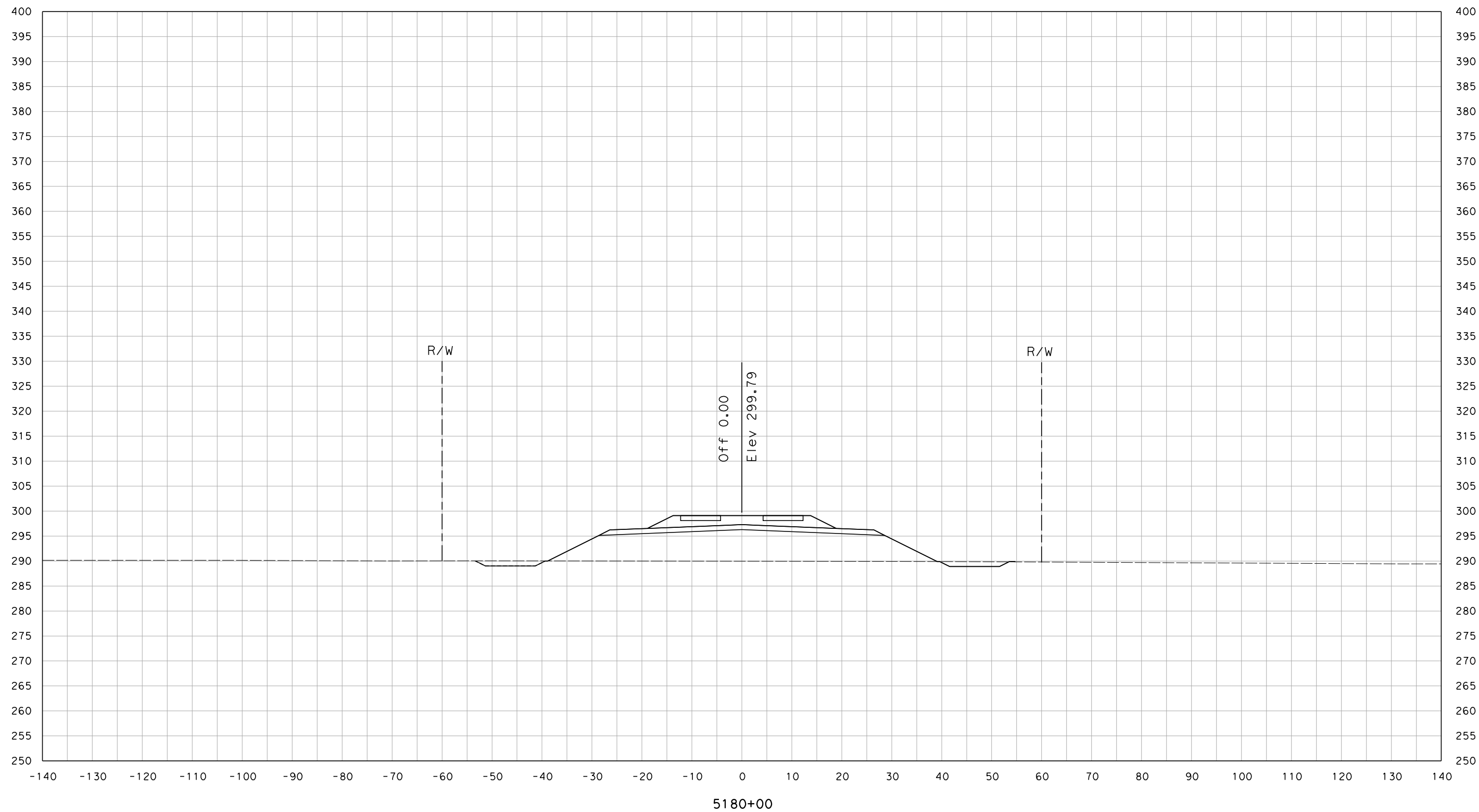
STA. 5165+00 TO STA. 5165+00



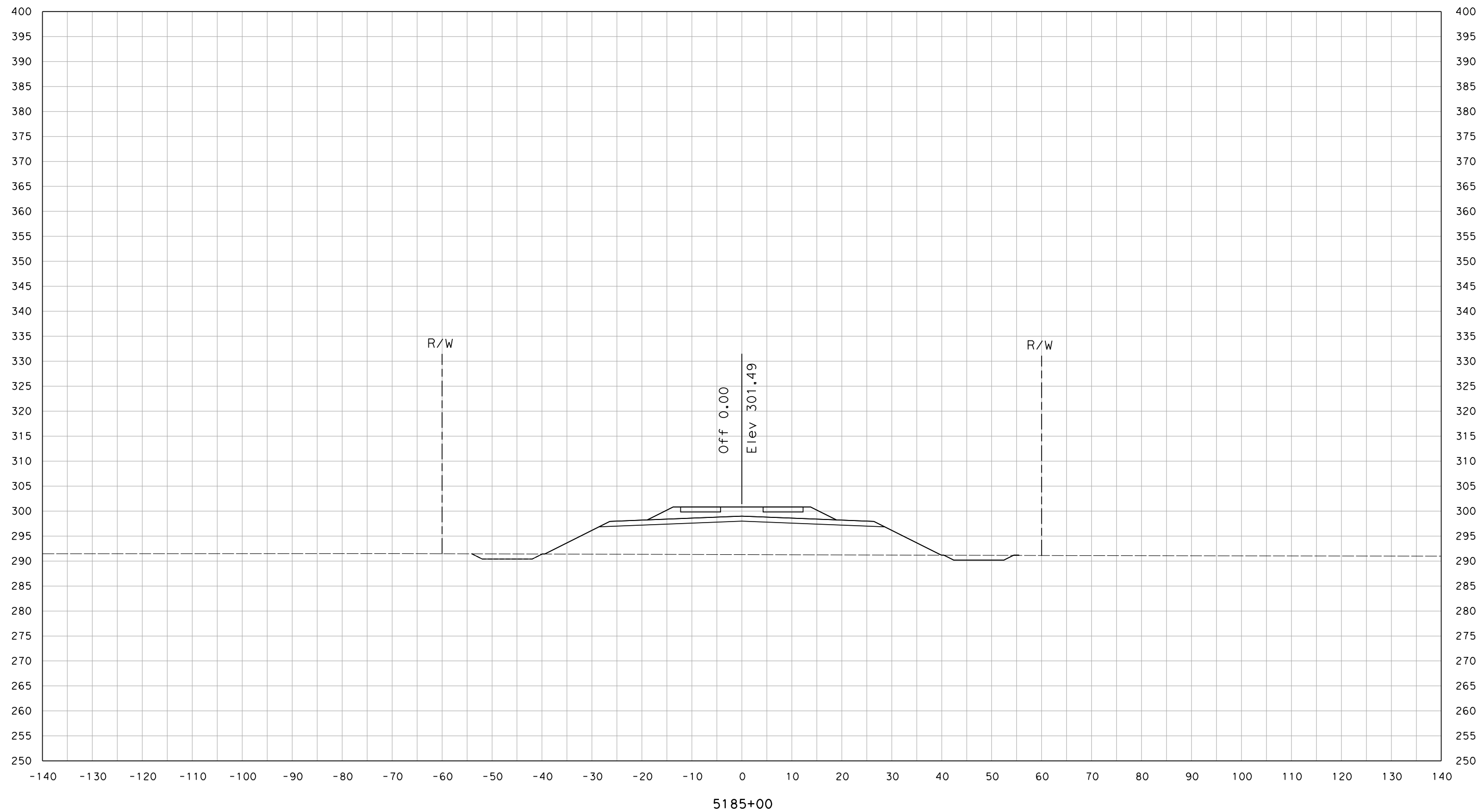
STA. 5170+00 TO STA. 5170+00



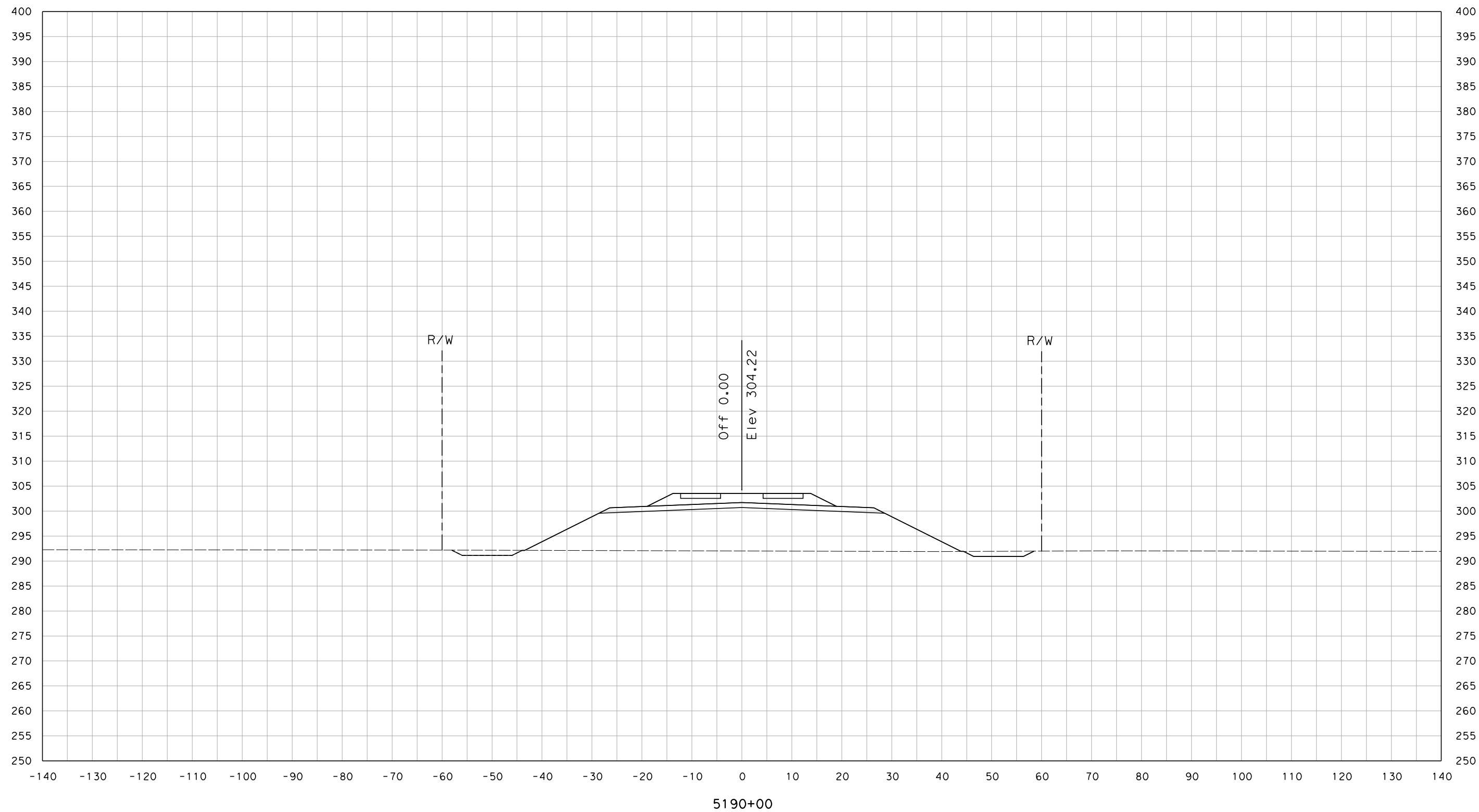
STA. 5175+00 TO STA. 5175+00



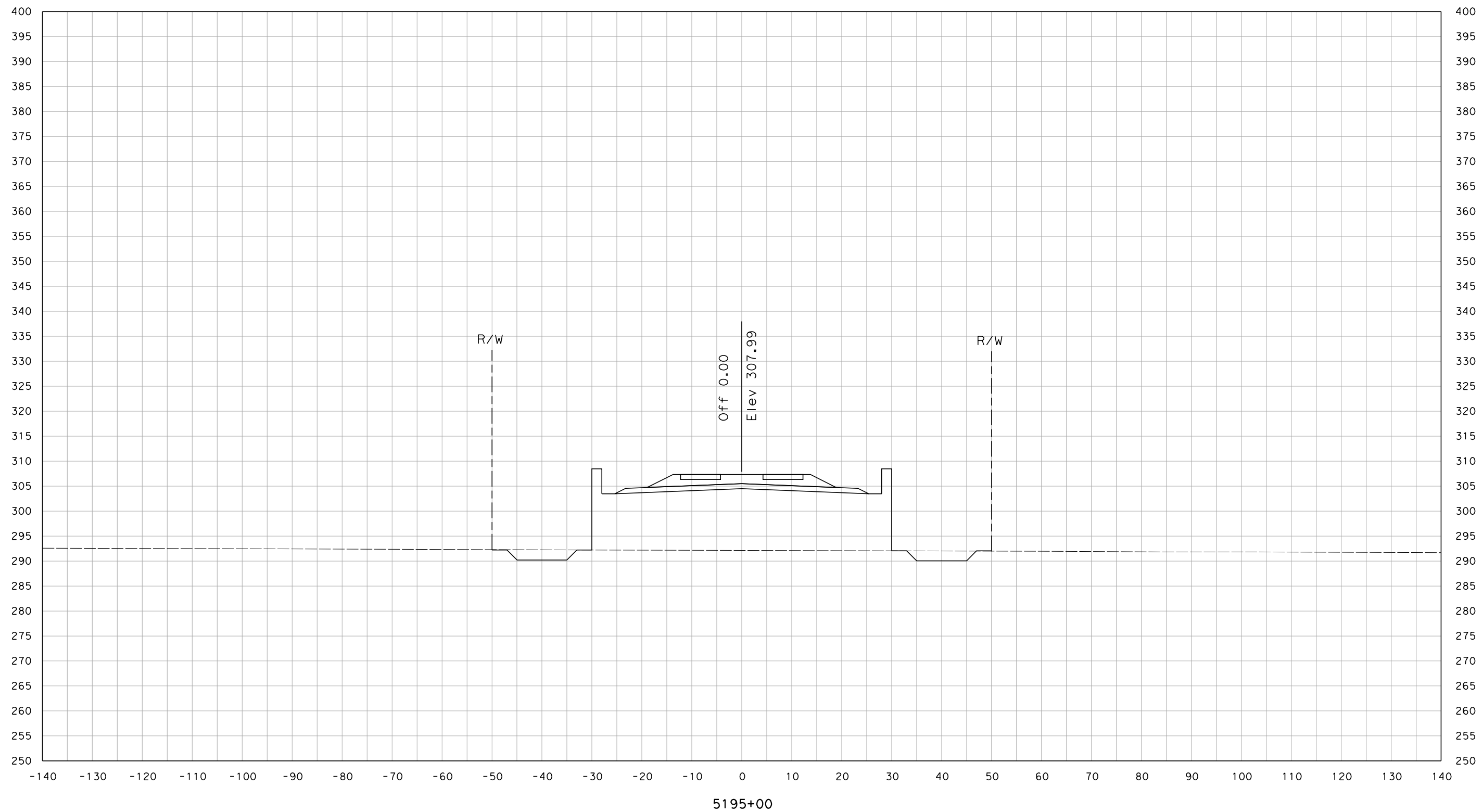
STA. 5180+00 TO STA. 5180+00



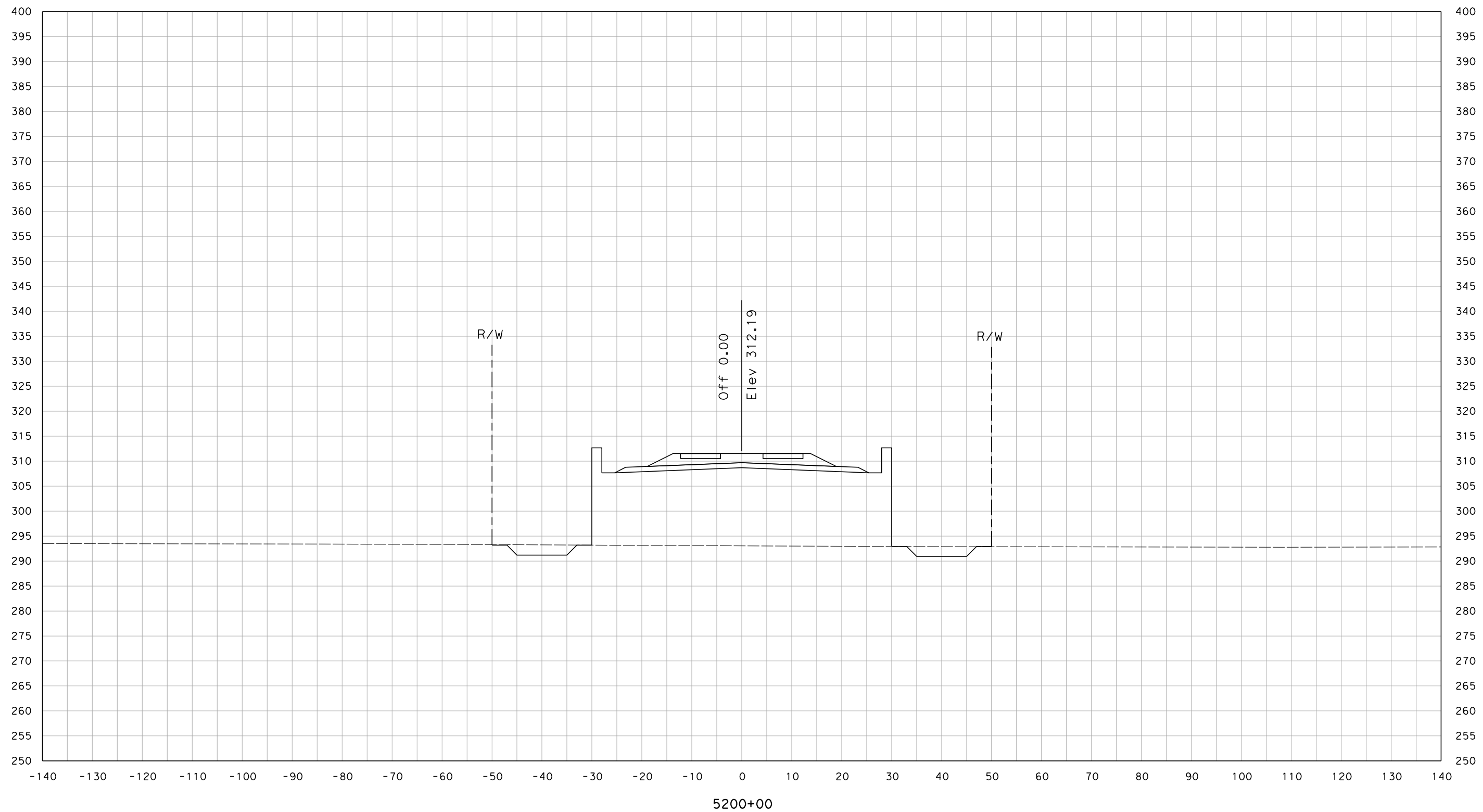
STA. 5185+00 TO STA. 5185+00



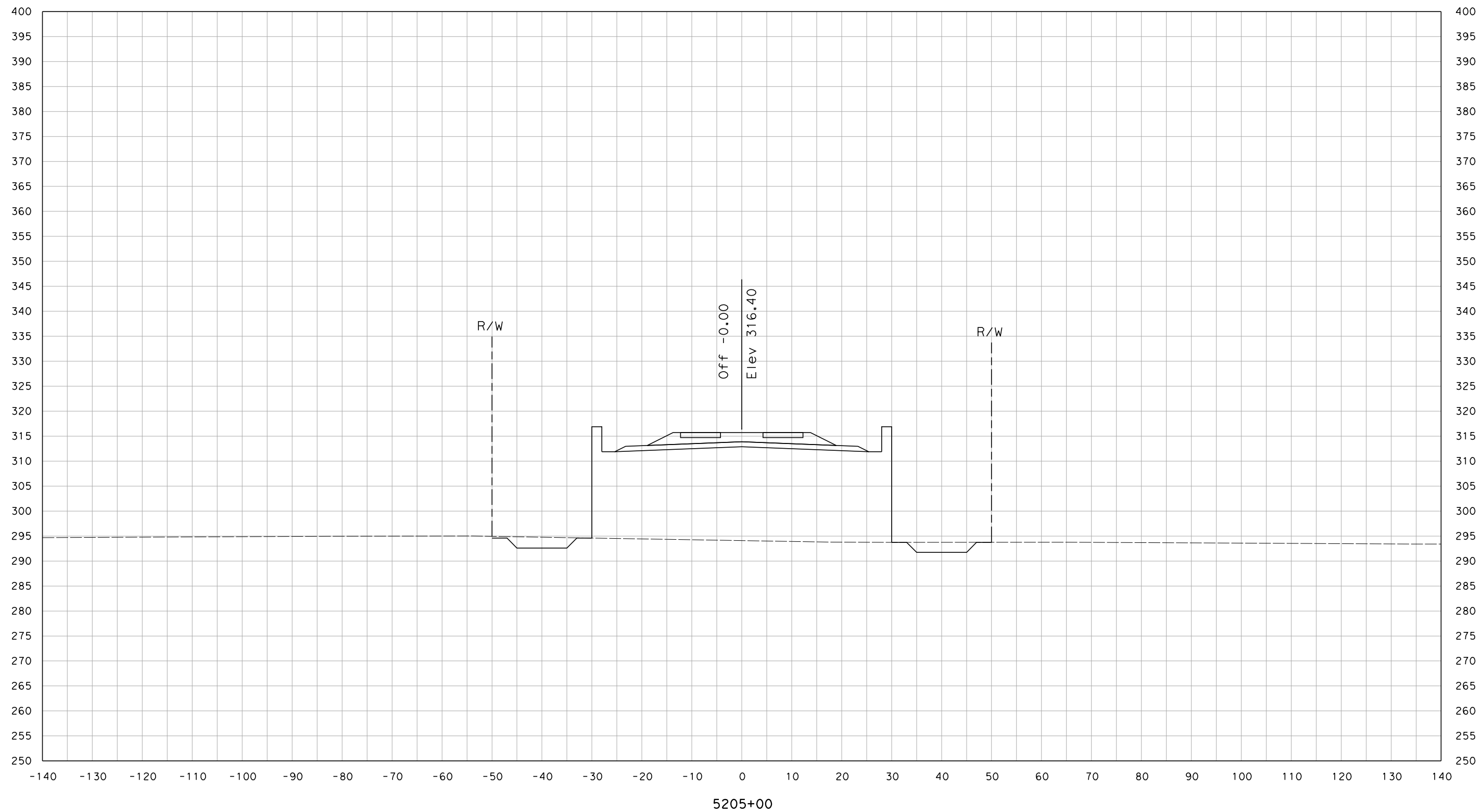
STA. 5190+00 TO STA. 5190+00



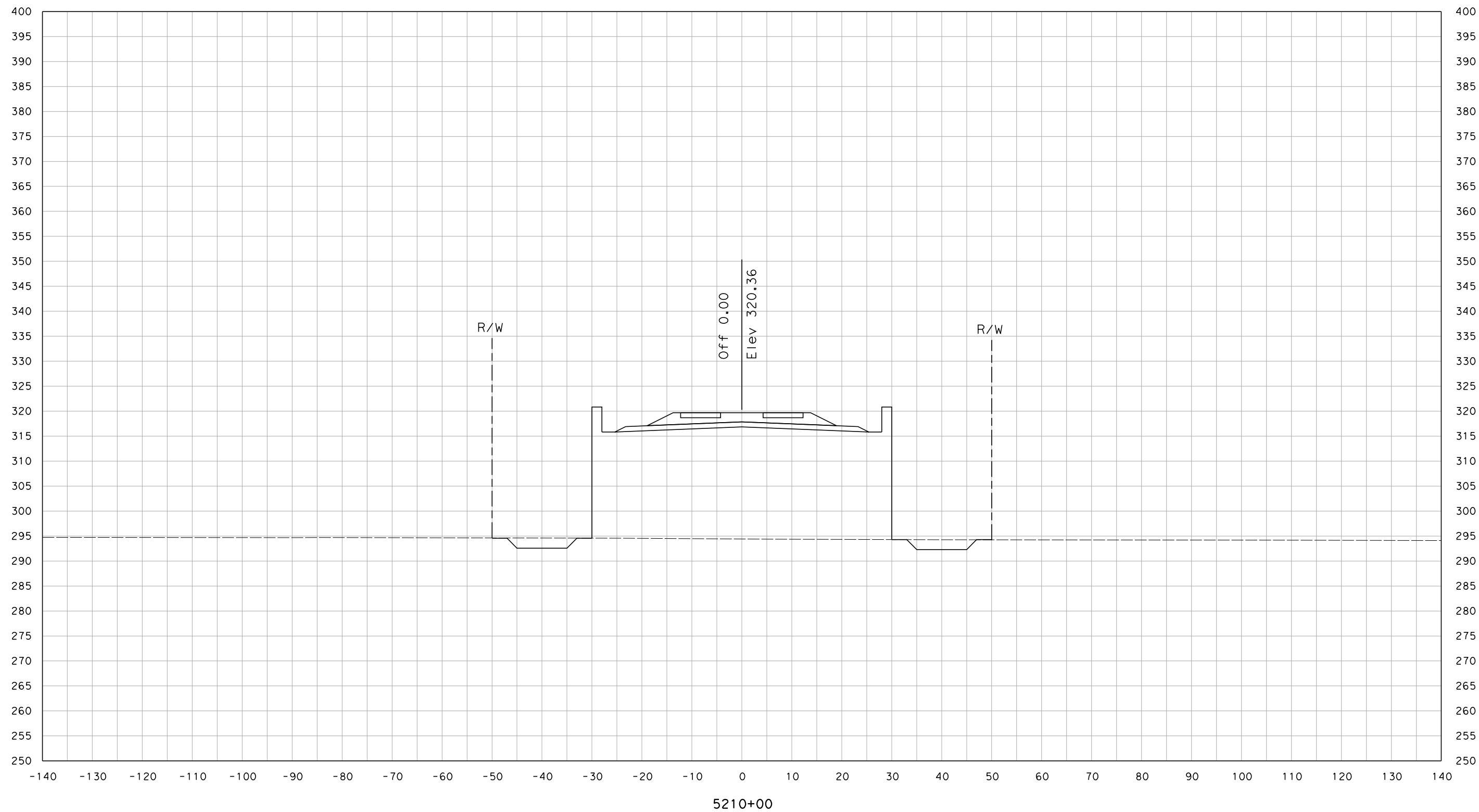
STA. 5195+00 TO STA. 5195+00



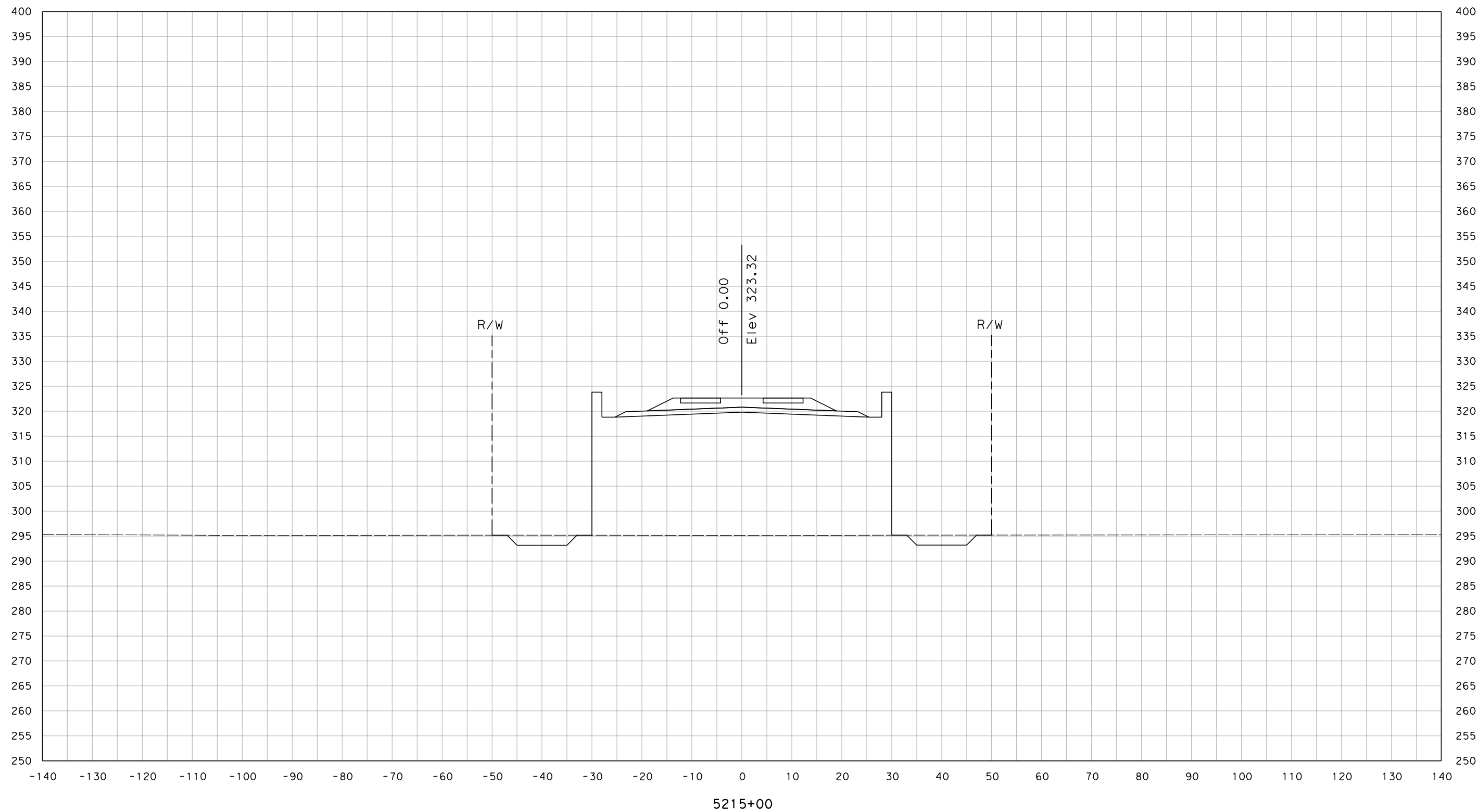
STA. 5200+00 TO STA. 5200+00



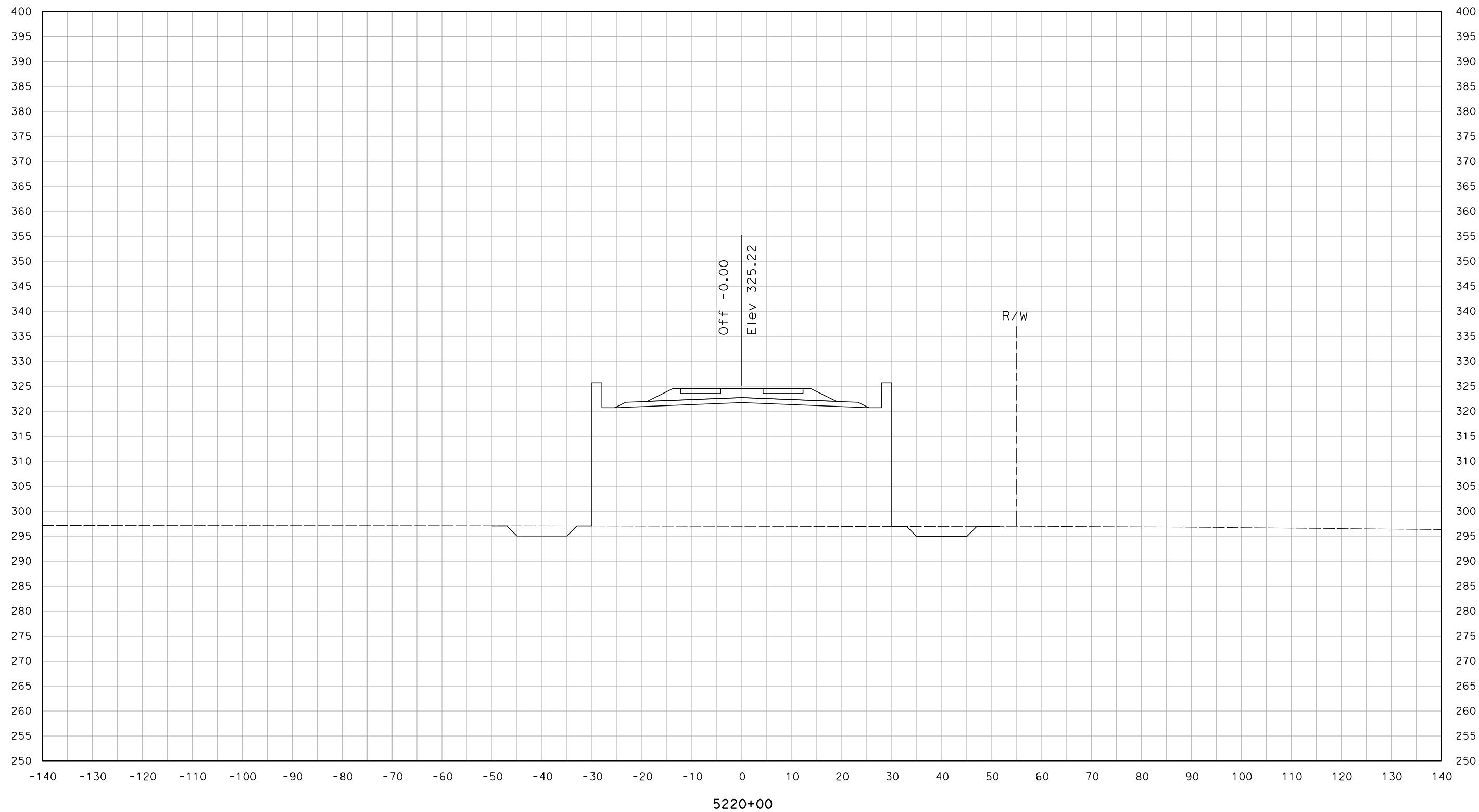
STA. 5205+00 TO STA. 5205+00



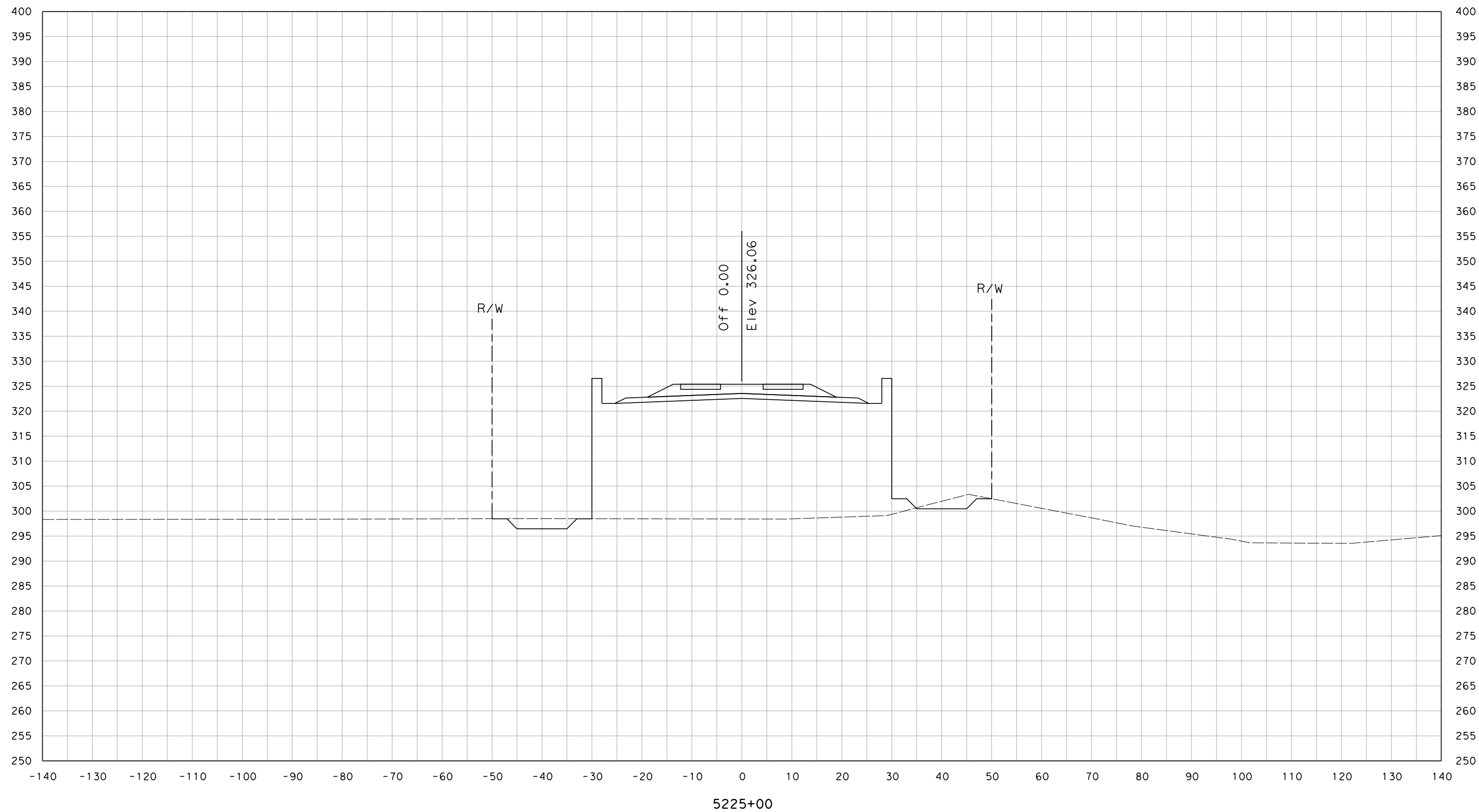
STA. 5210+00 TO STA. 5210+00



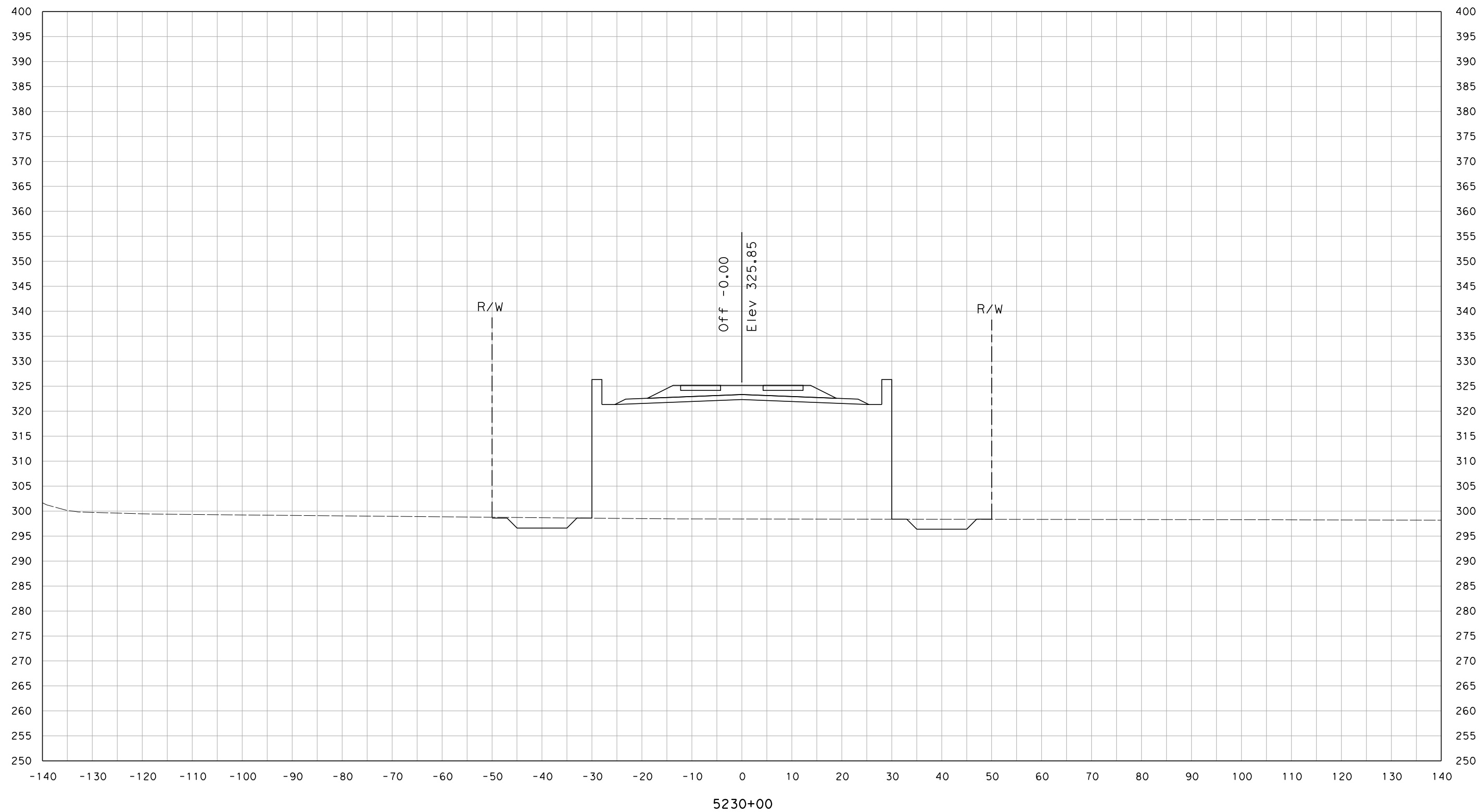
STA. 5215+00 TO STA. 5215+00



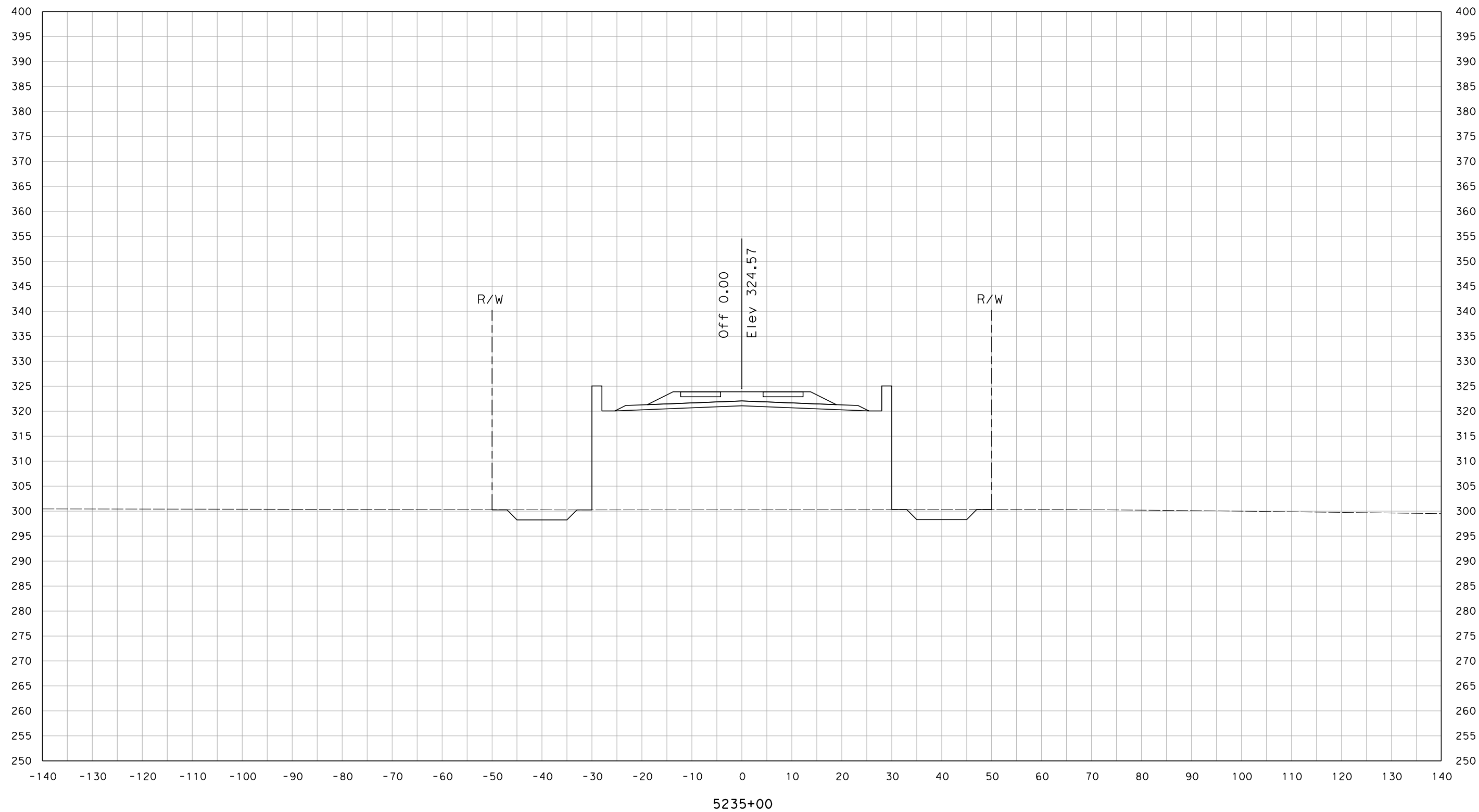
STA. 5220+00 TO STA. 5220+00



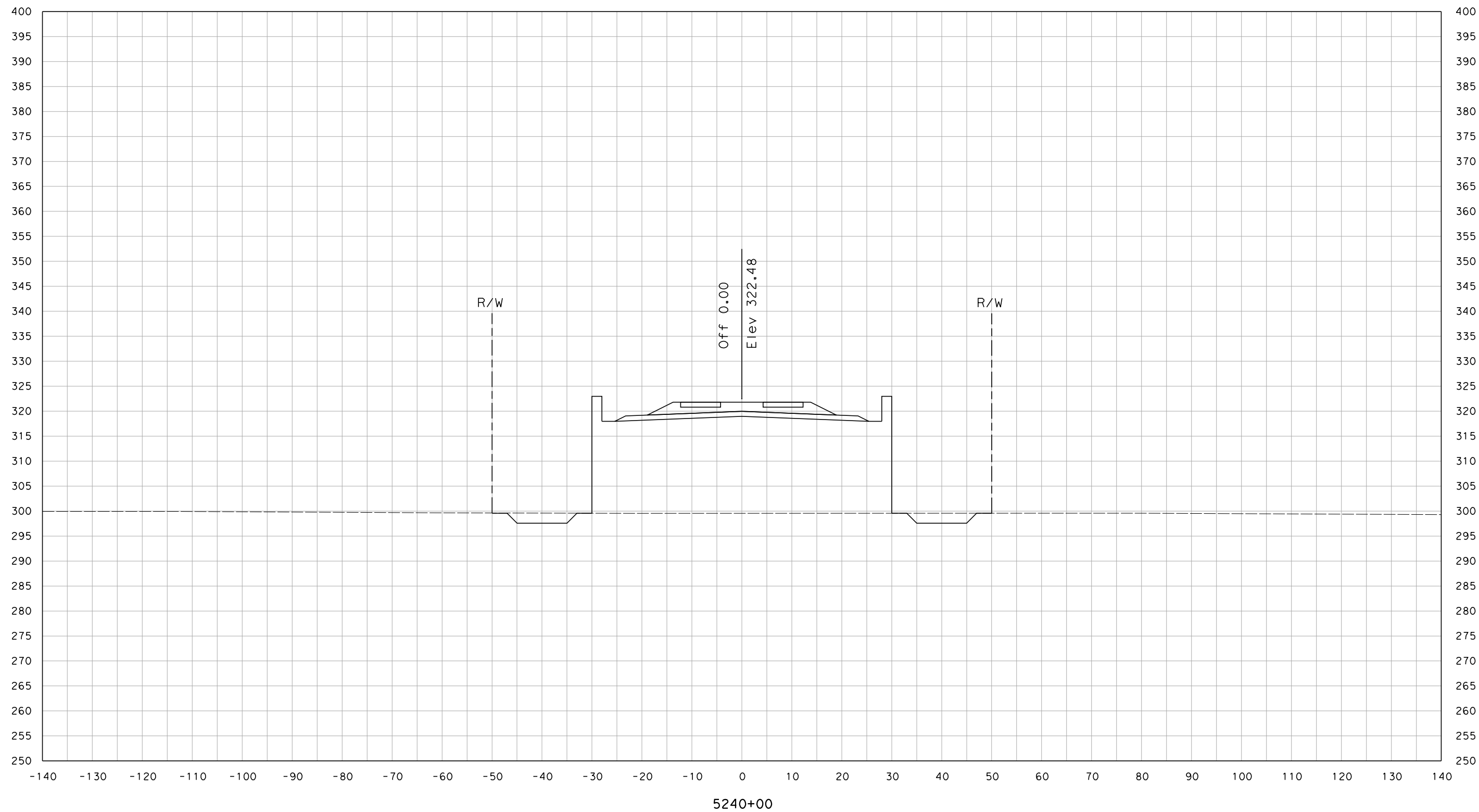
STA. 5225+00 TO STA. 5225+00



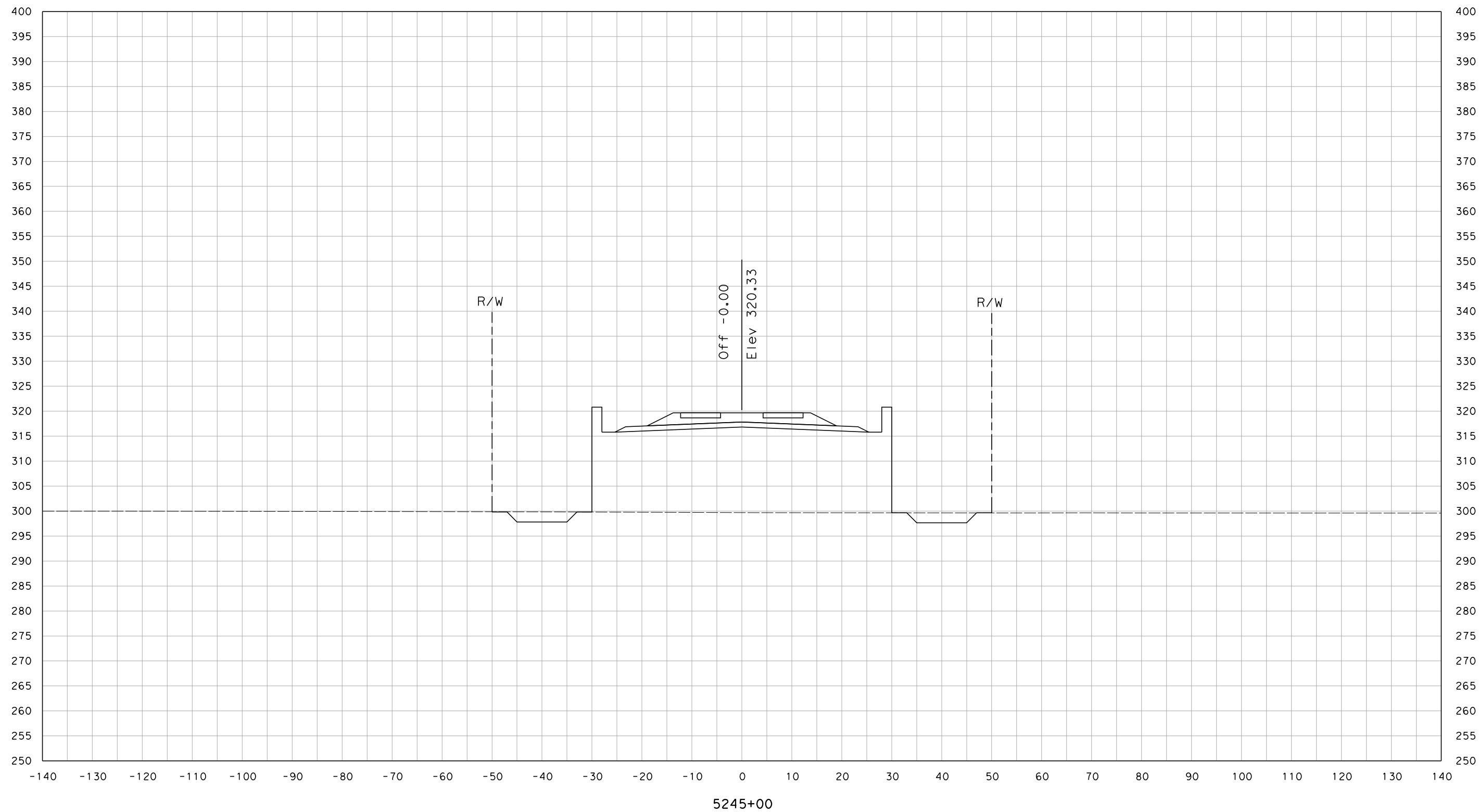
STA. 5230+00 TO STA. 5230+00



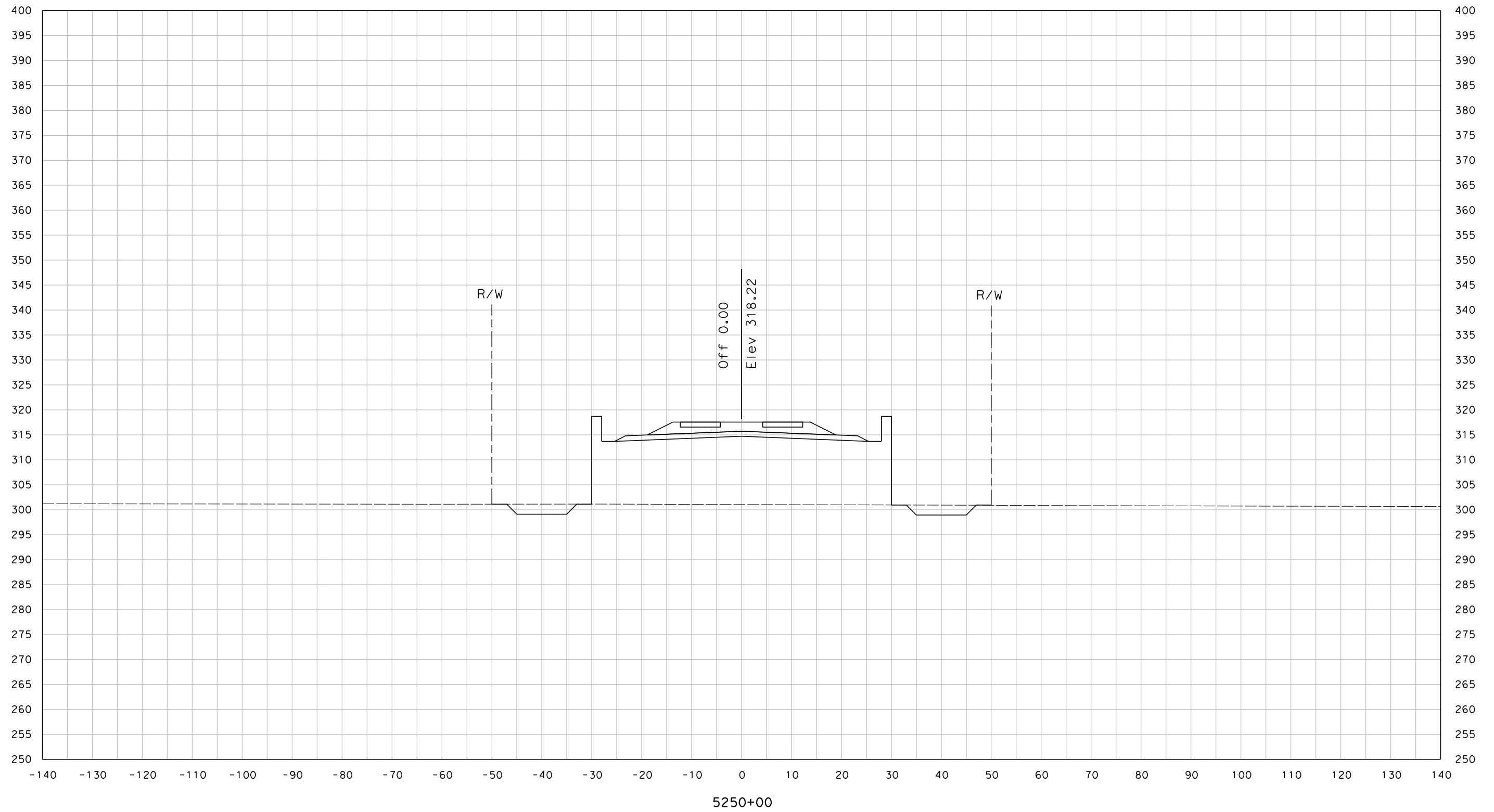
STA. 5235+00 TO STA. 5235+00



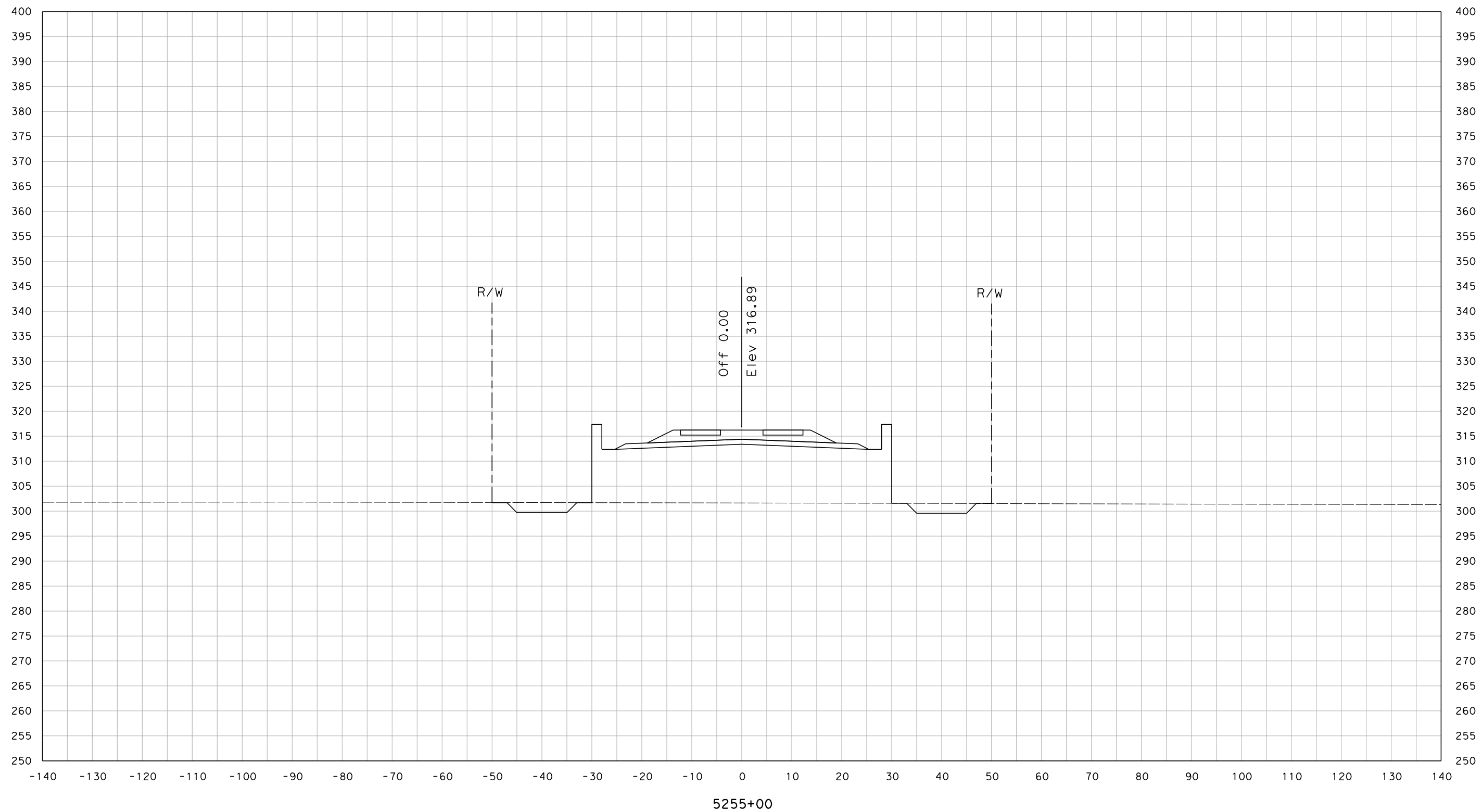
STA. 5240+00 TO STA. 5240+00



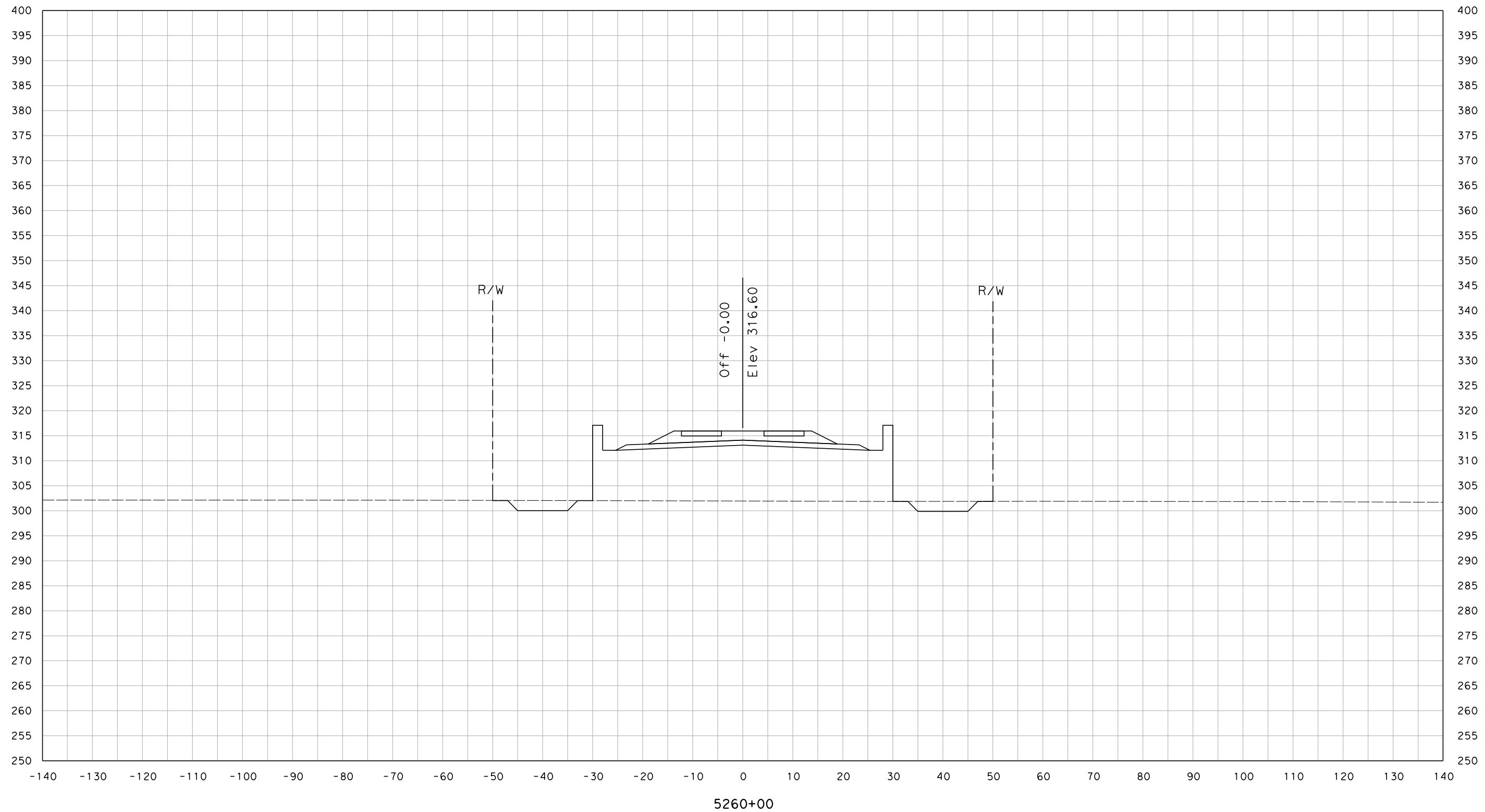
STA. 5245+00 TO STA. 5245+00



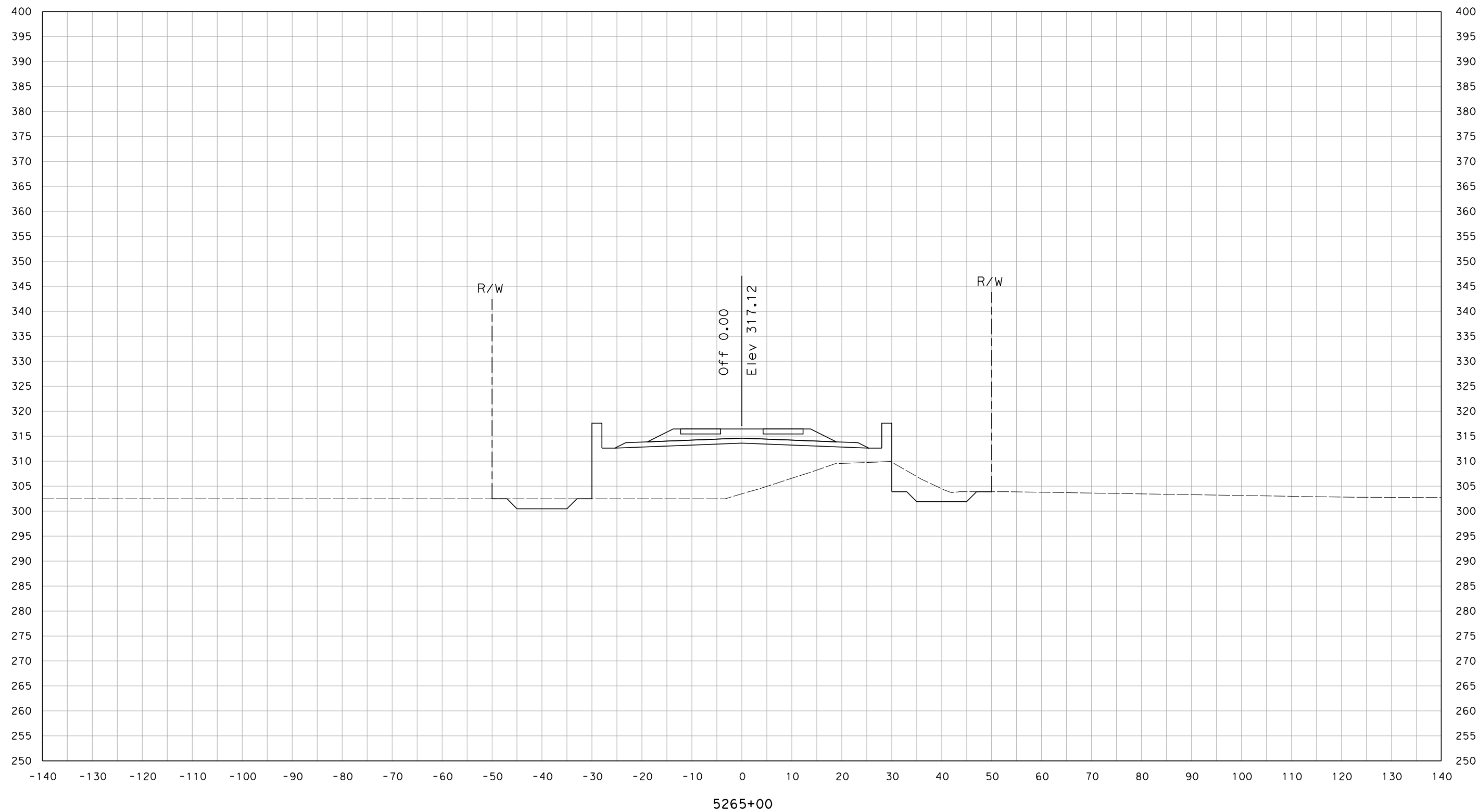
STA. 5250+00 TO STA. 5250+00



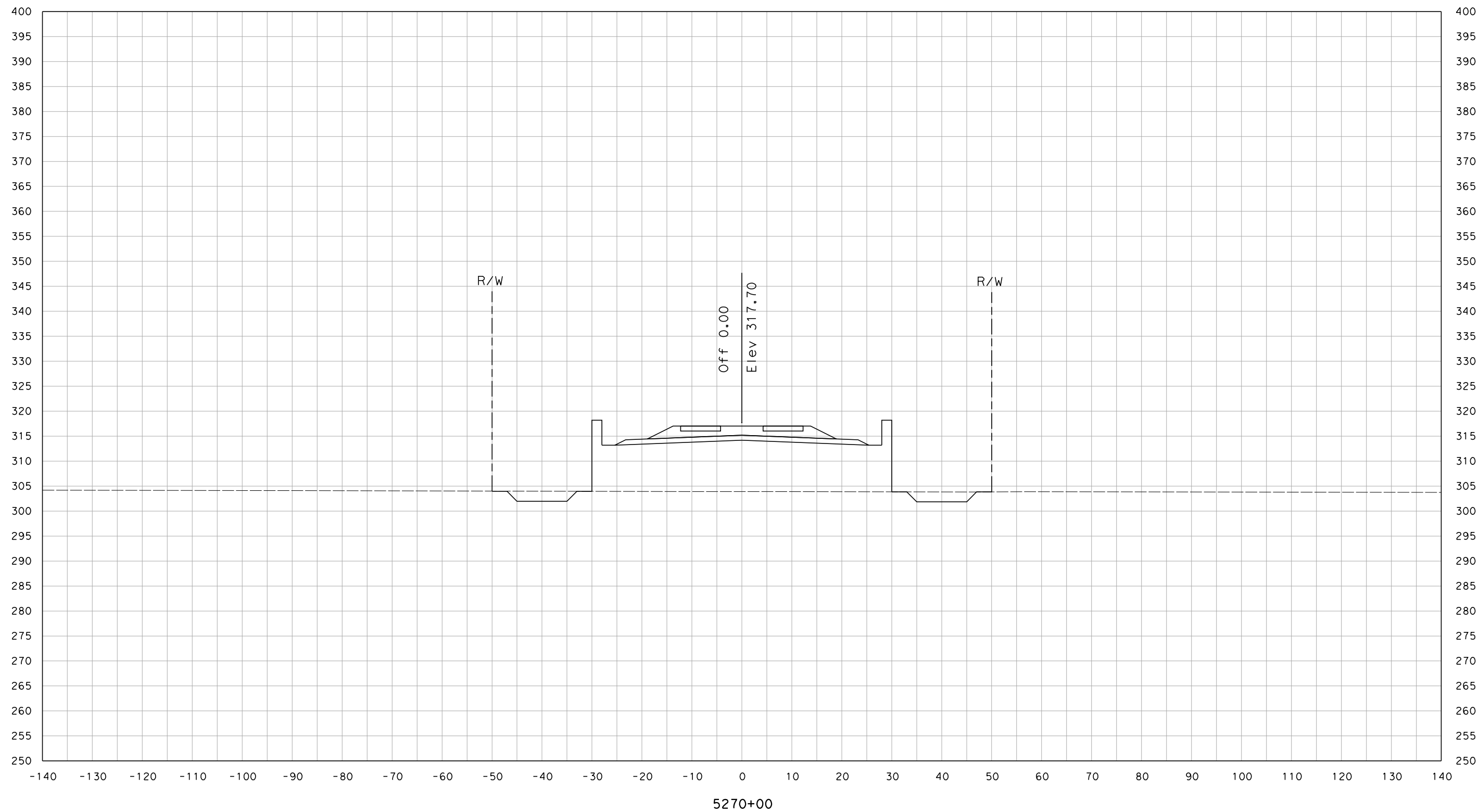
STA. 5255+00 TO STA. 5255+00



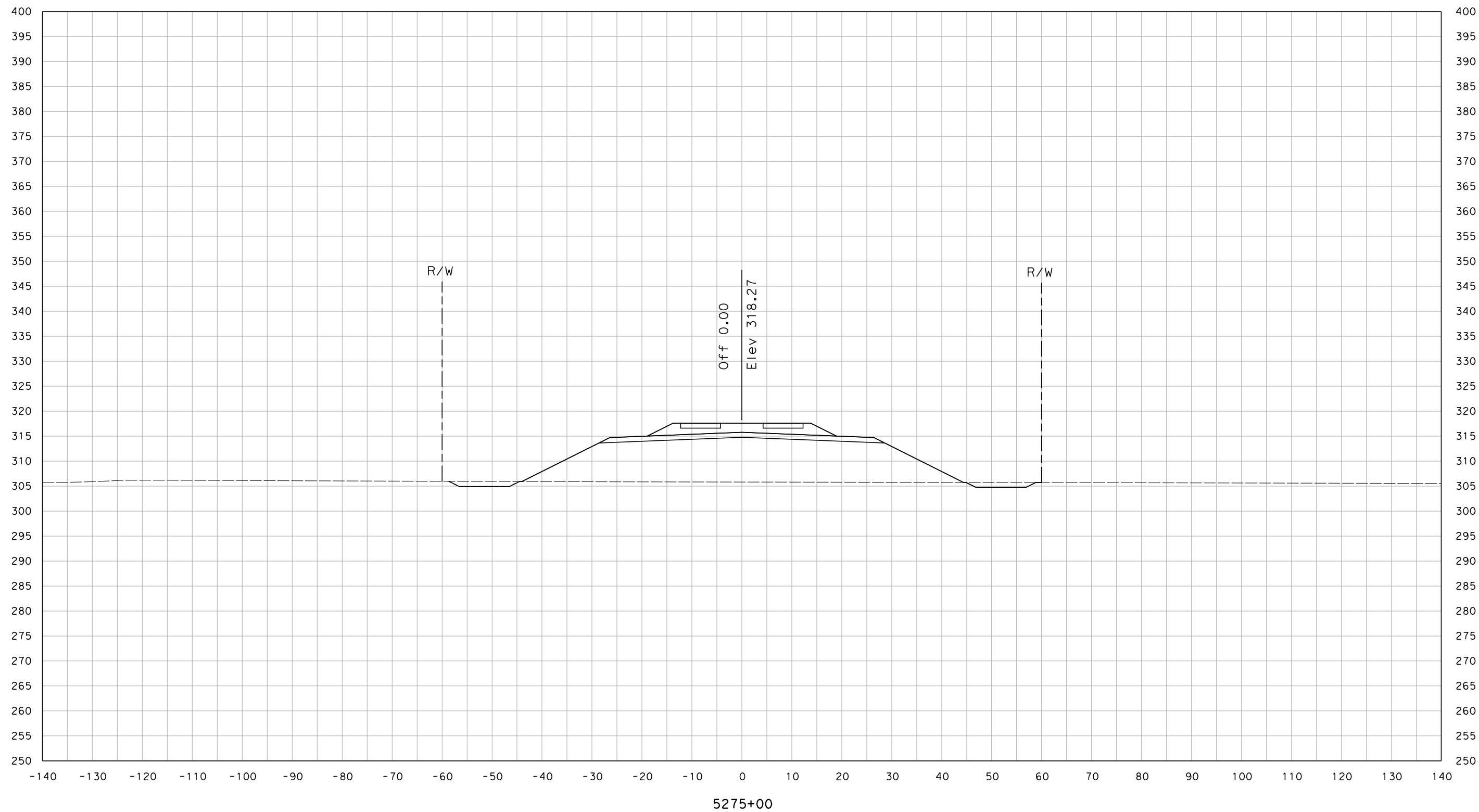
STA. 5260+00 TO STA. 5260+00



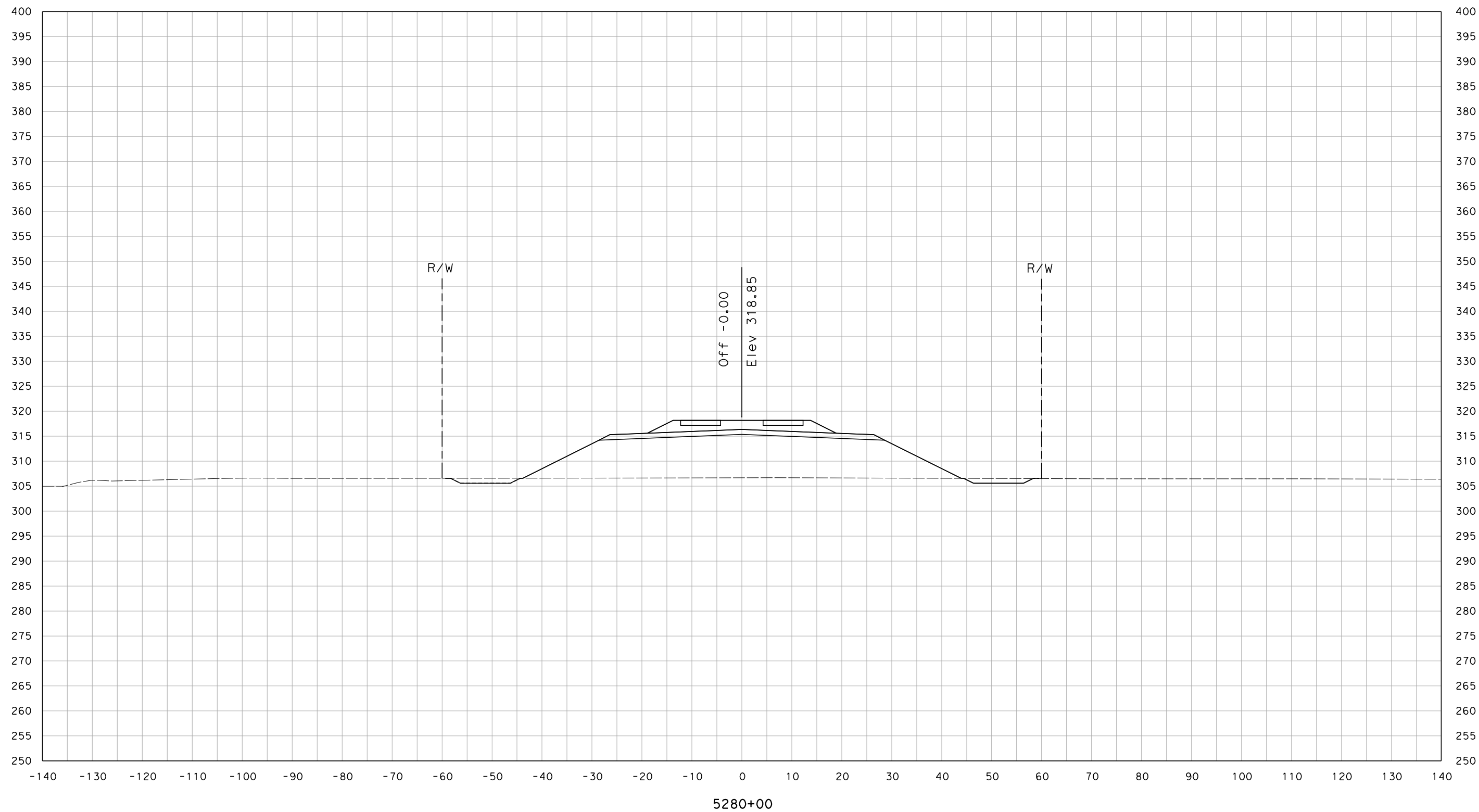
STA. 5265+00 TO STA. 5265+00



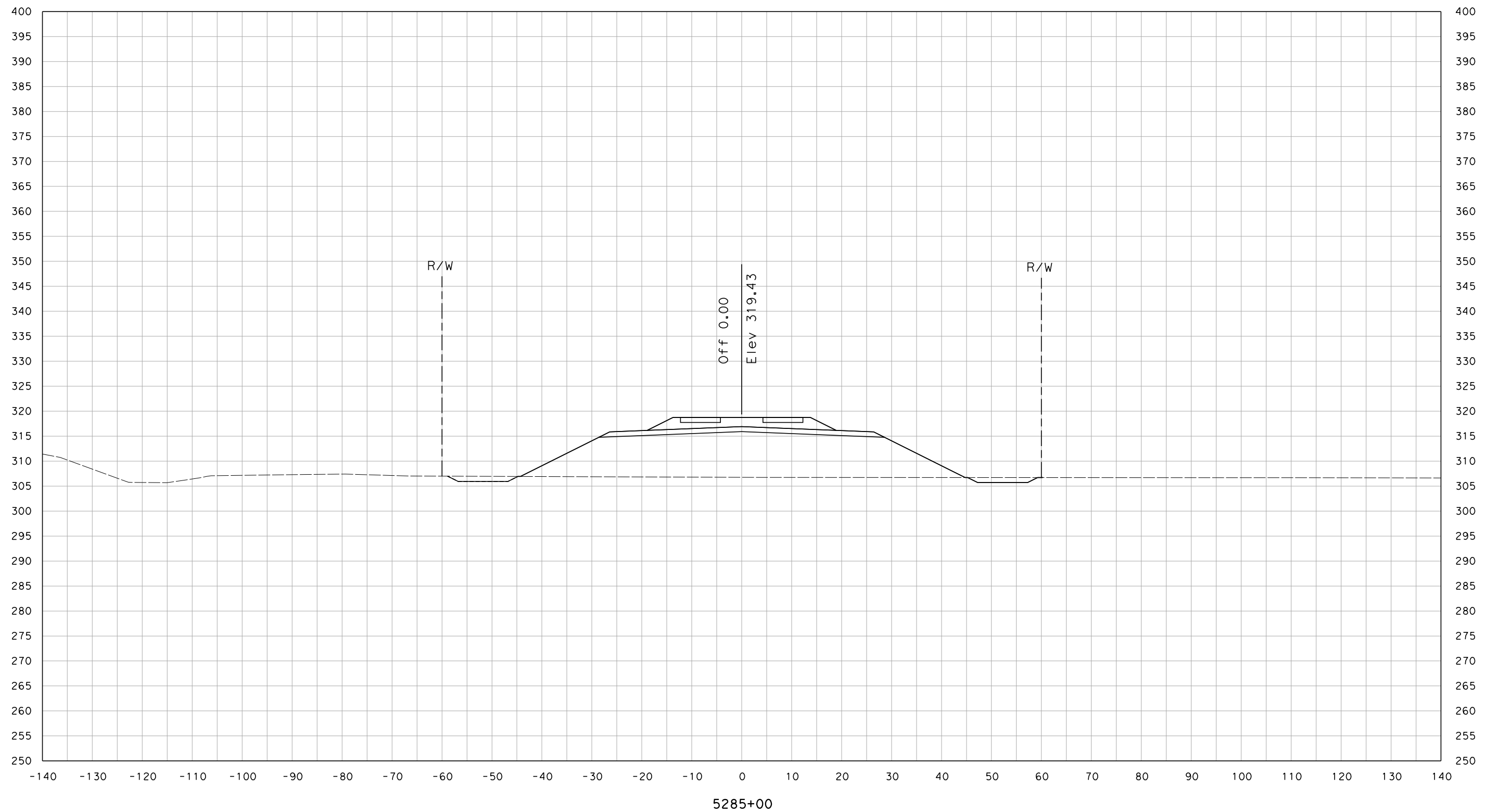
STA. 5270+00 TO STA. 5270+00



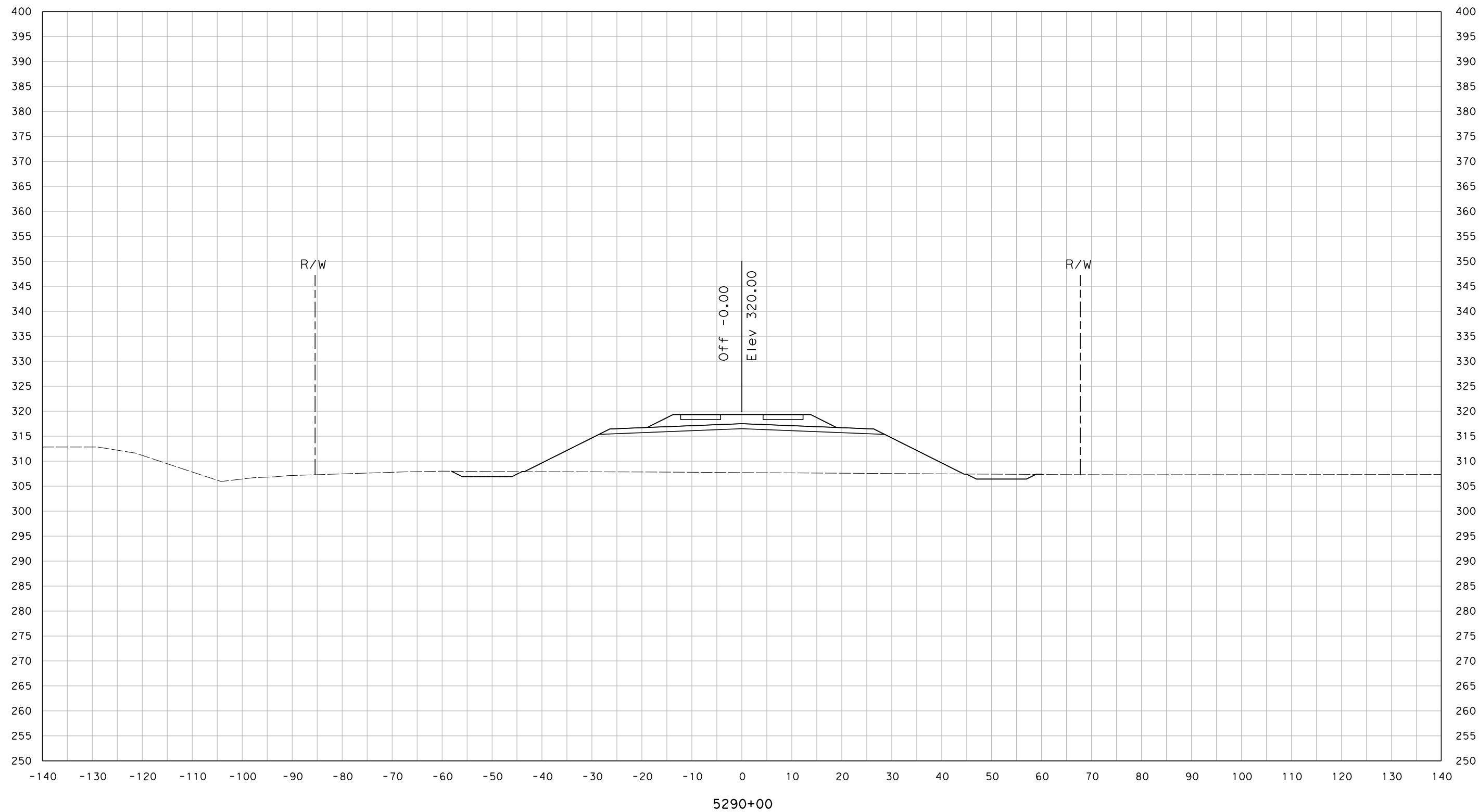
STA. 5275+00 TO STA. 5275+00



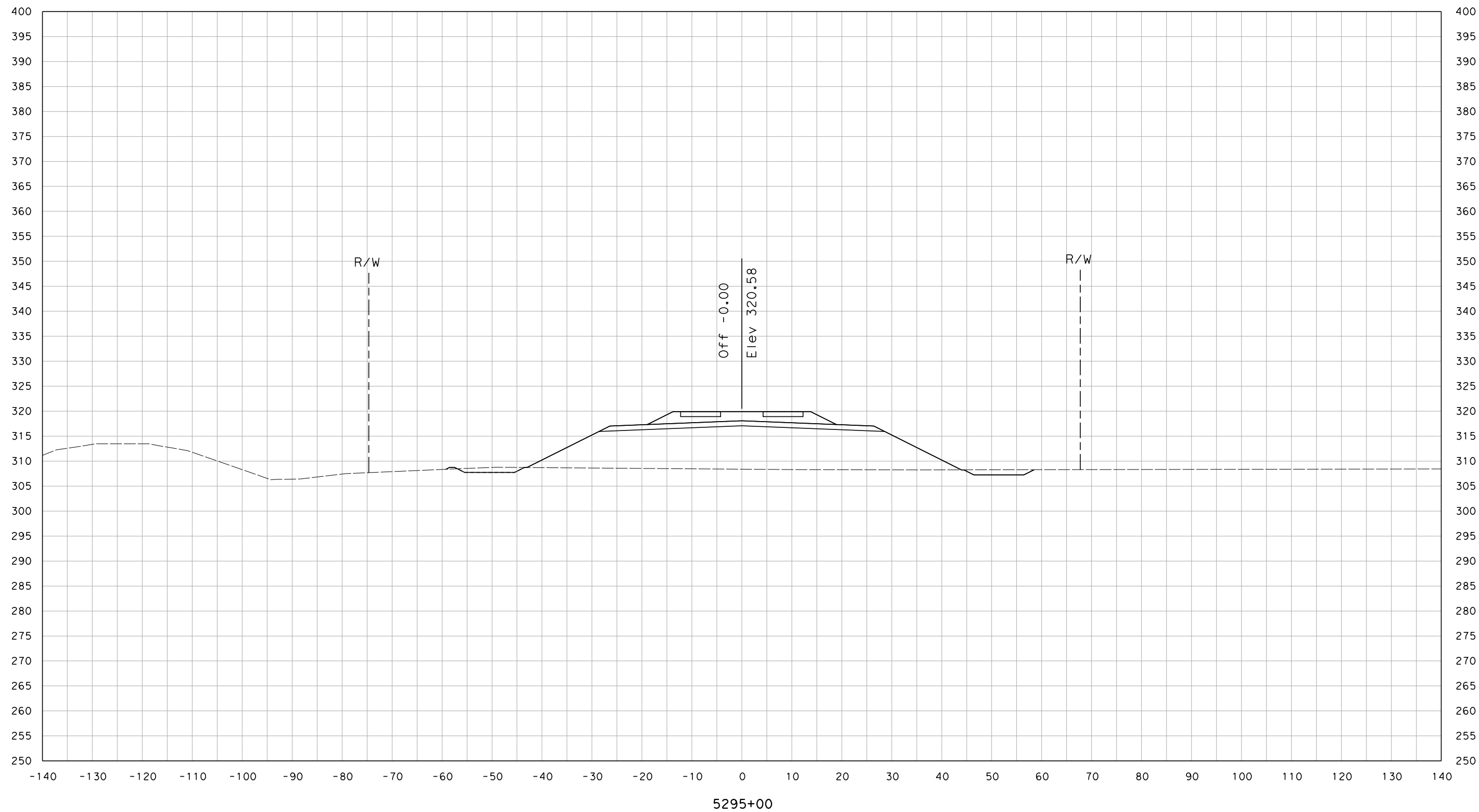
STA. 5280+00 TO STA. 5280+00



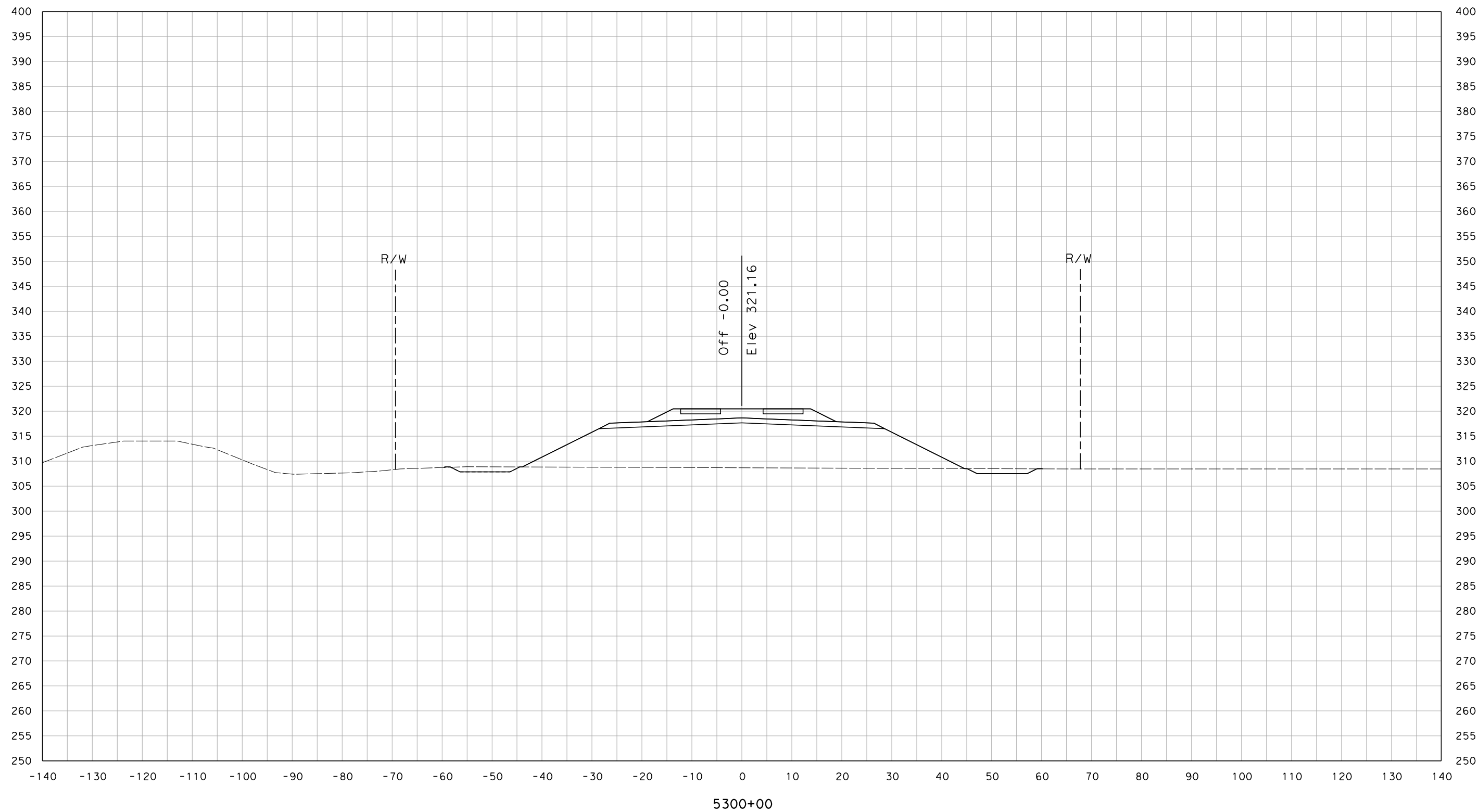
STA. 5285+00 TO STA. 5285+00



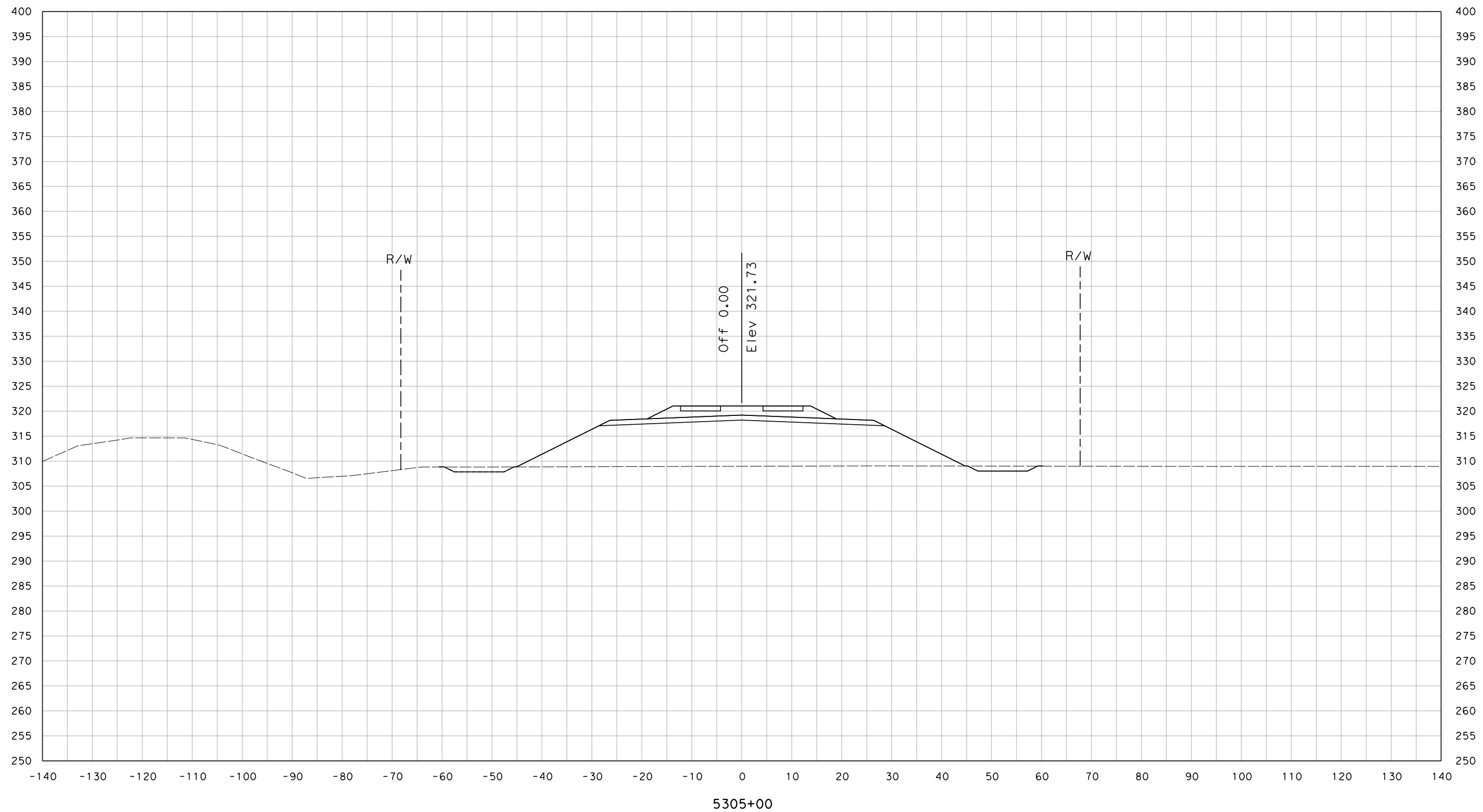
STA. 5290+00 TO STA. 5290+00



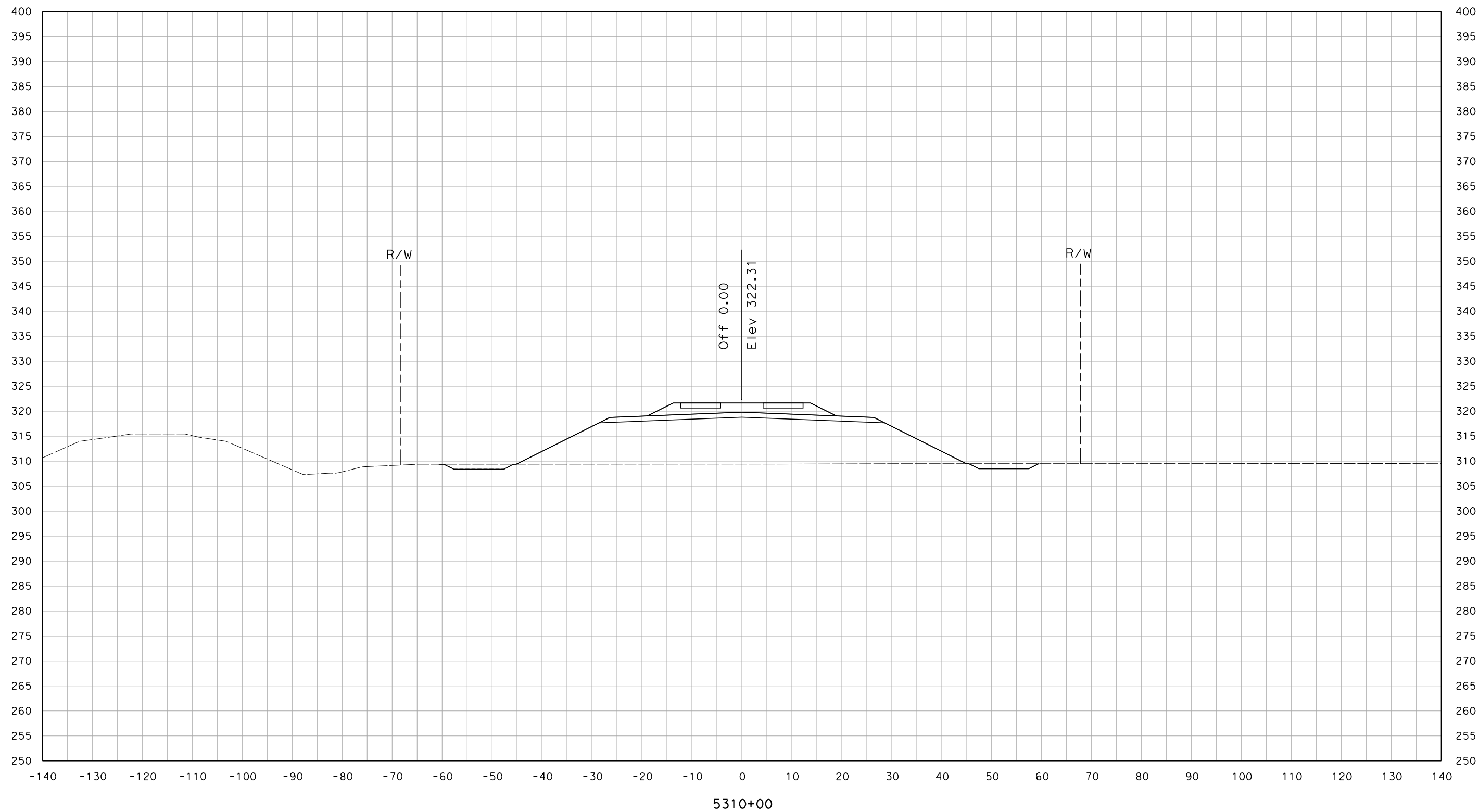
STA. 5295+00 TO STA. 5295+00



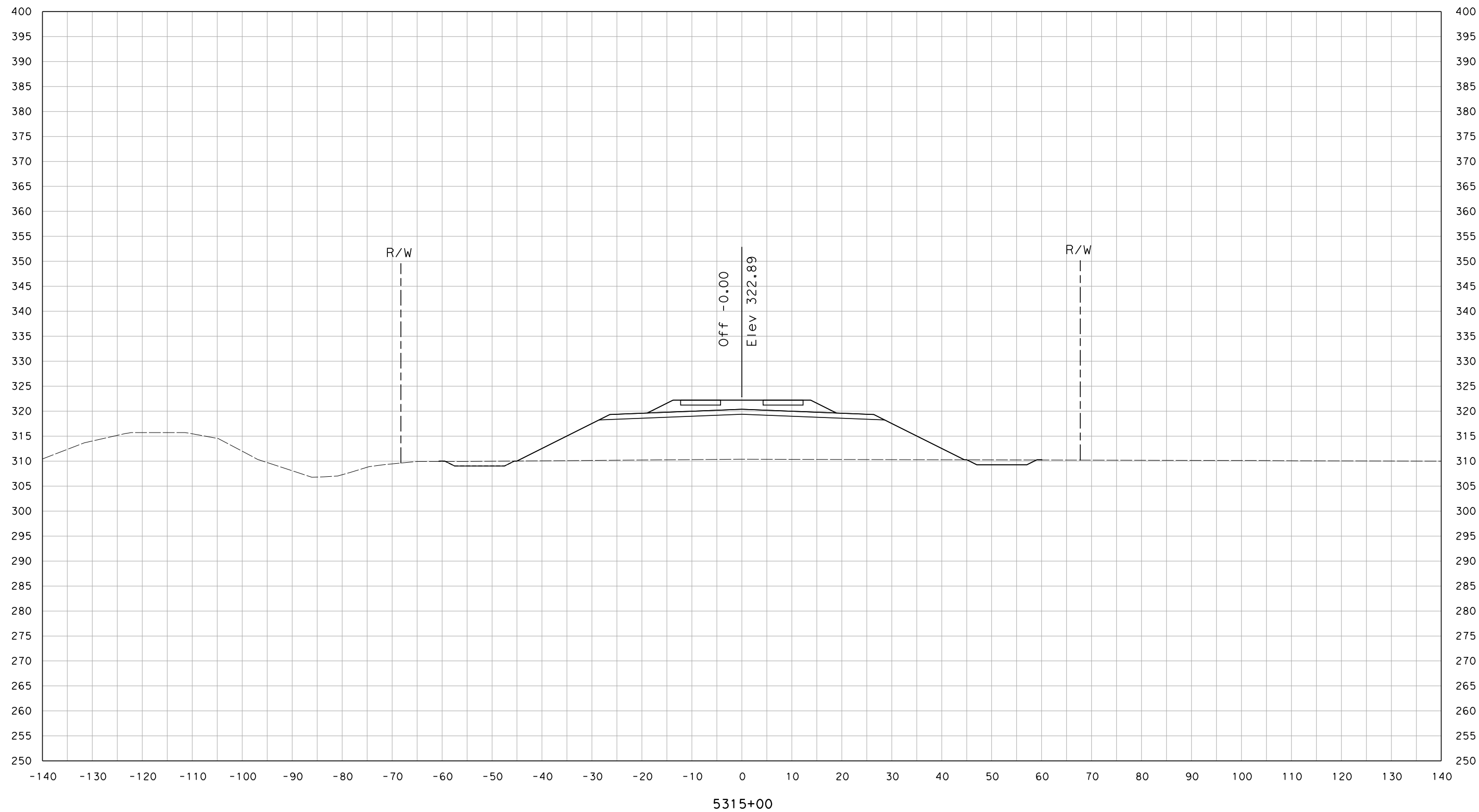
STA. 5300+00 TO STA. 5300+00



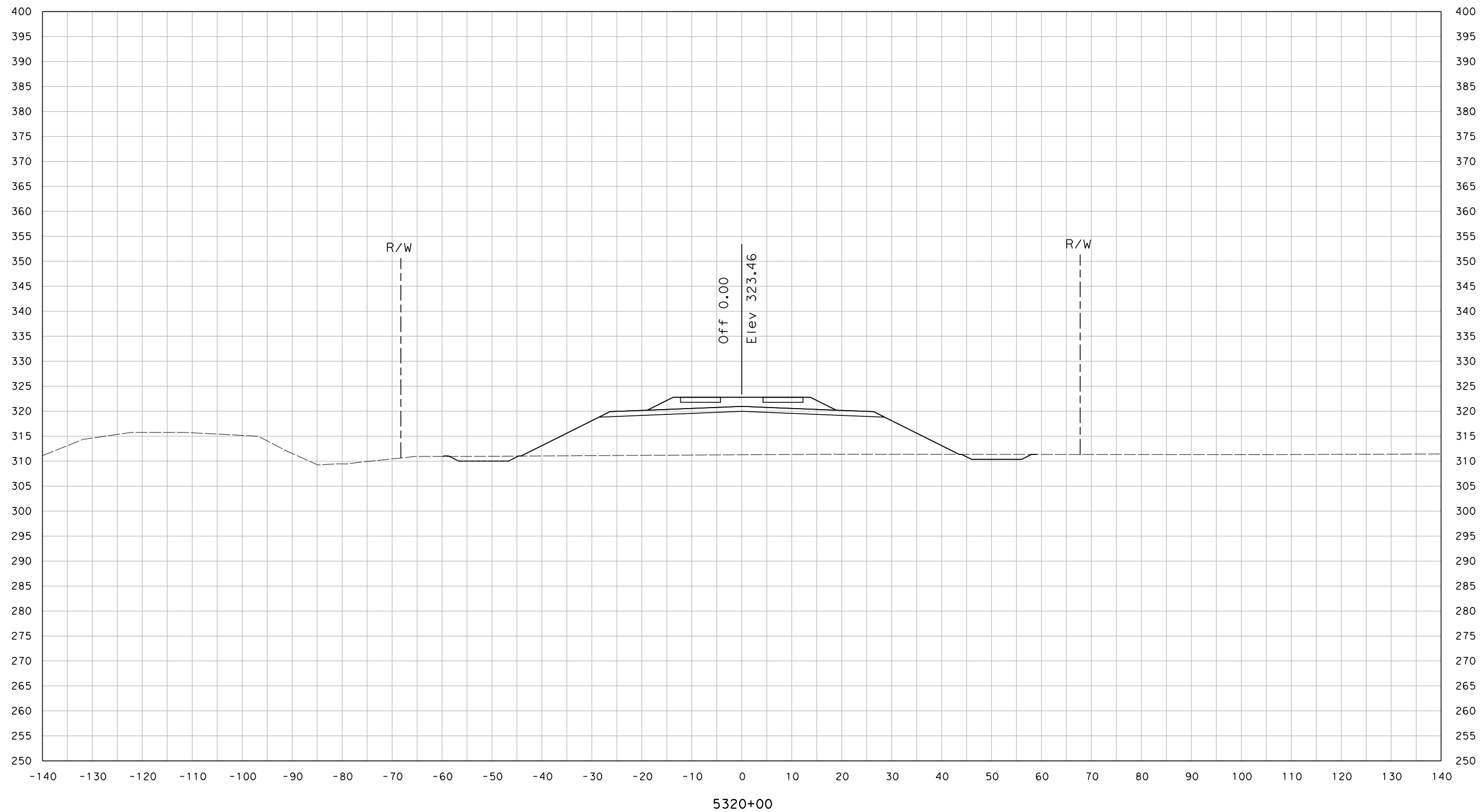
STA. 5305+00 TO STA. 5305+00



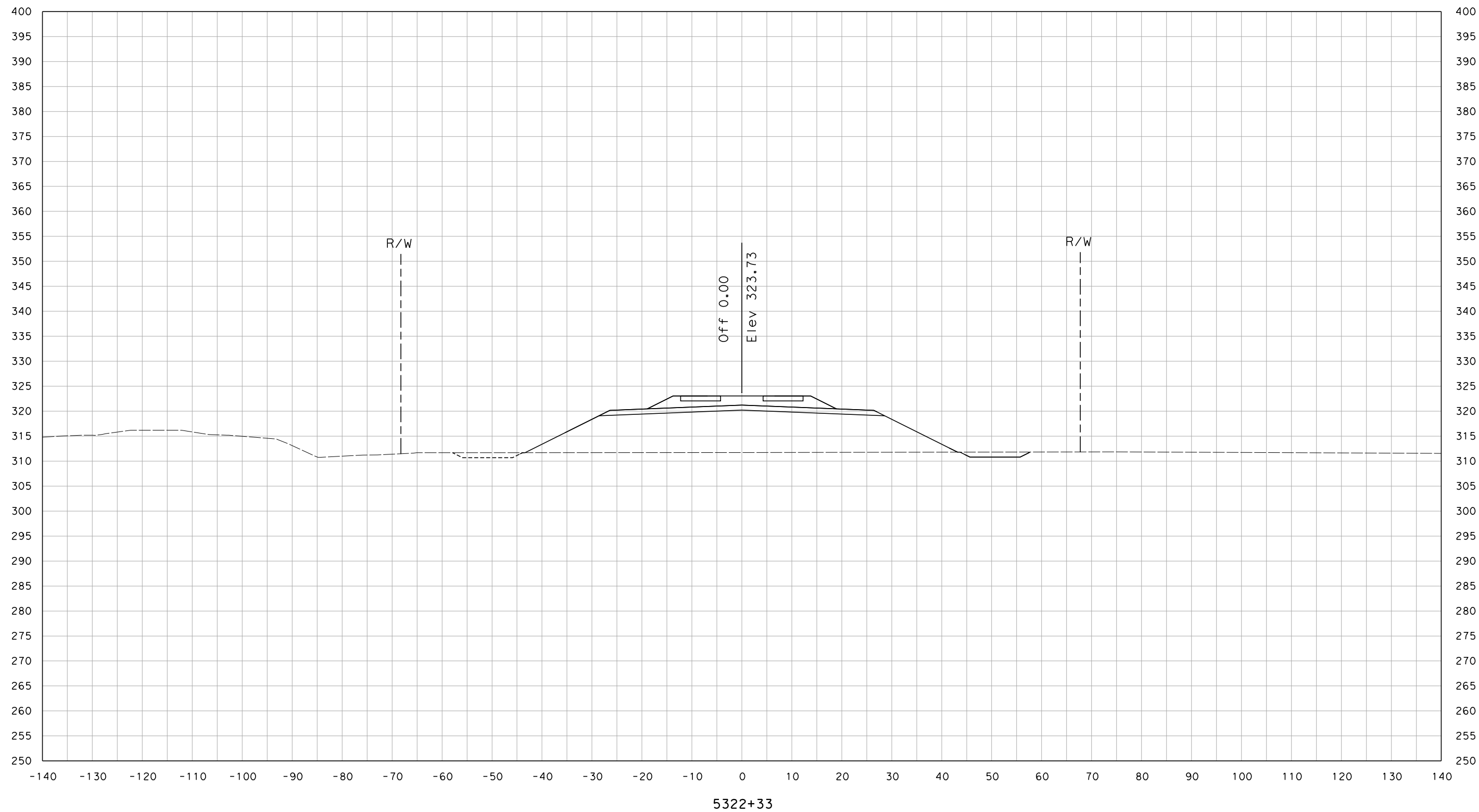
STA. 5310+00 TO STA. 5310+00



STA. 5315+00 TO STA. 5315+00



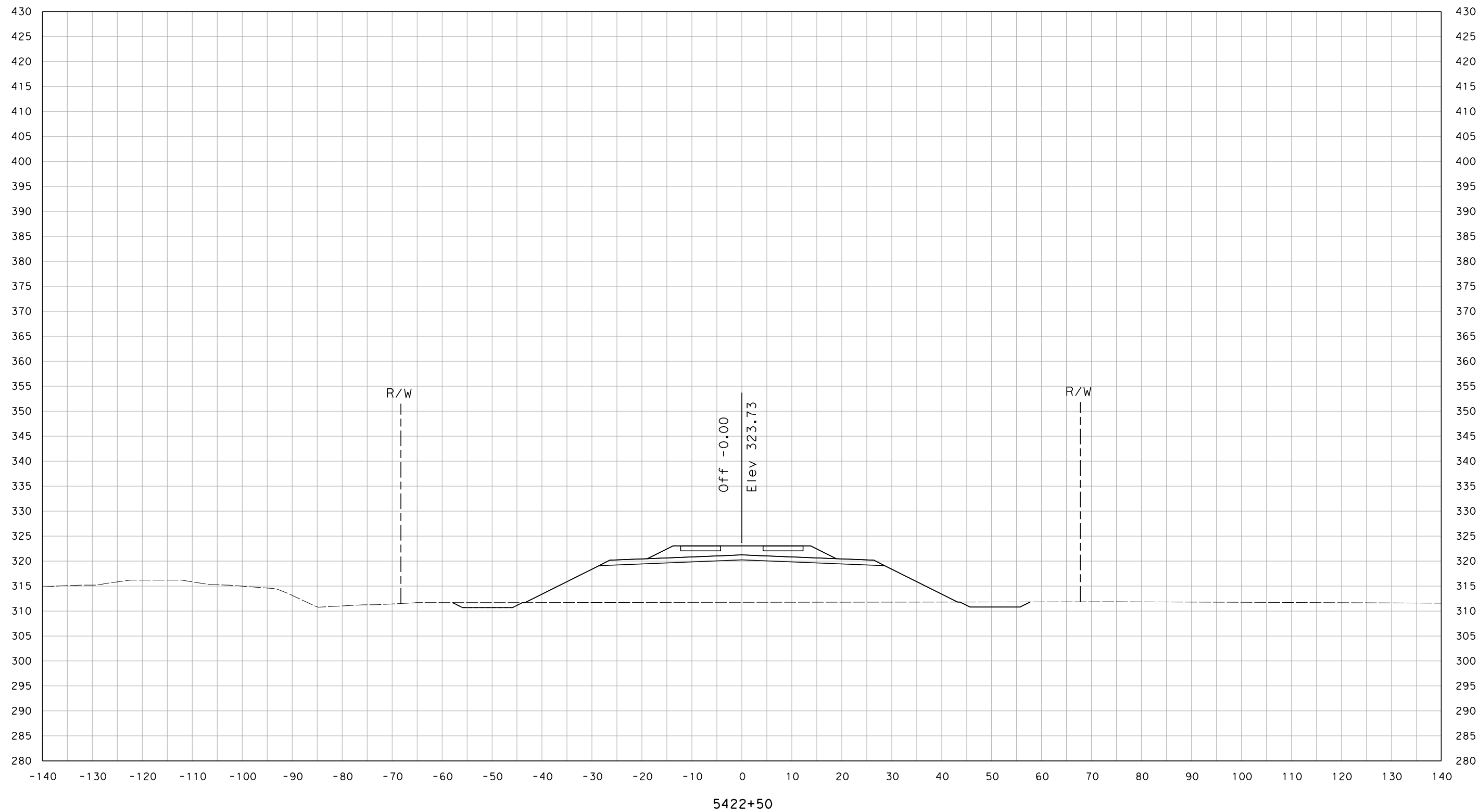
STA. 5320+00 TO STA. 5320+00



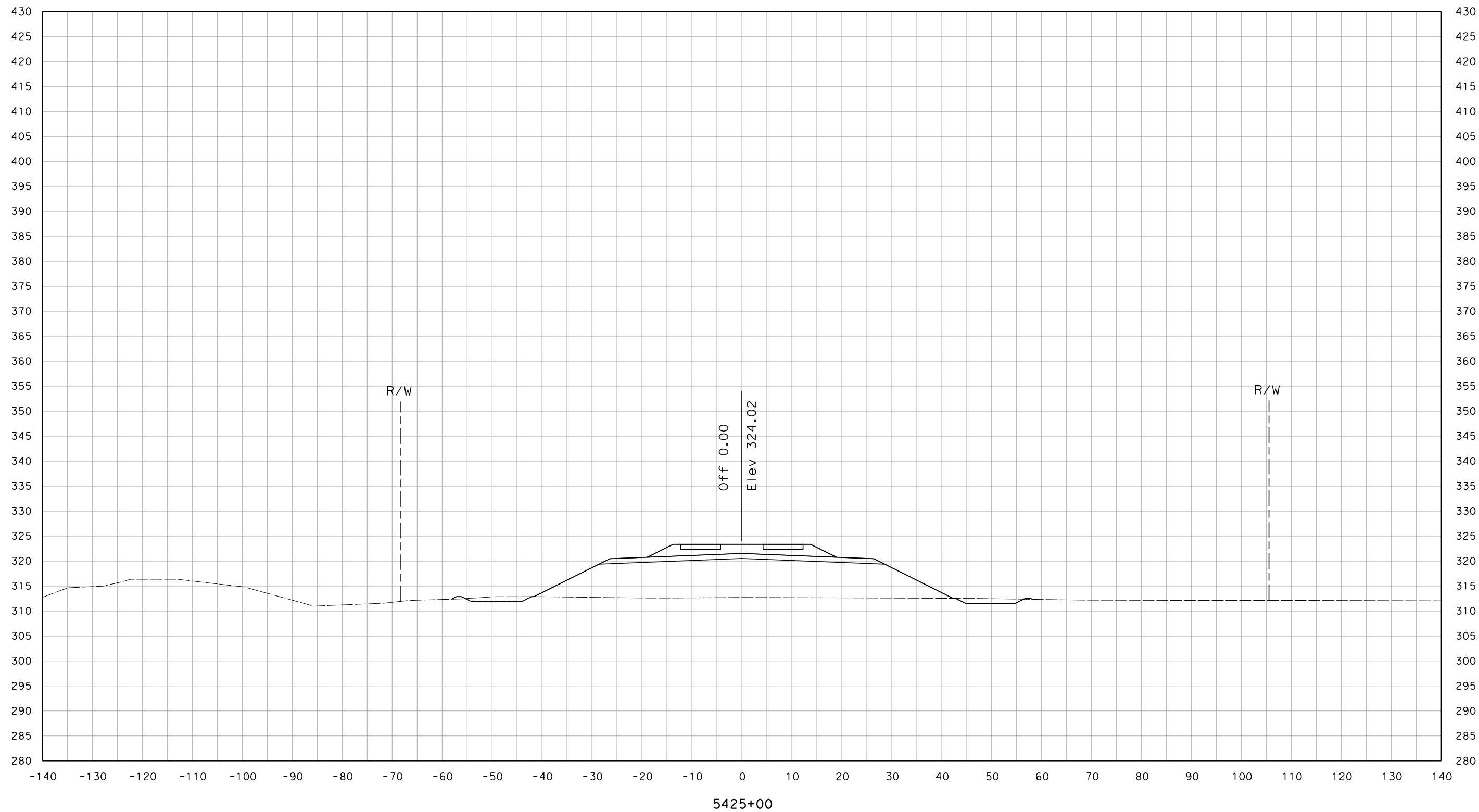
STA. 5322+33 TO STA. 5322+33

ALIGNMENT

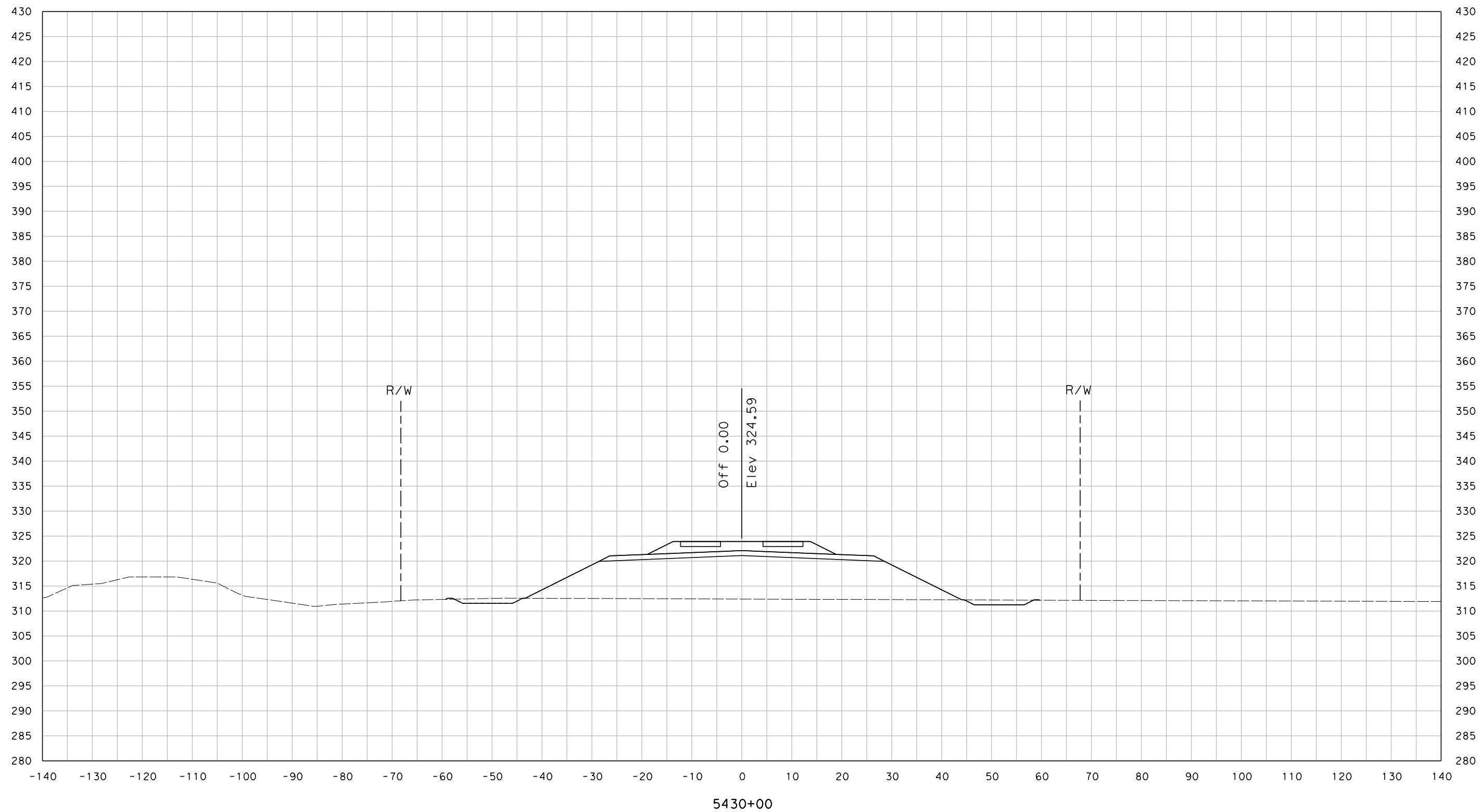
"WS1"



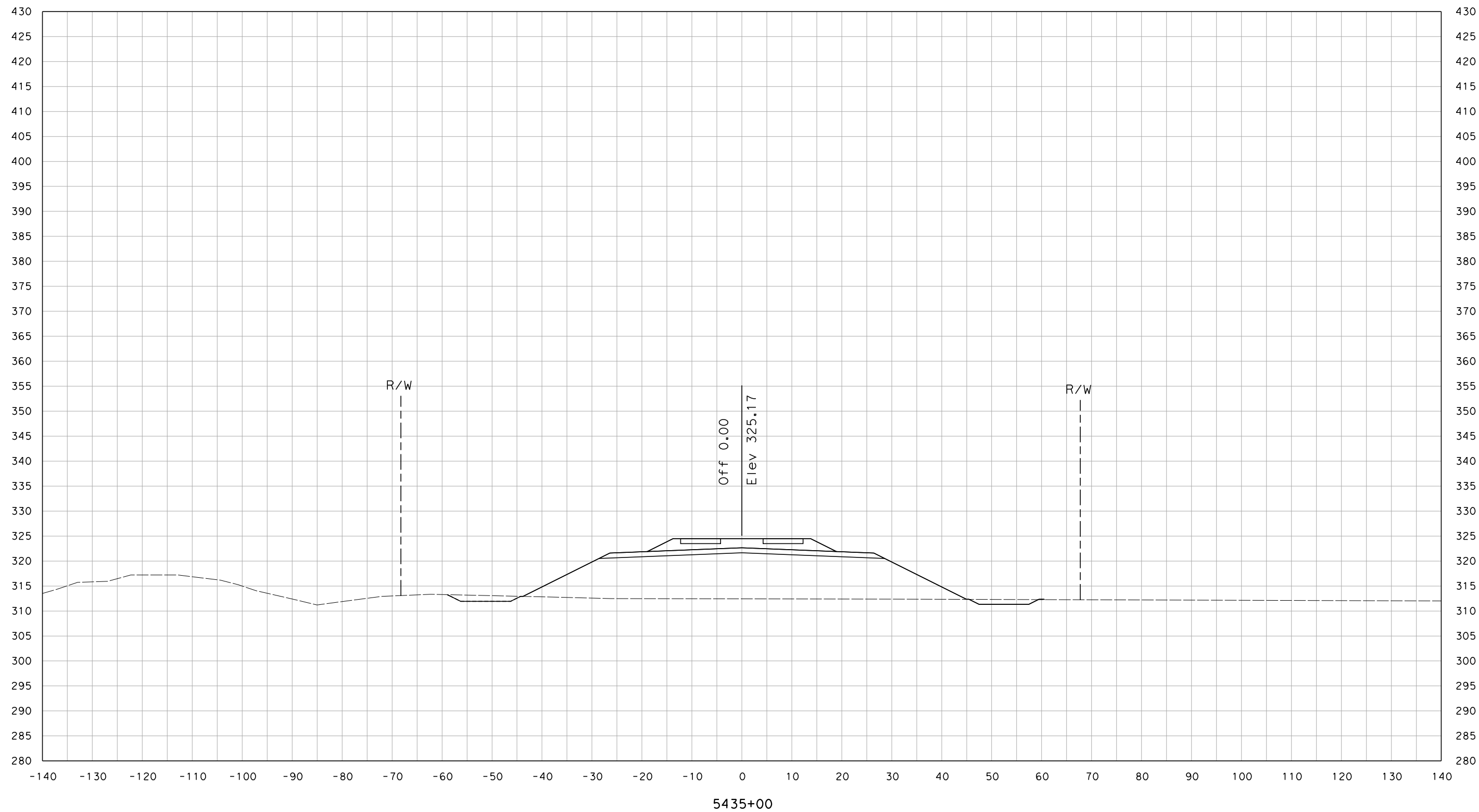
STA. 5422+50 TO STA. 5422+50



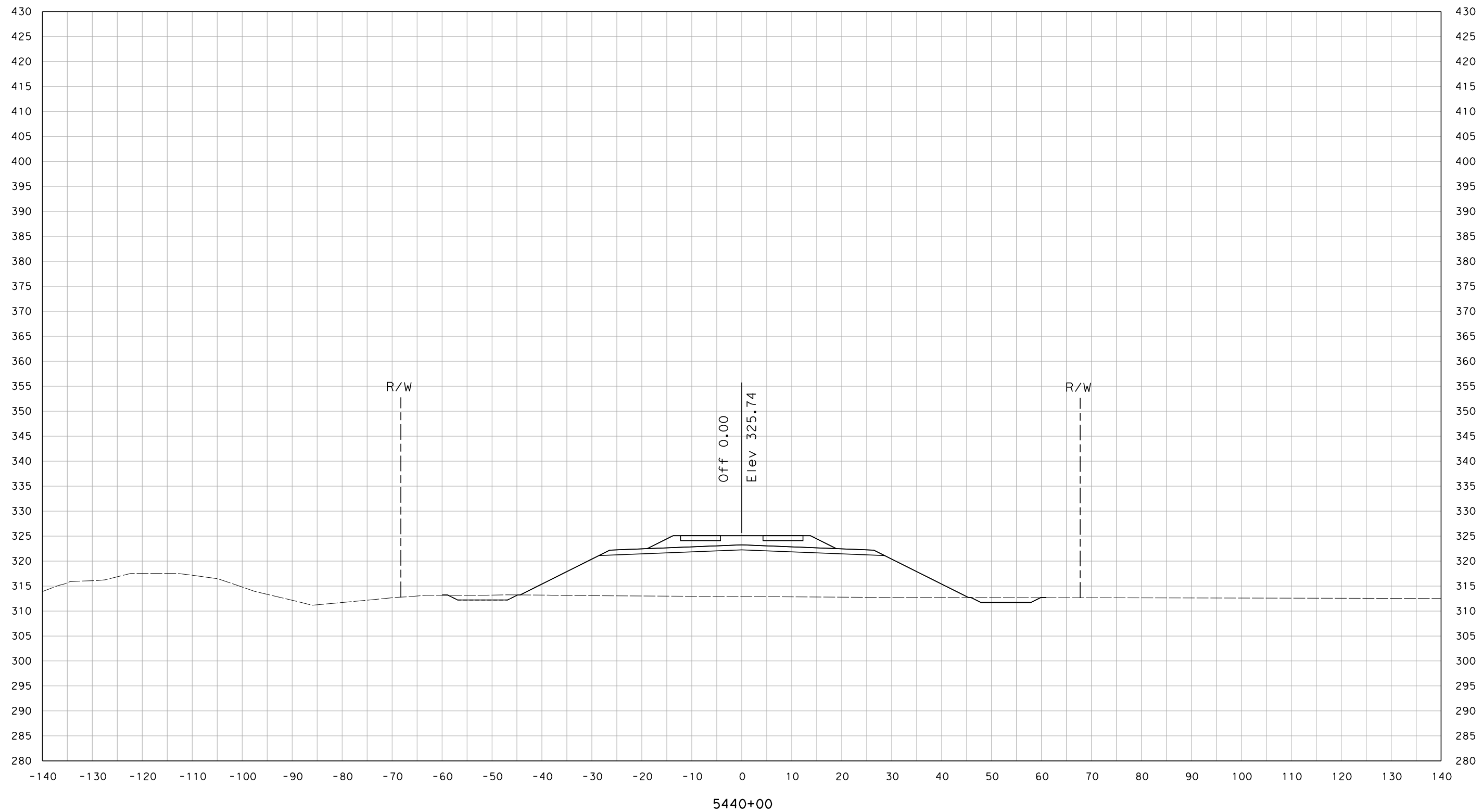
STA. 5425+00 TO STA. 5425+00



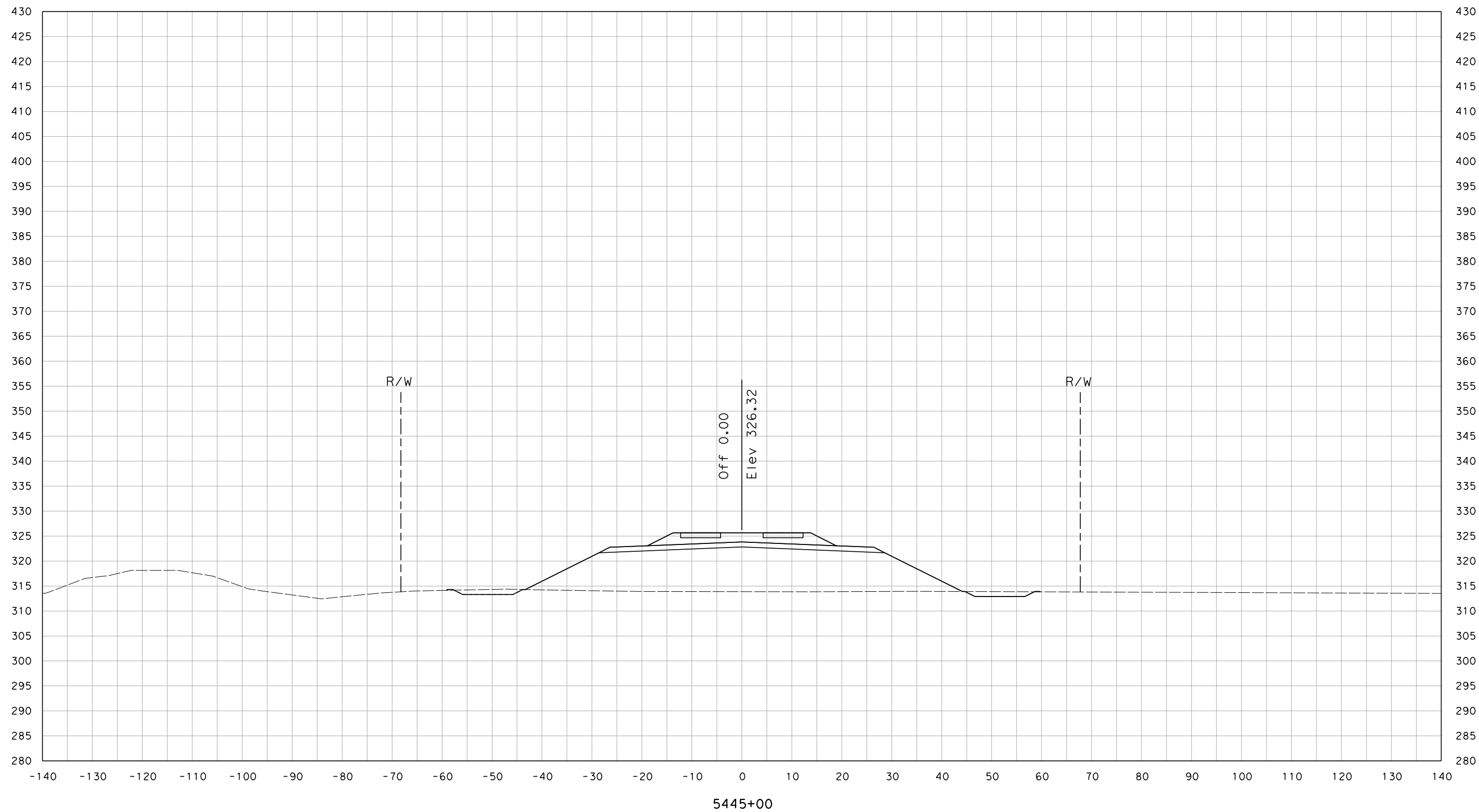
STA. 5430+00 TO STA. 5430+00



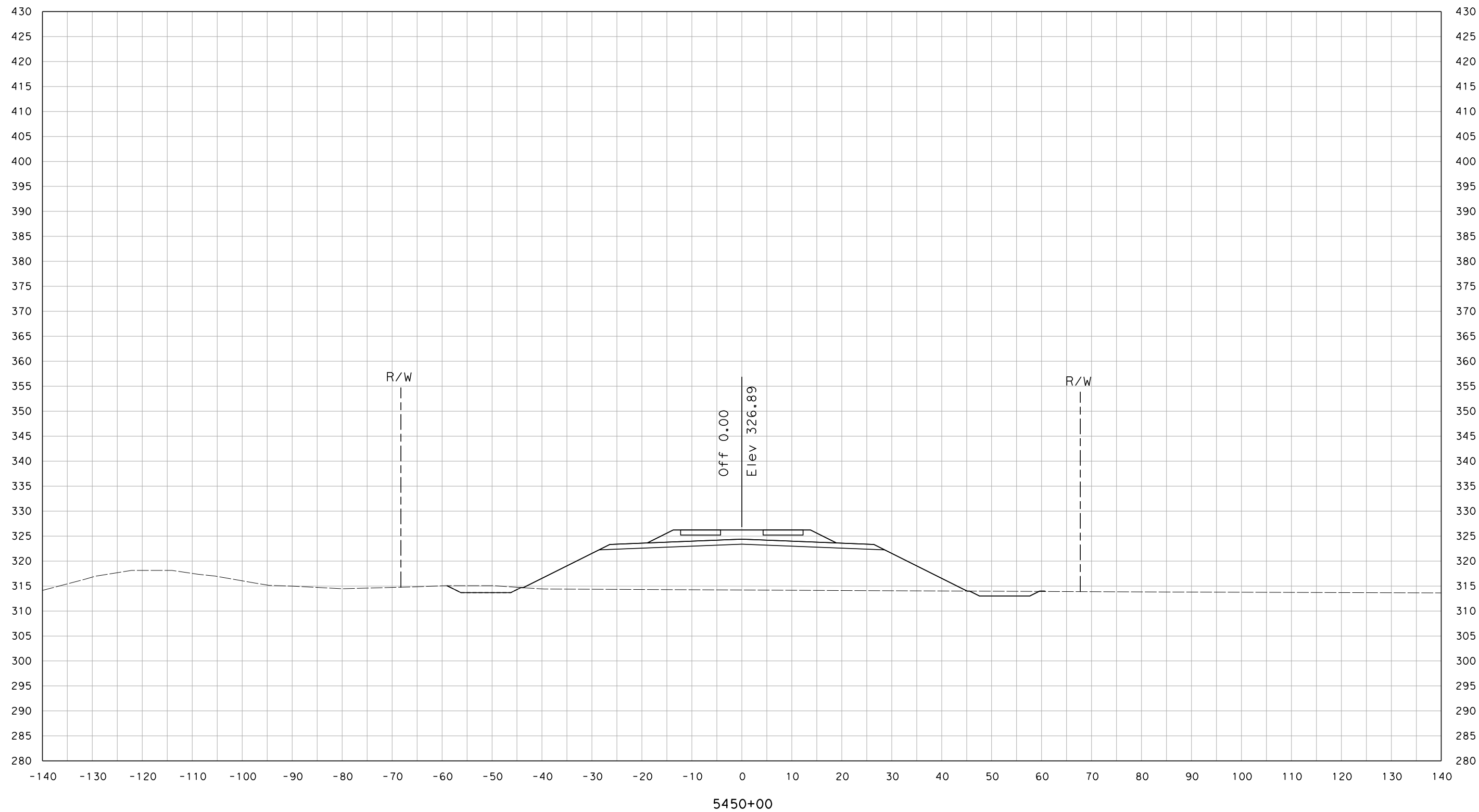
STA. 5435+00 TO STA. 5435+00



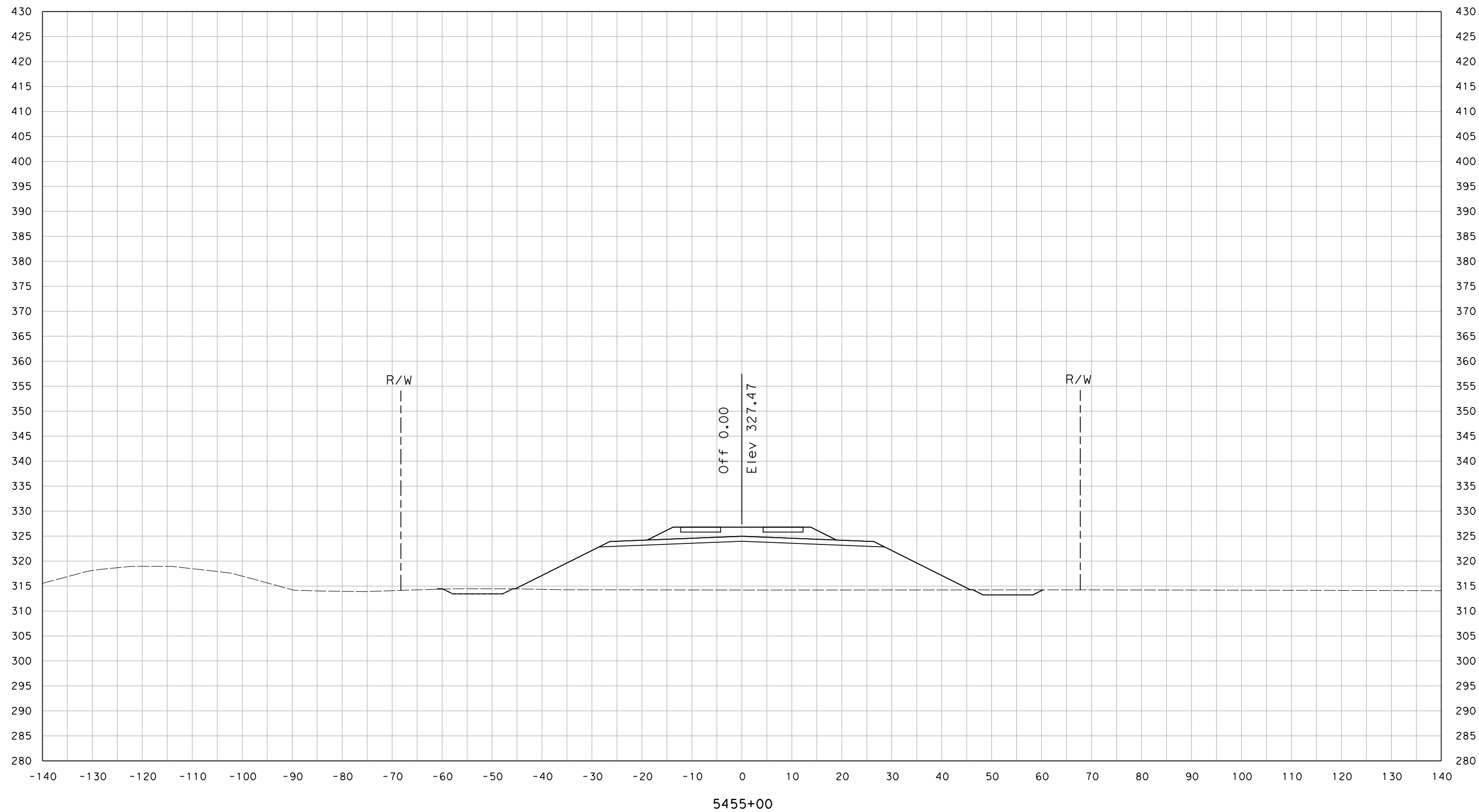
STA. 5440+00 TO STA. 5440+00



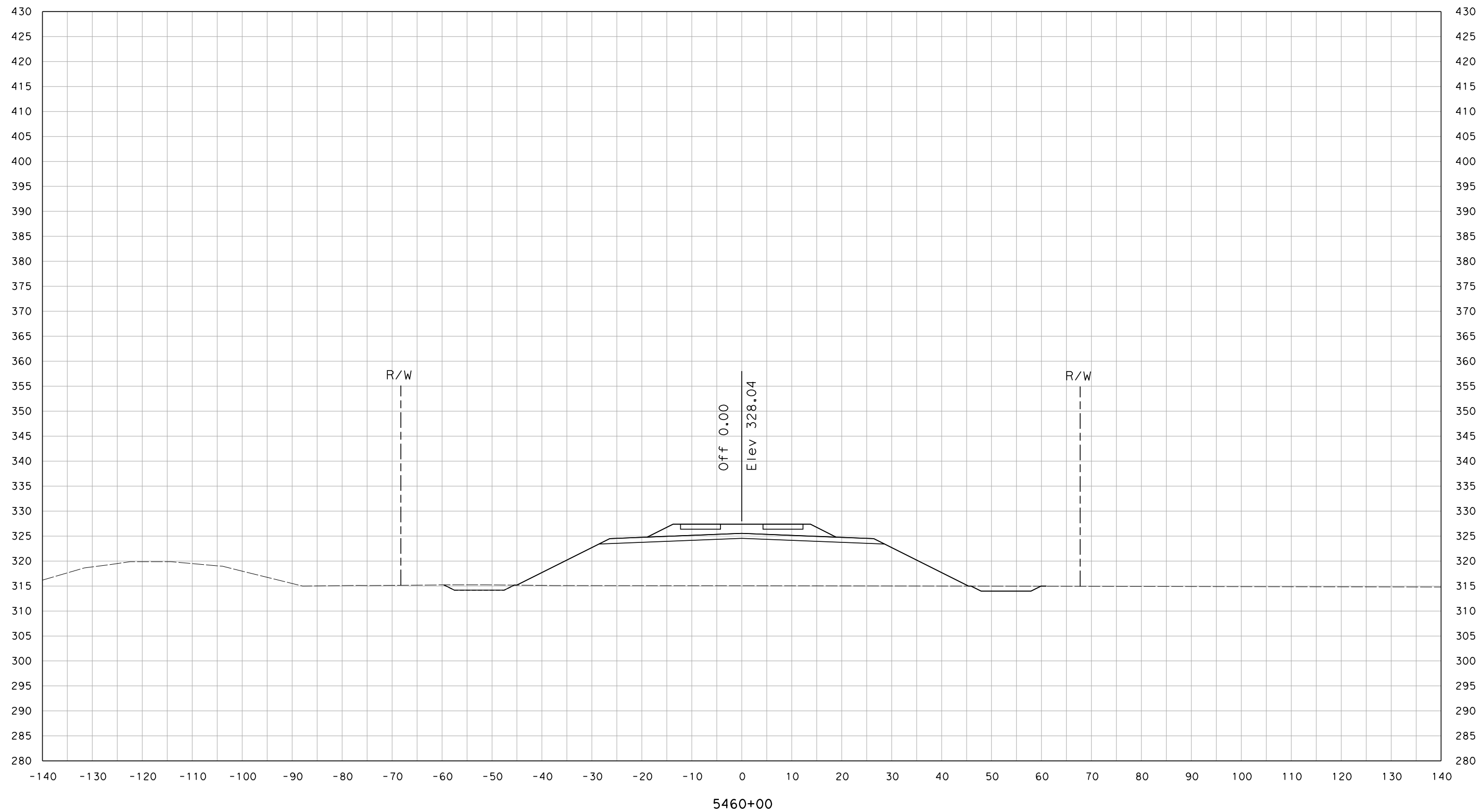
STA. 5445+00 TO STA. 5445+00



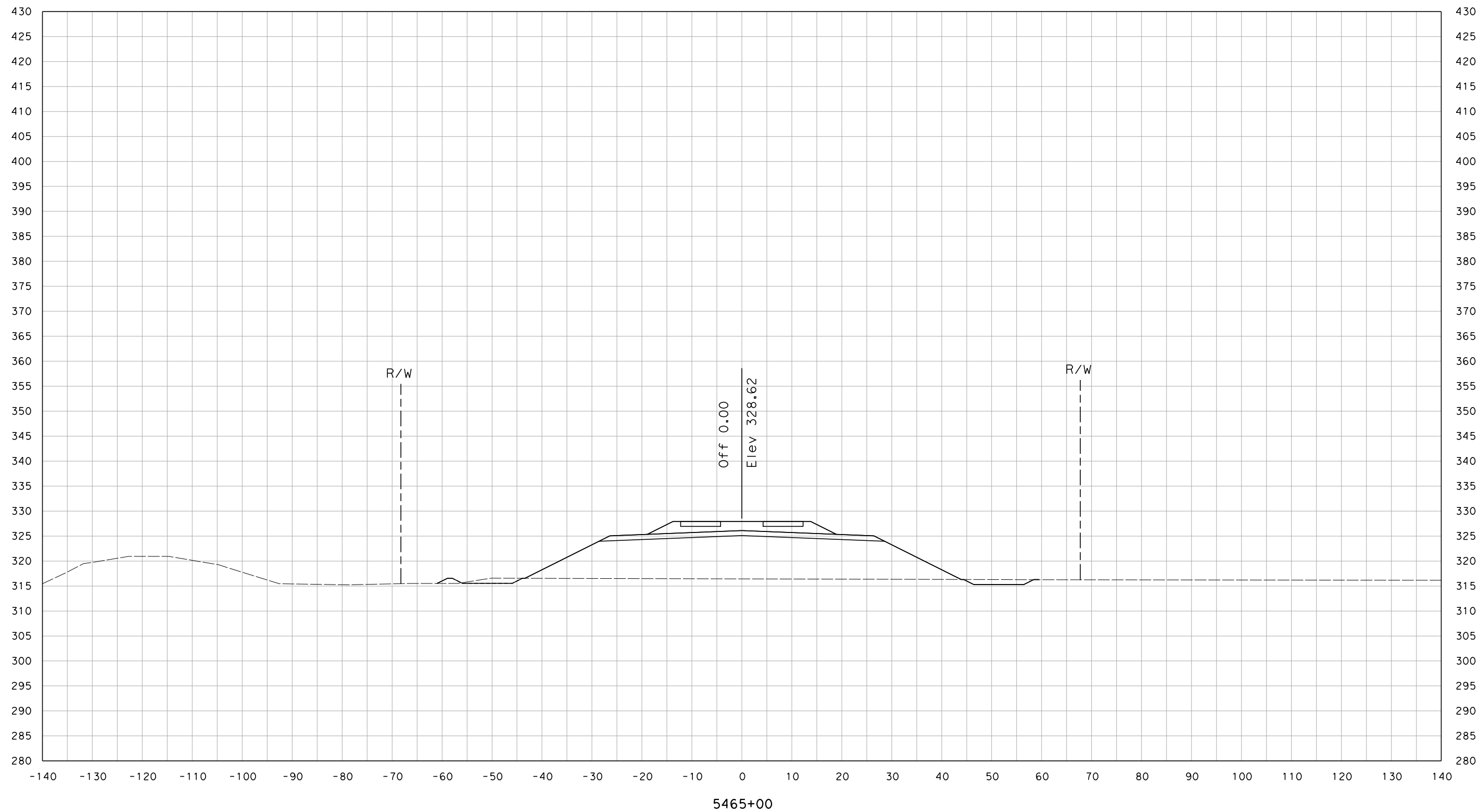
STA. 5450+00 TO STA. 5450+00



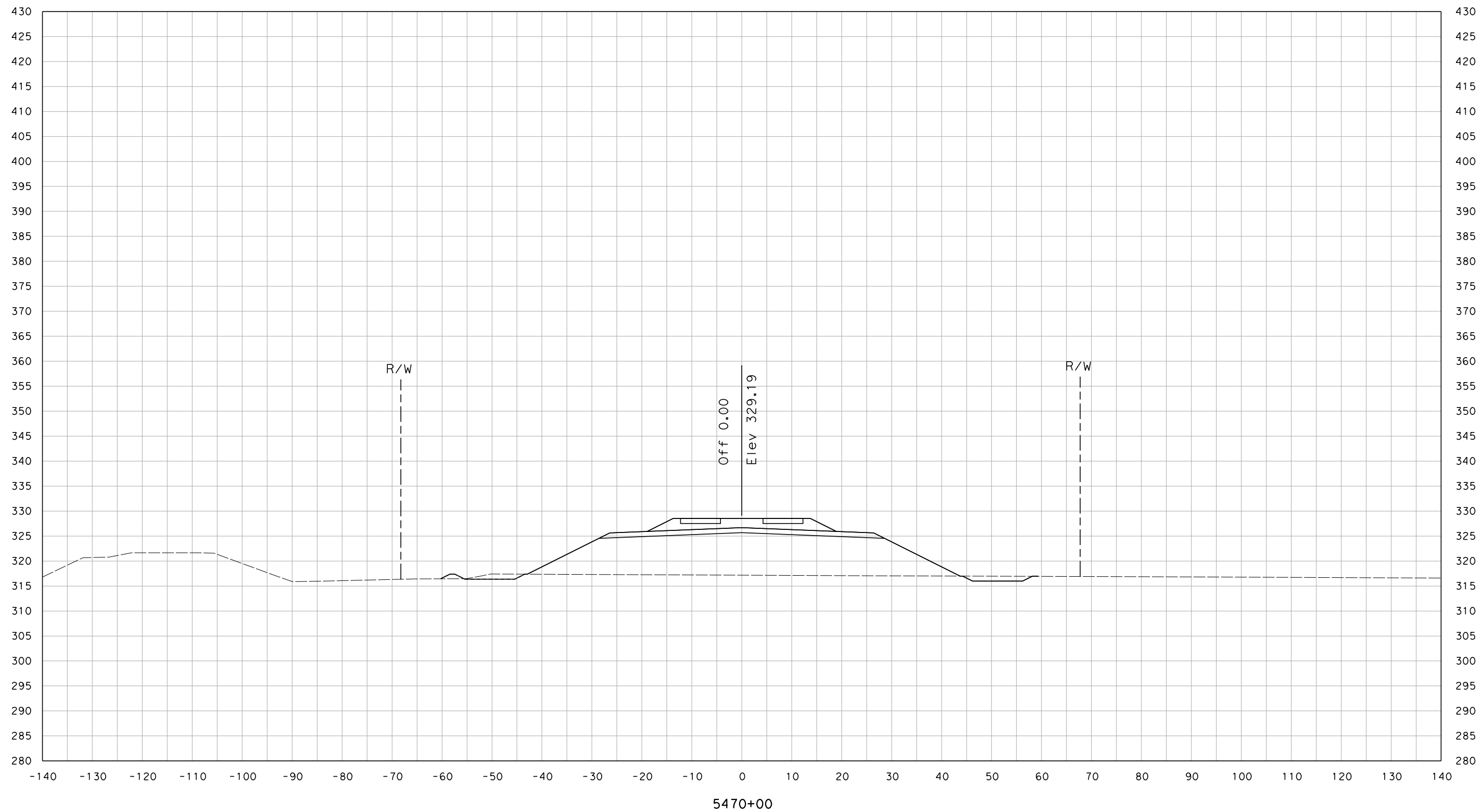
STA. 5455+00 TO STA. 5455+00



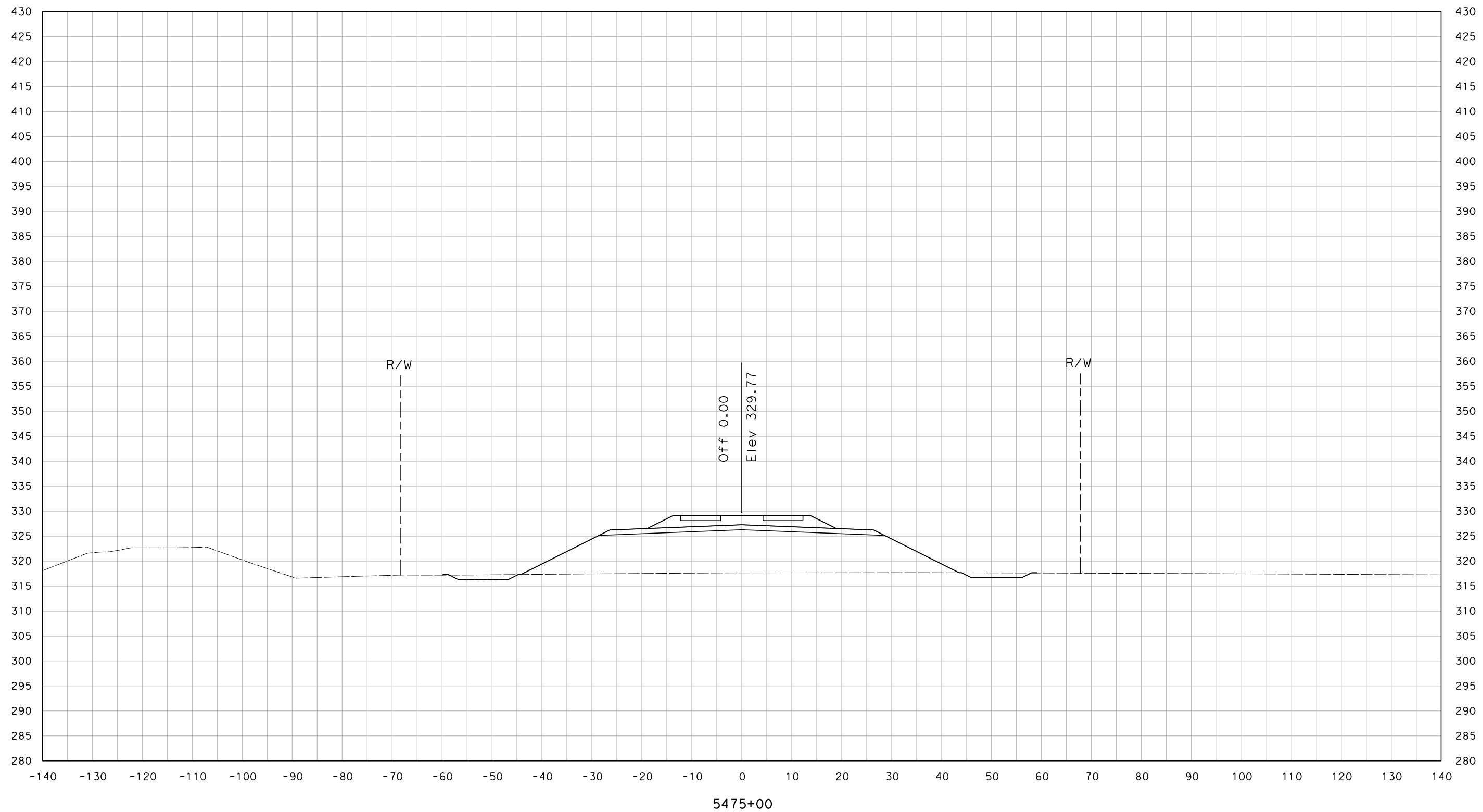
STA. 5460+00 TO STA. 5460+00



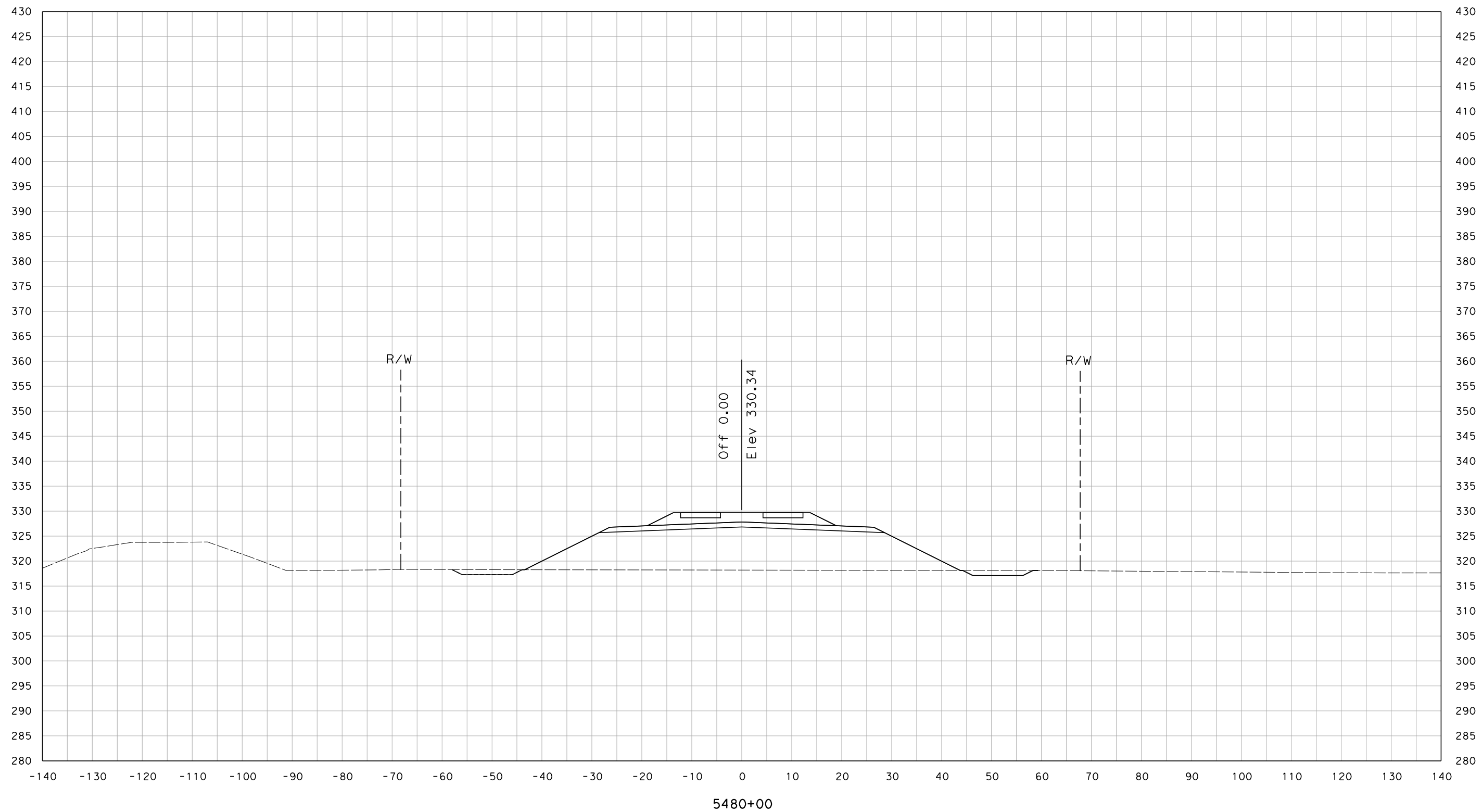
STA. 5465+00 TO STA. 5465+00



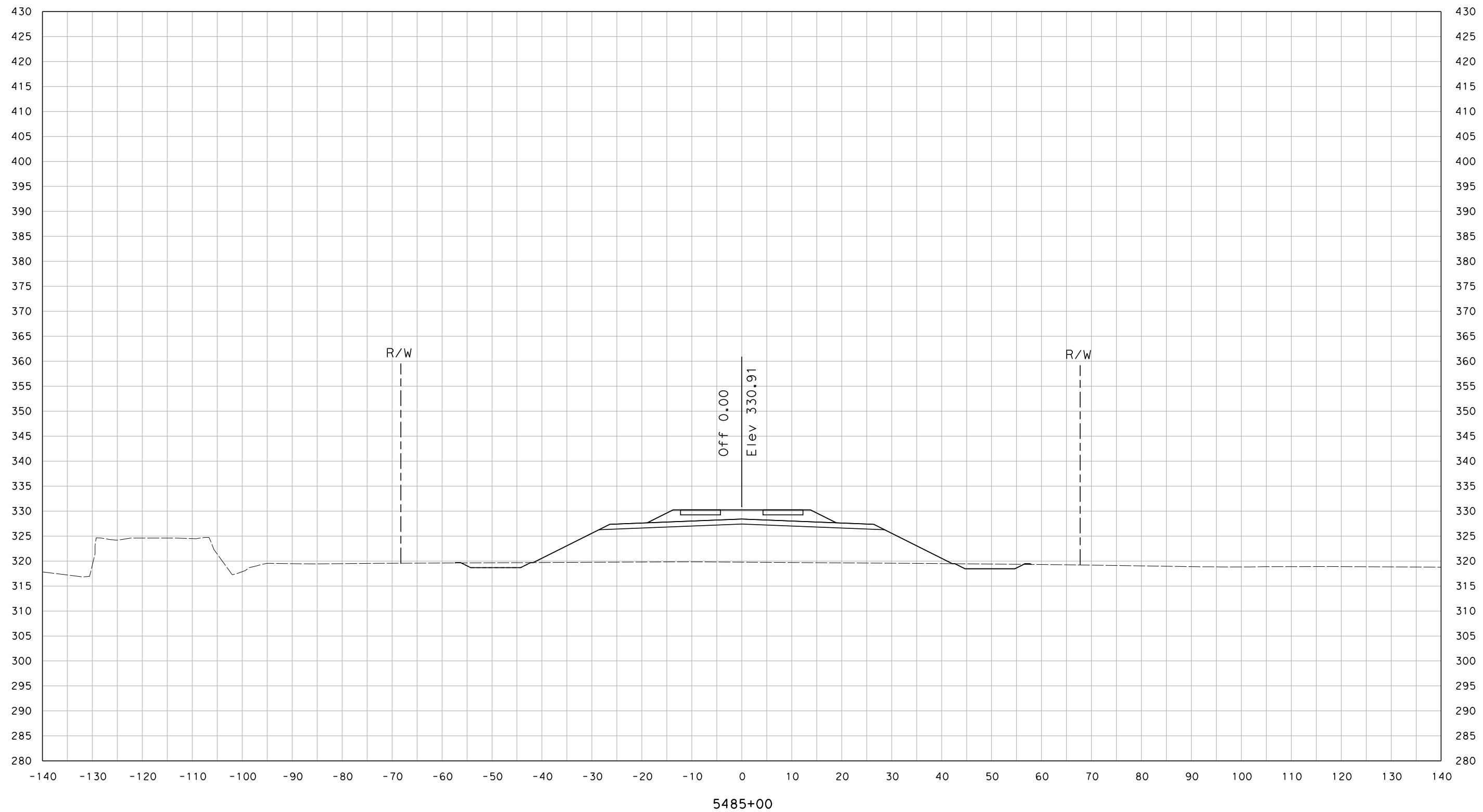
STA. 5470+00 TO STA. 5470+00



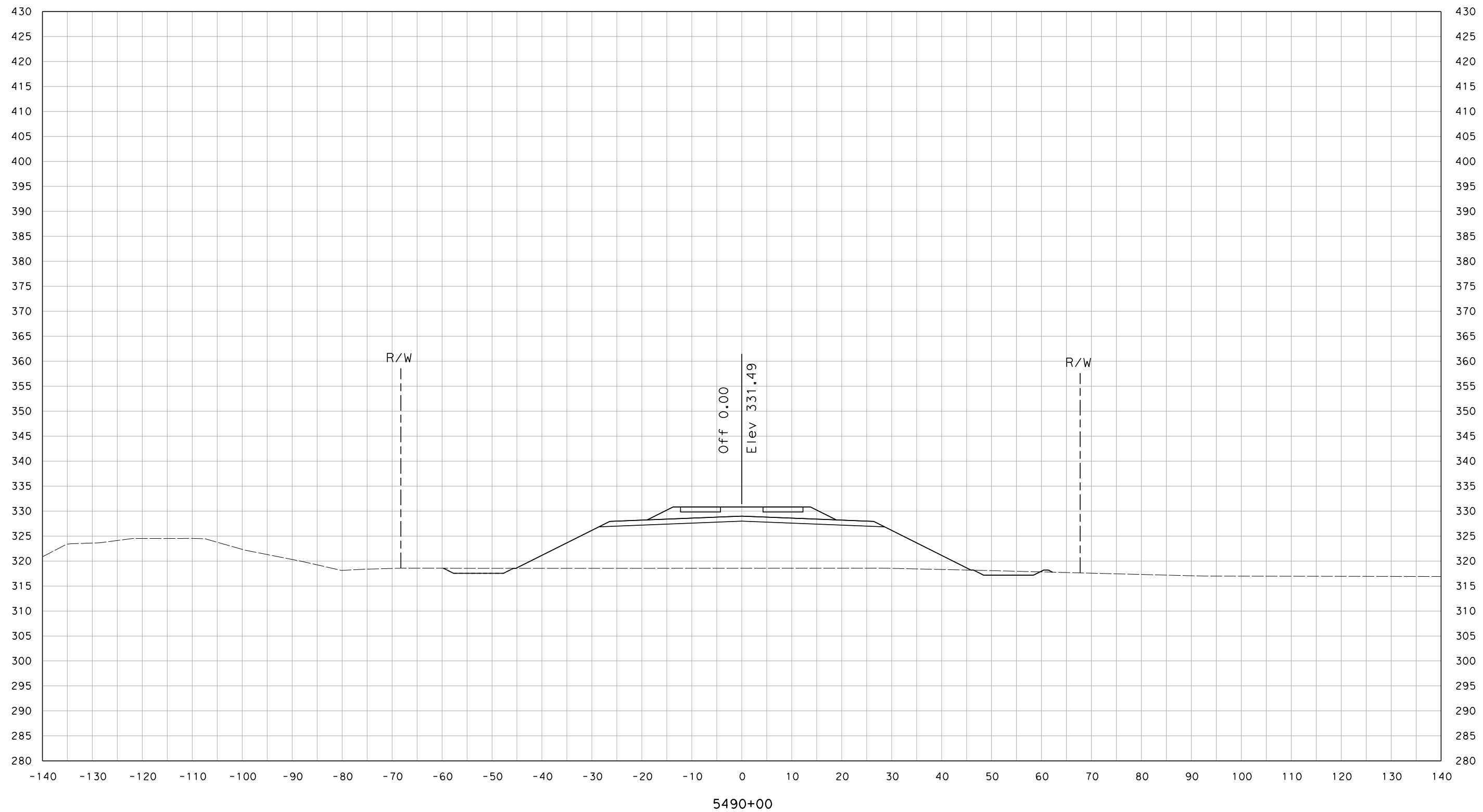
STA. 5475+00 TO STA. 5475+00



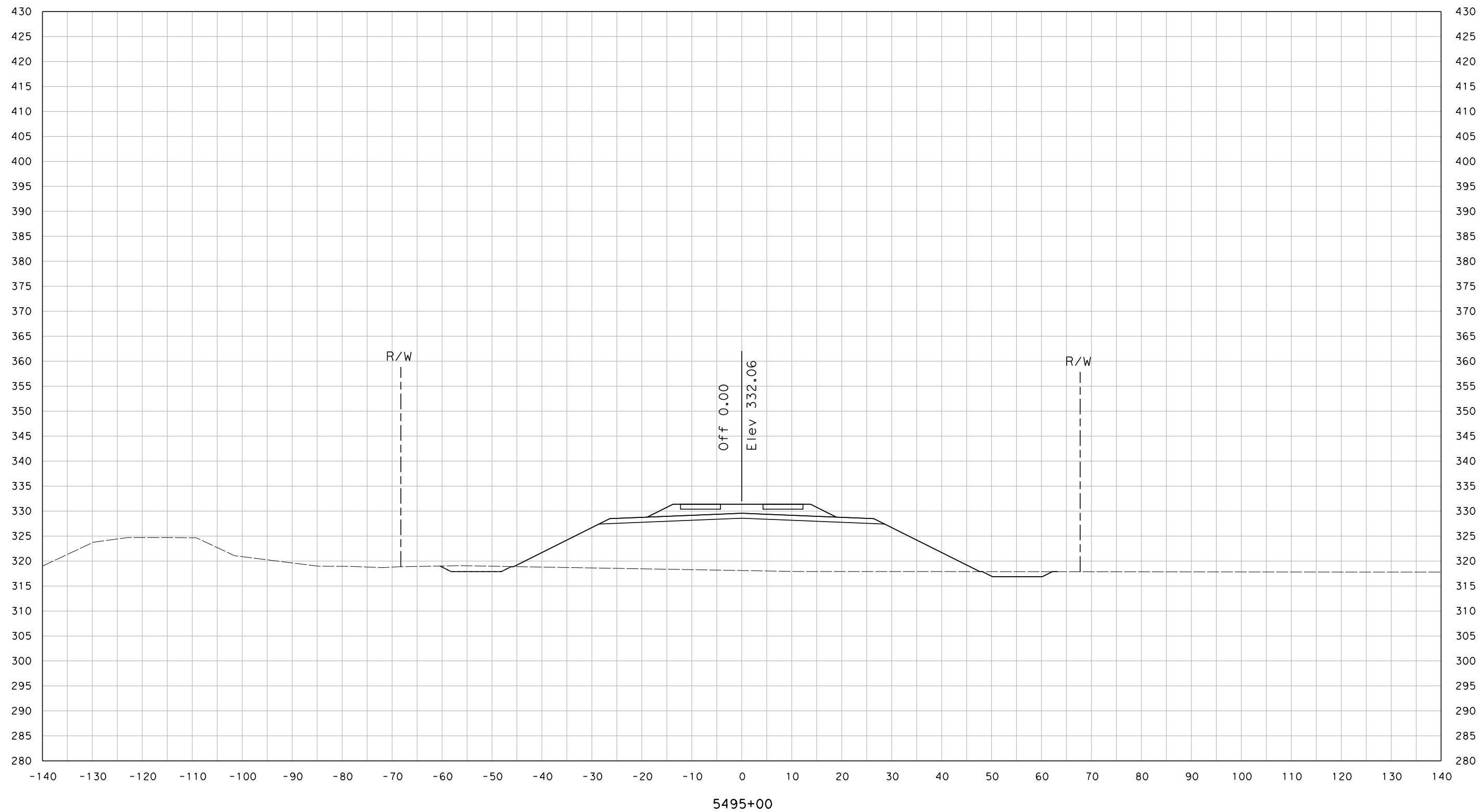
STA. 5480+00 TO STA. 5480+00



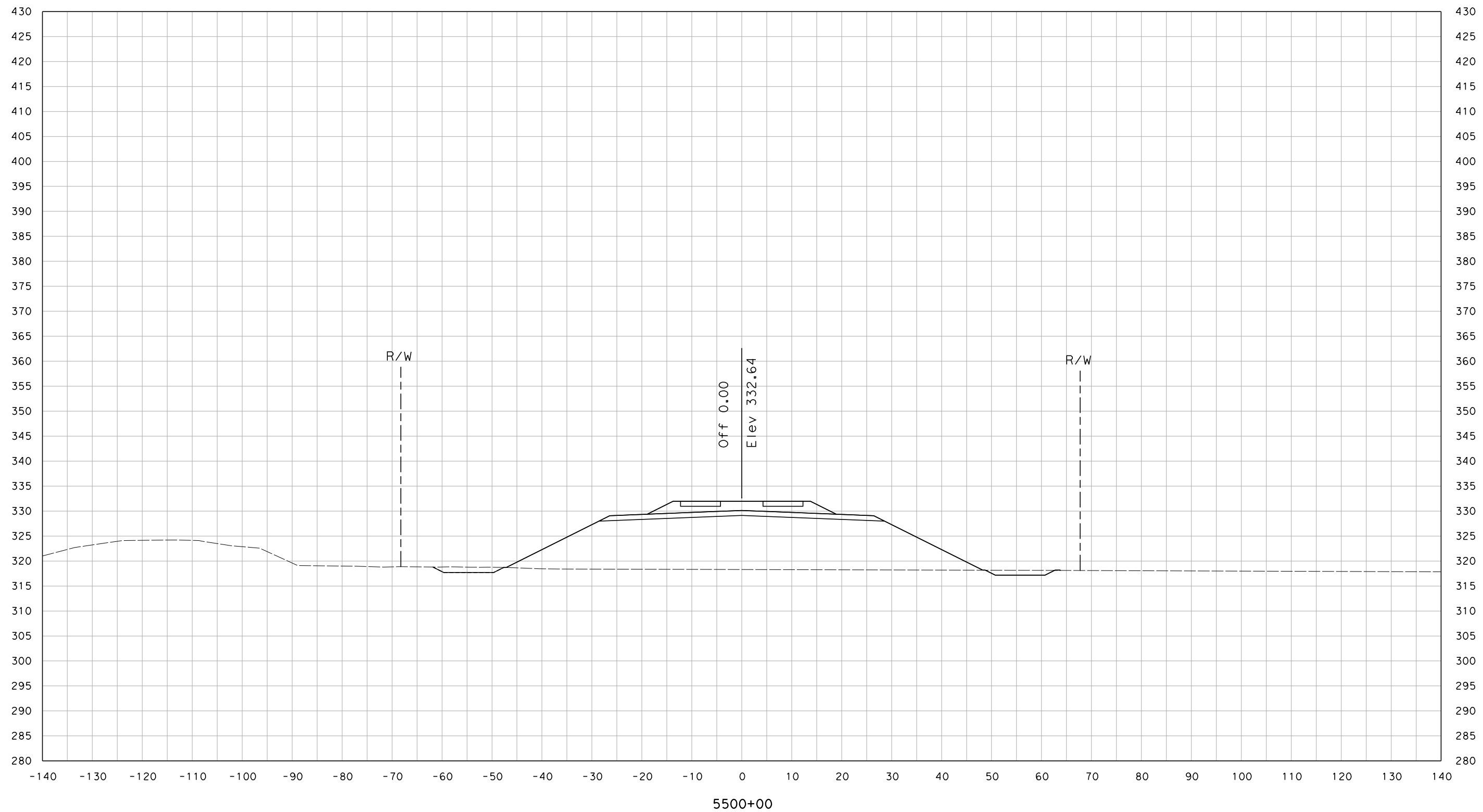
STA. 5485+00 TO STA. 5485+00



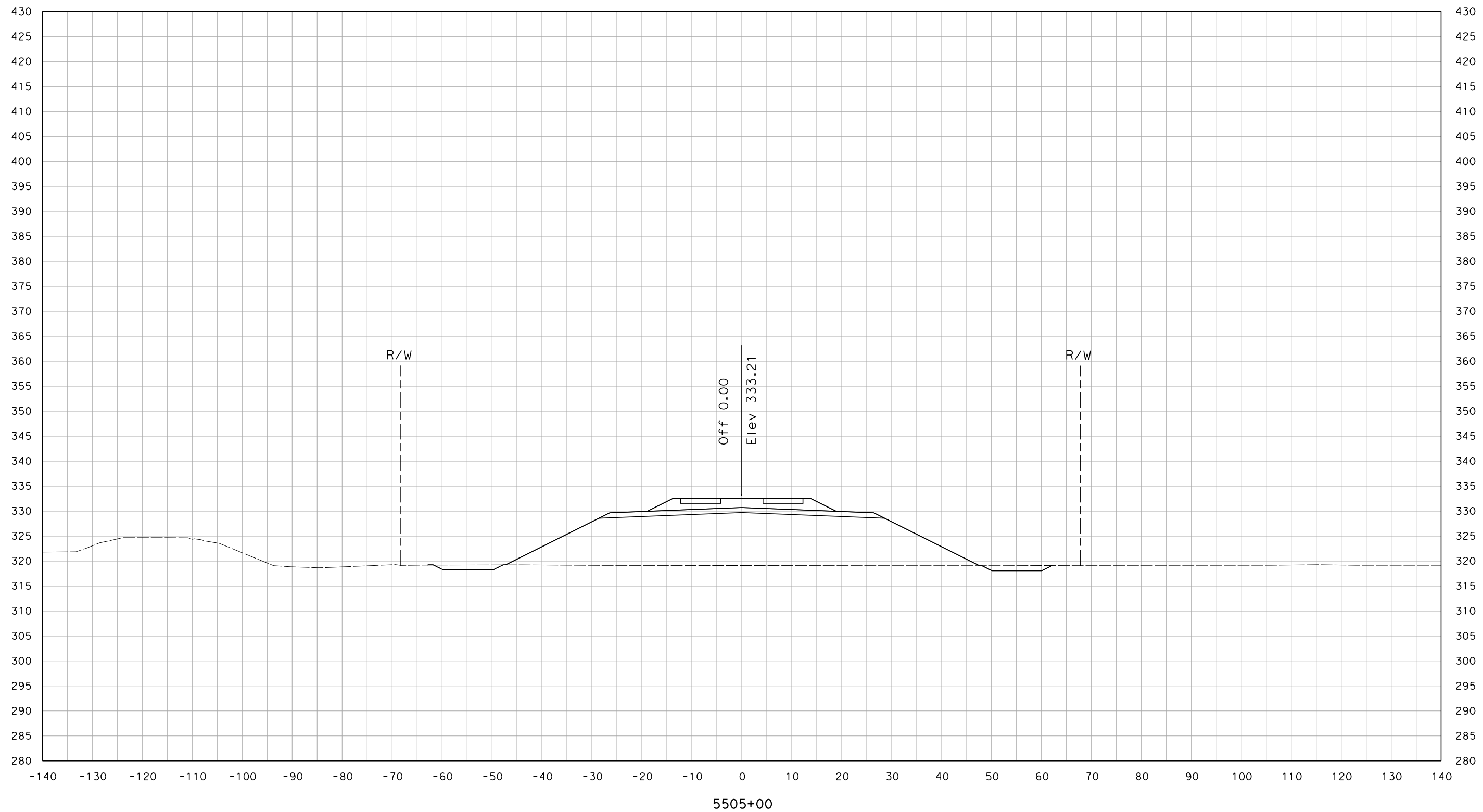
STA. 5490+00 TO STA. 5490+00



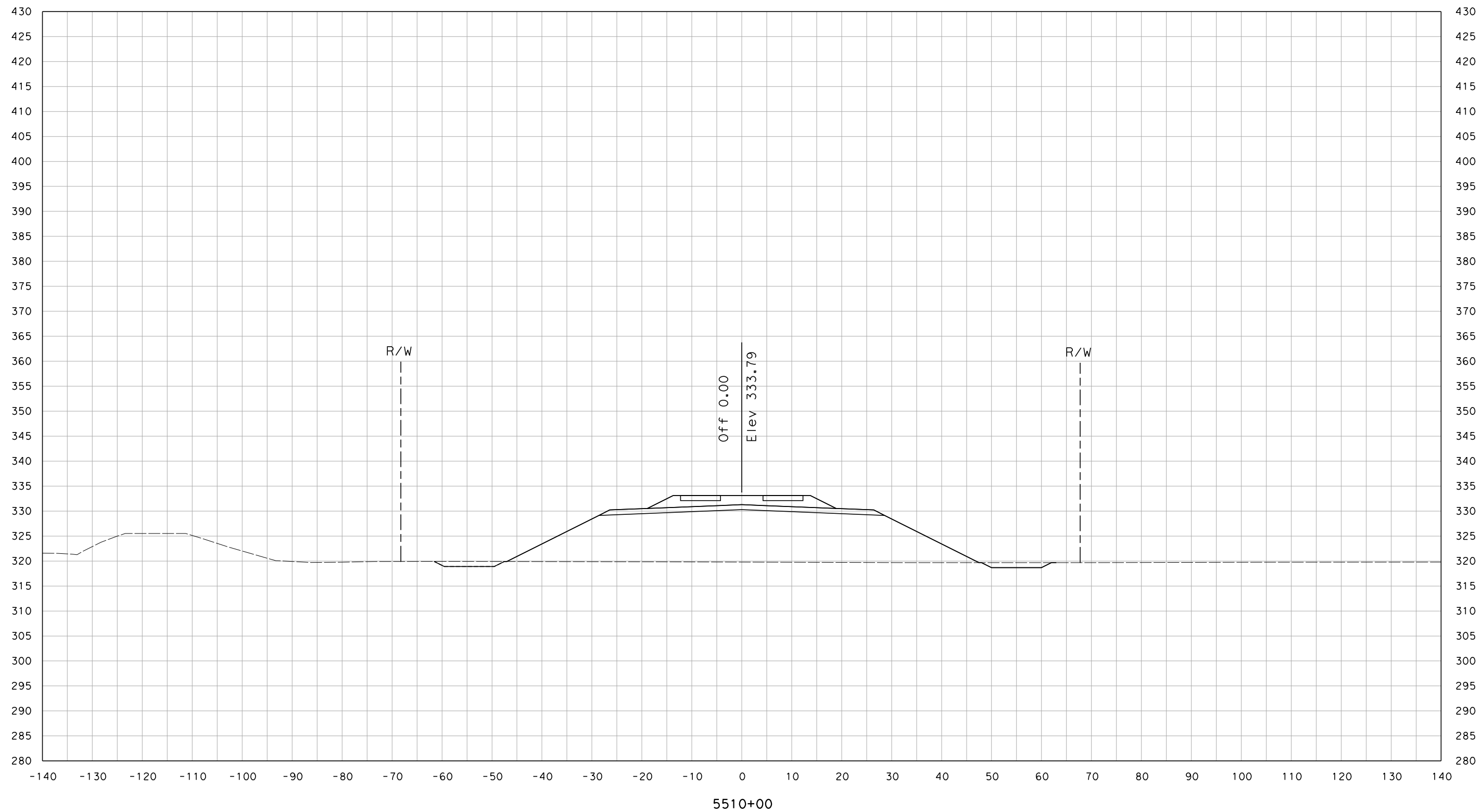
STA. 5495+00 TO STA. 5495+00



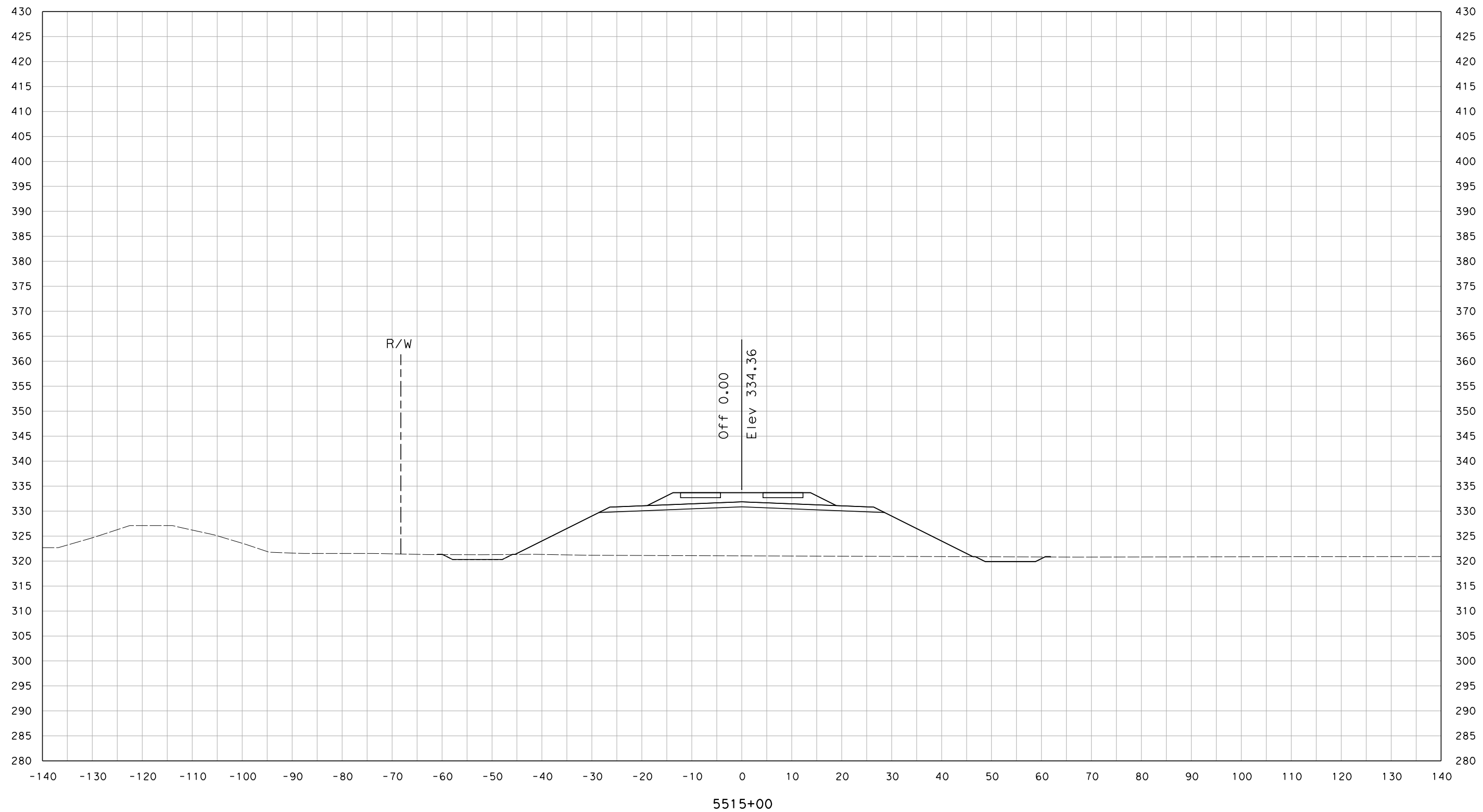
STA. 5500+00 TO STA. 5500+00



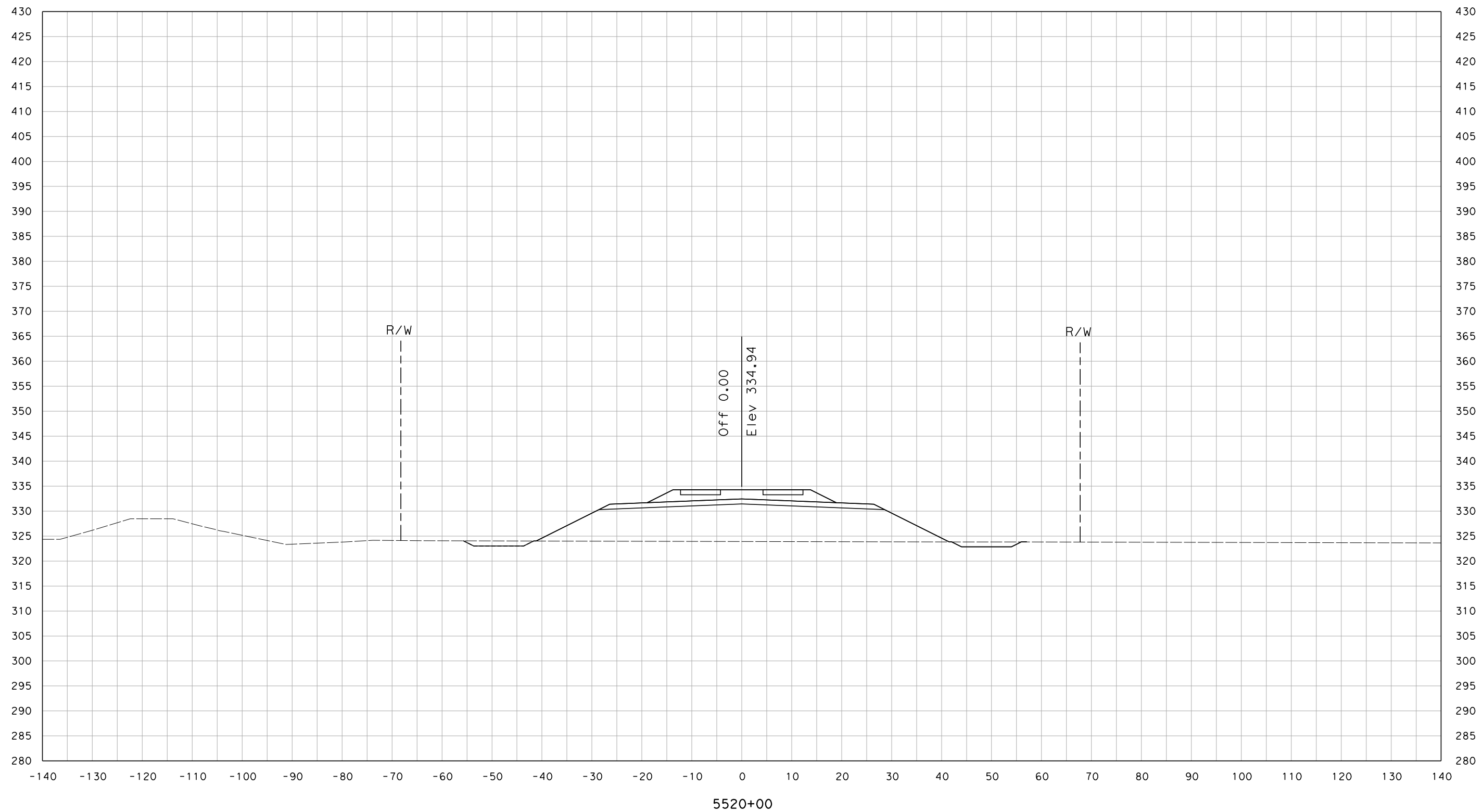
STA. 5505+00 TO STA. 5505+00



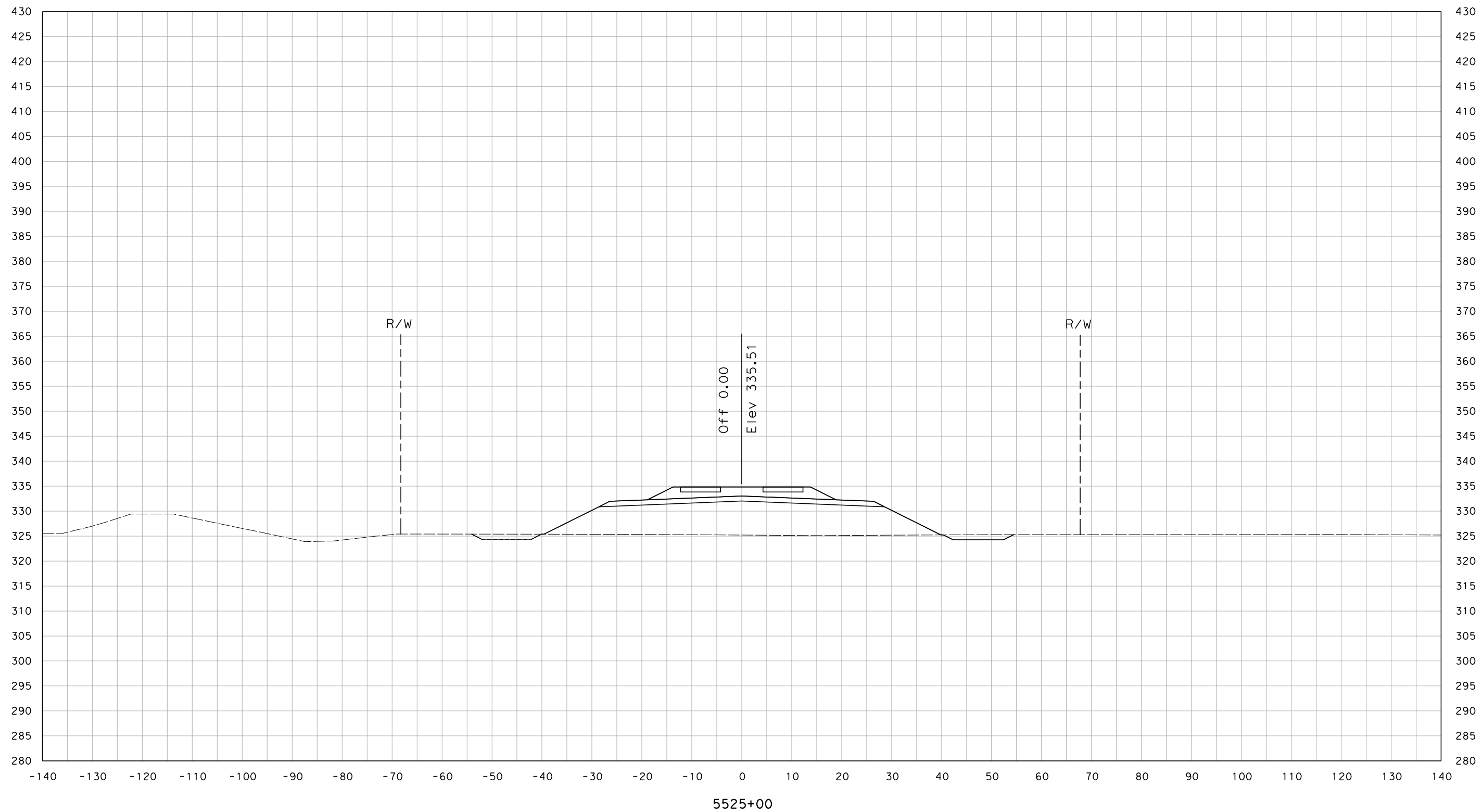
STA. 5510+00 TO STA. 5510+00



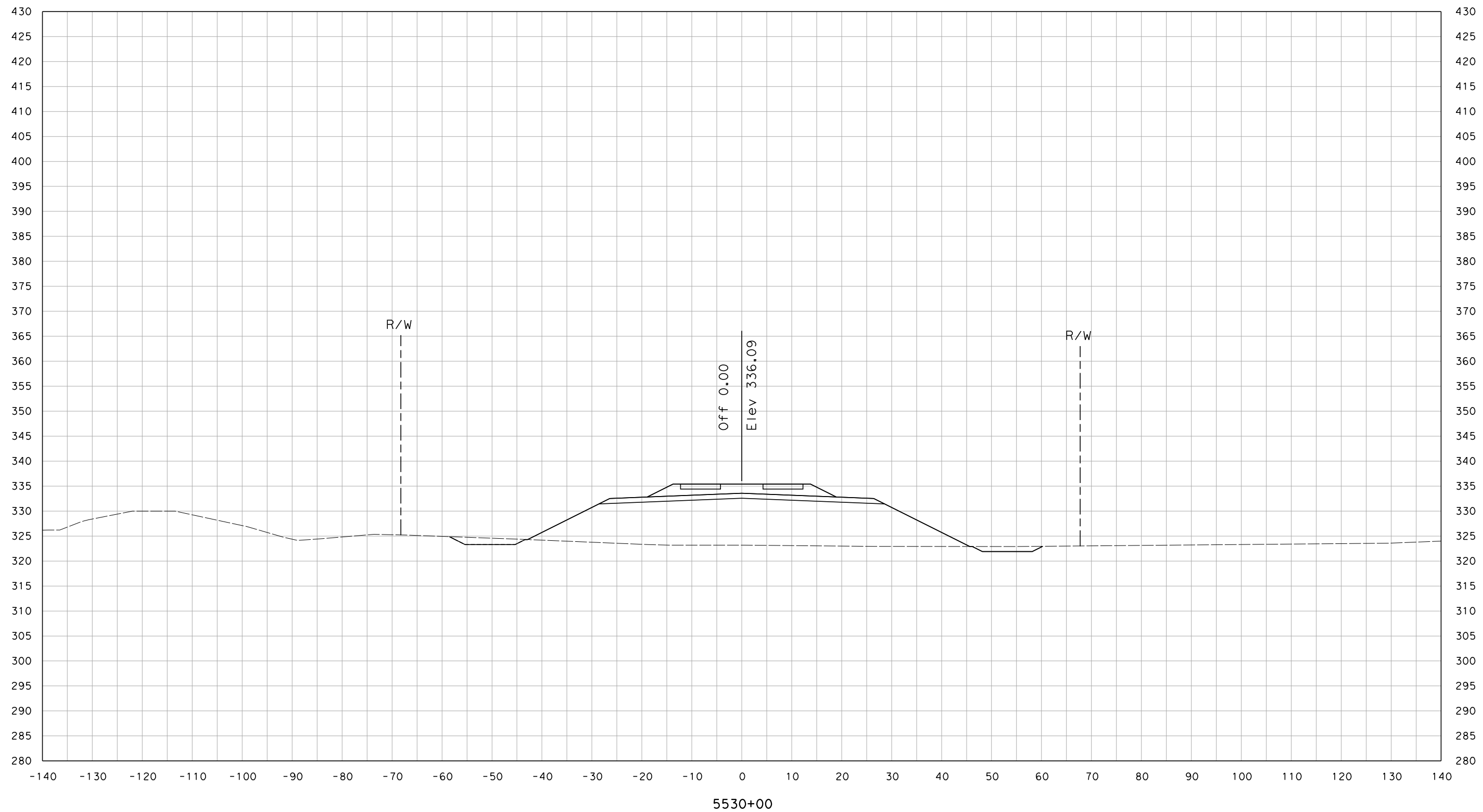
STA. 5515+00 TO STA. 5515+00



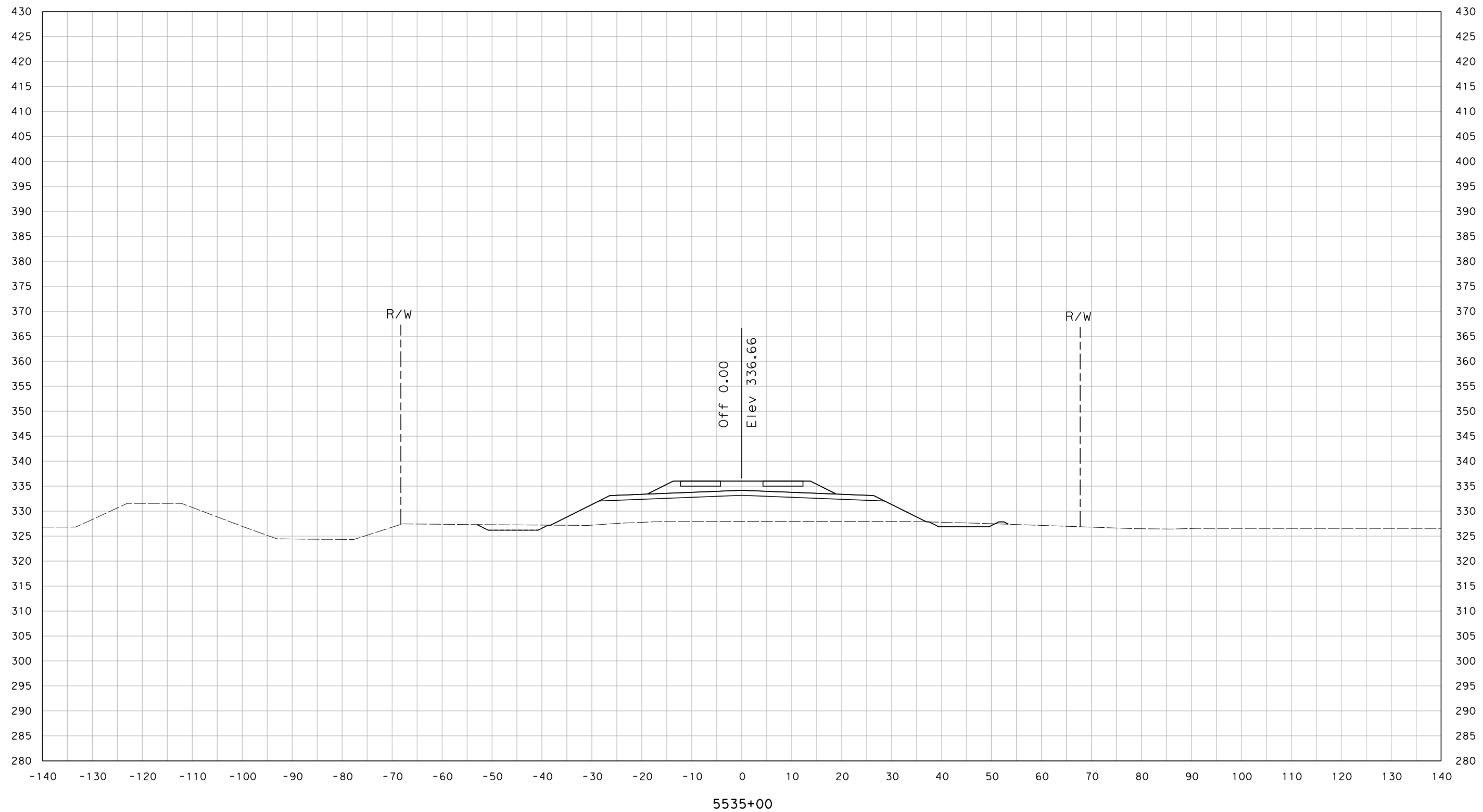
STA. 5520+00 TO STA. 5520+00



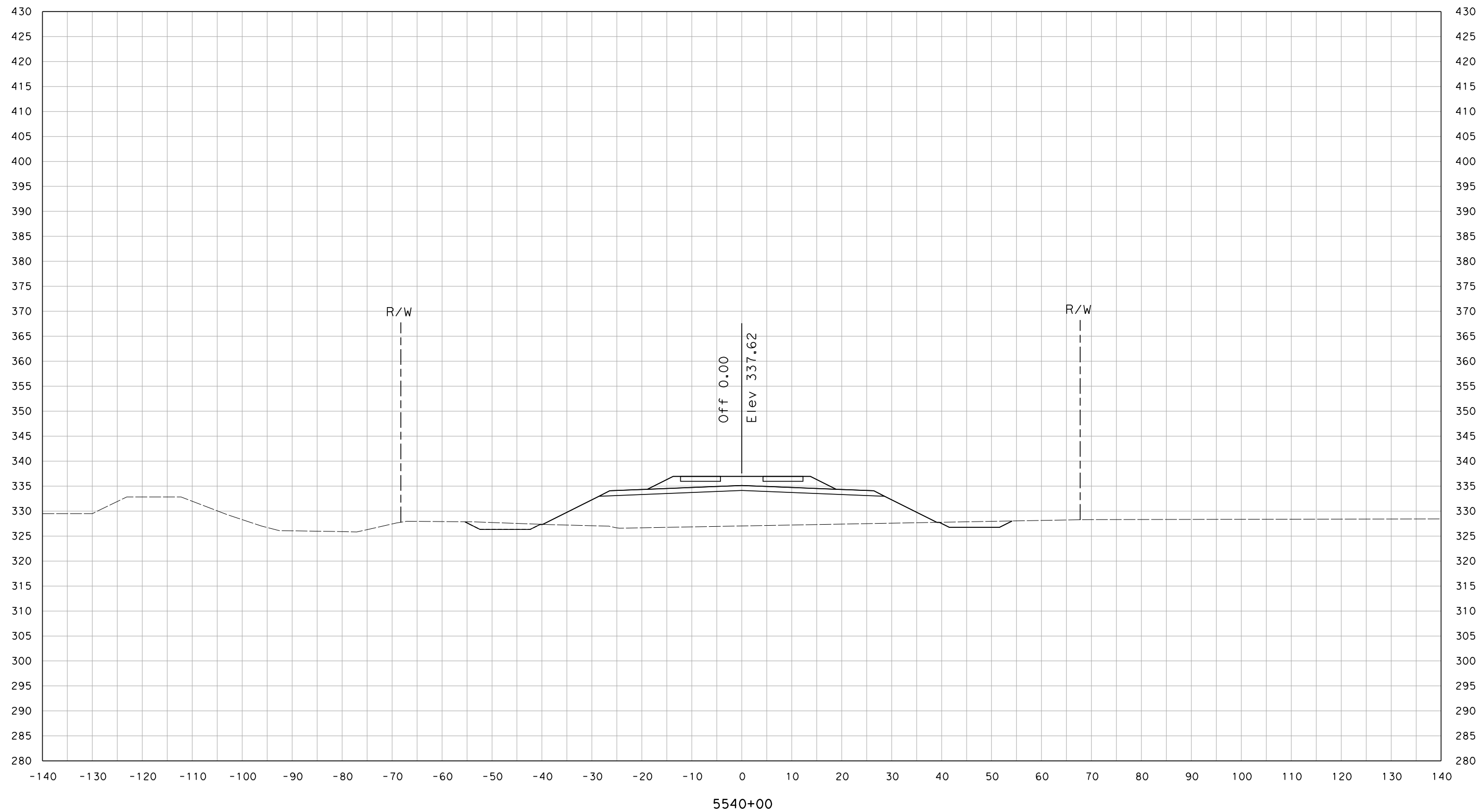
STA. 5525+00 TO STA. 5525+00



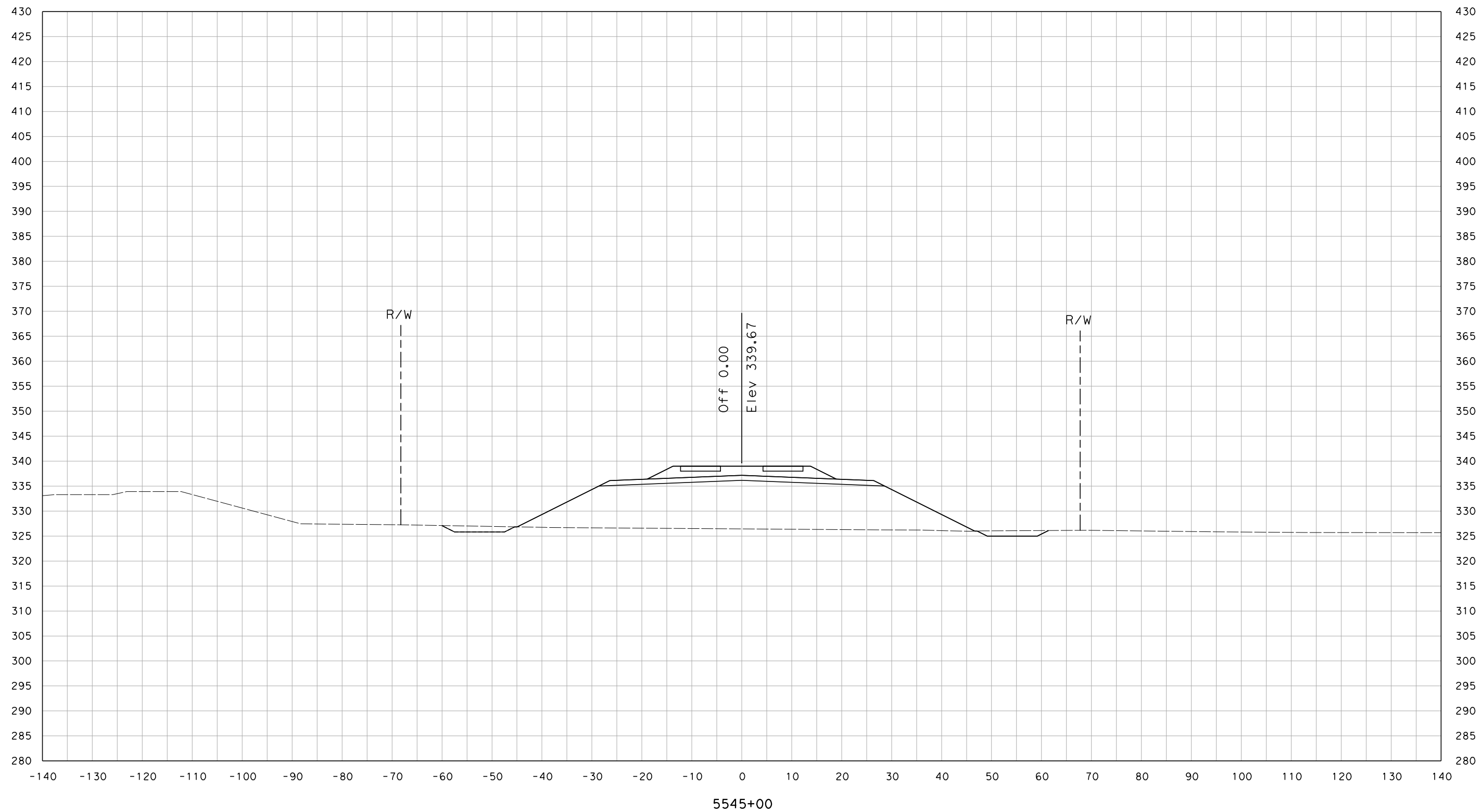
STA. 5530+00 TO STA. 5530+00



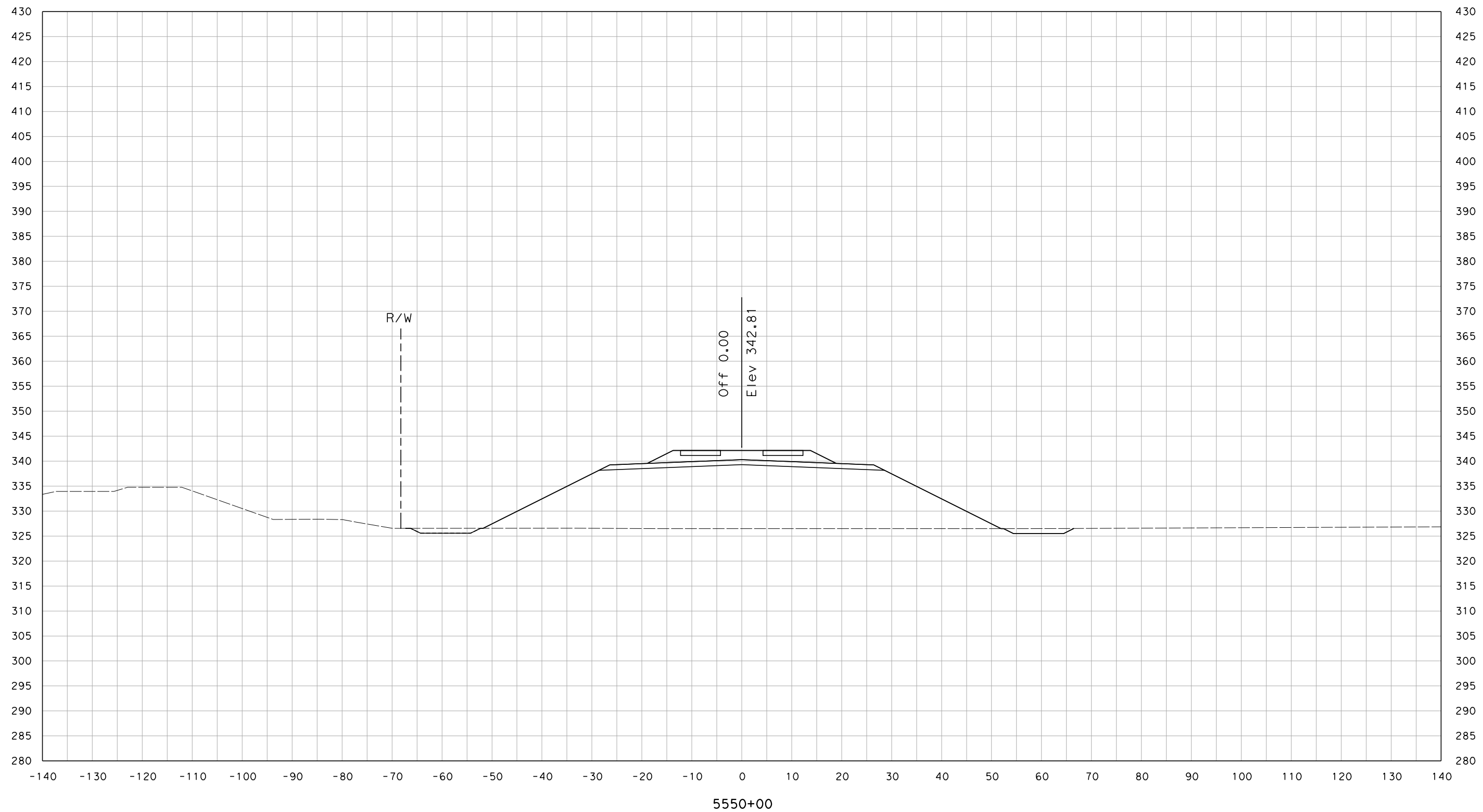
STA. 5535+00 TO STA. 5535+00



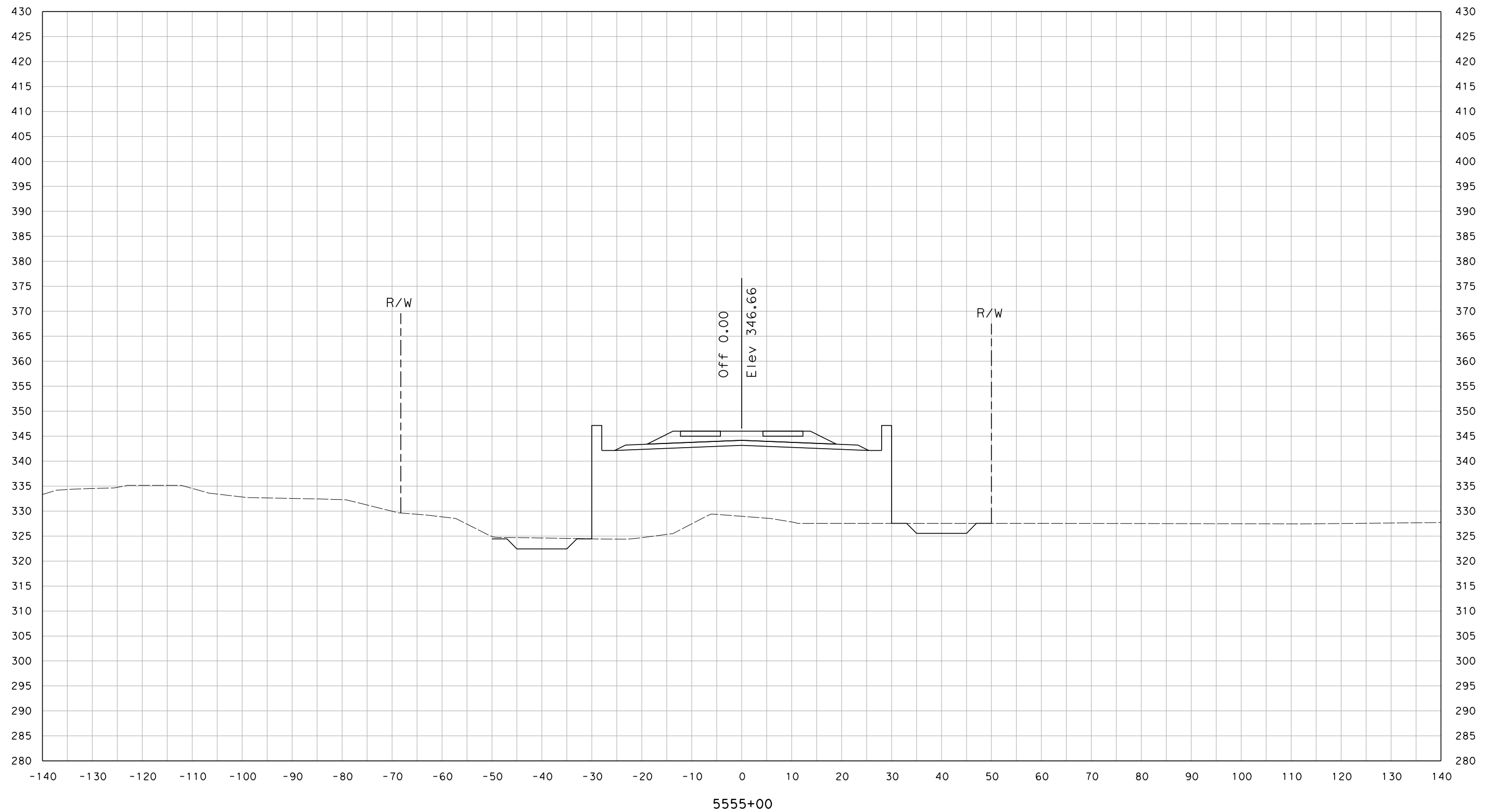
STA. 5540+00 TO STA. 5540+00



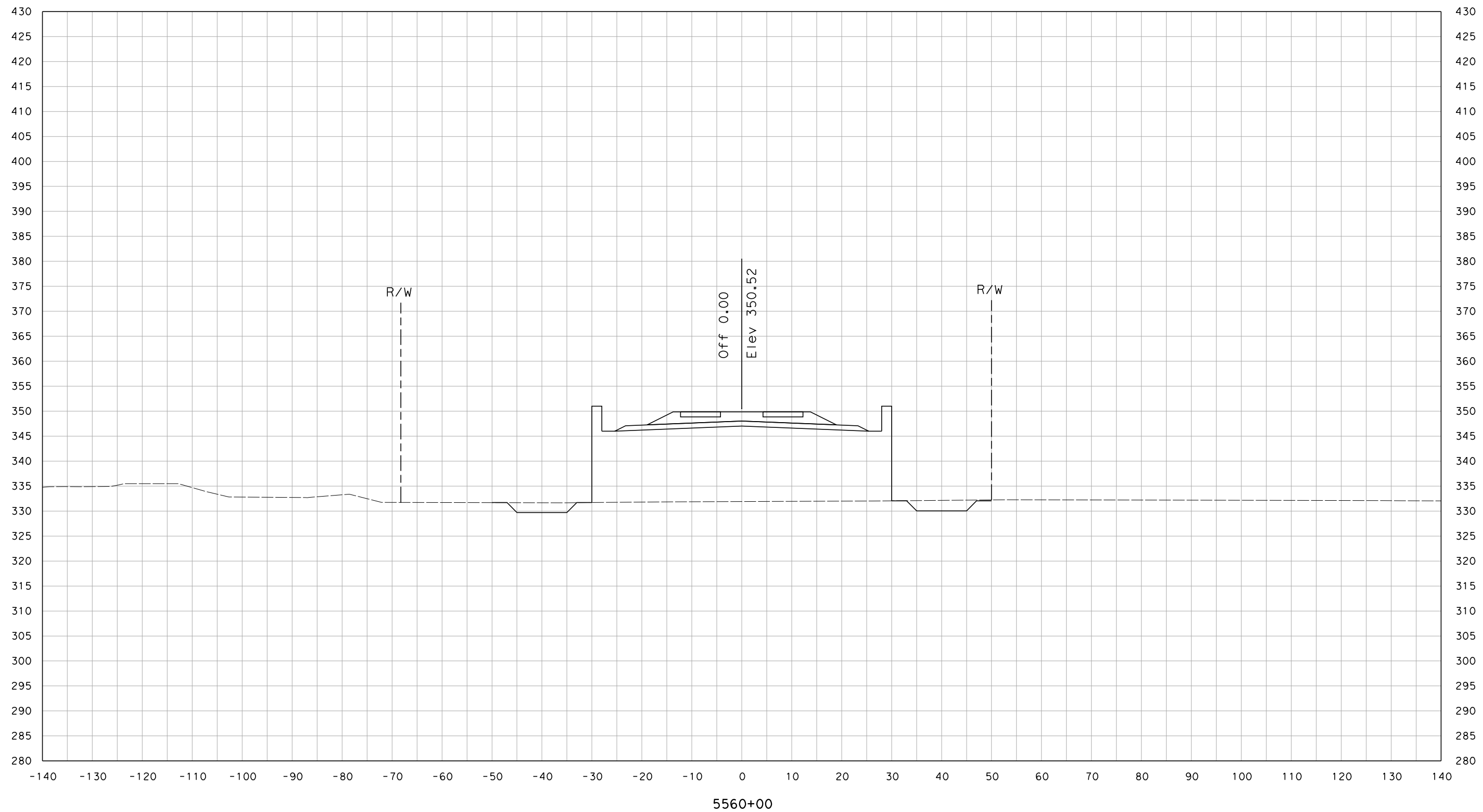
STA. 5545+00 TO STA. 5545+00



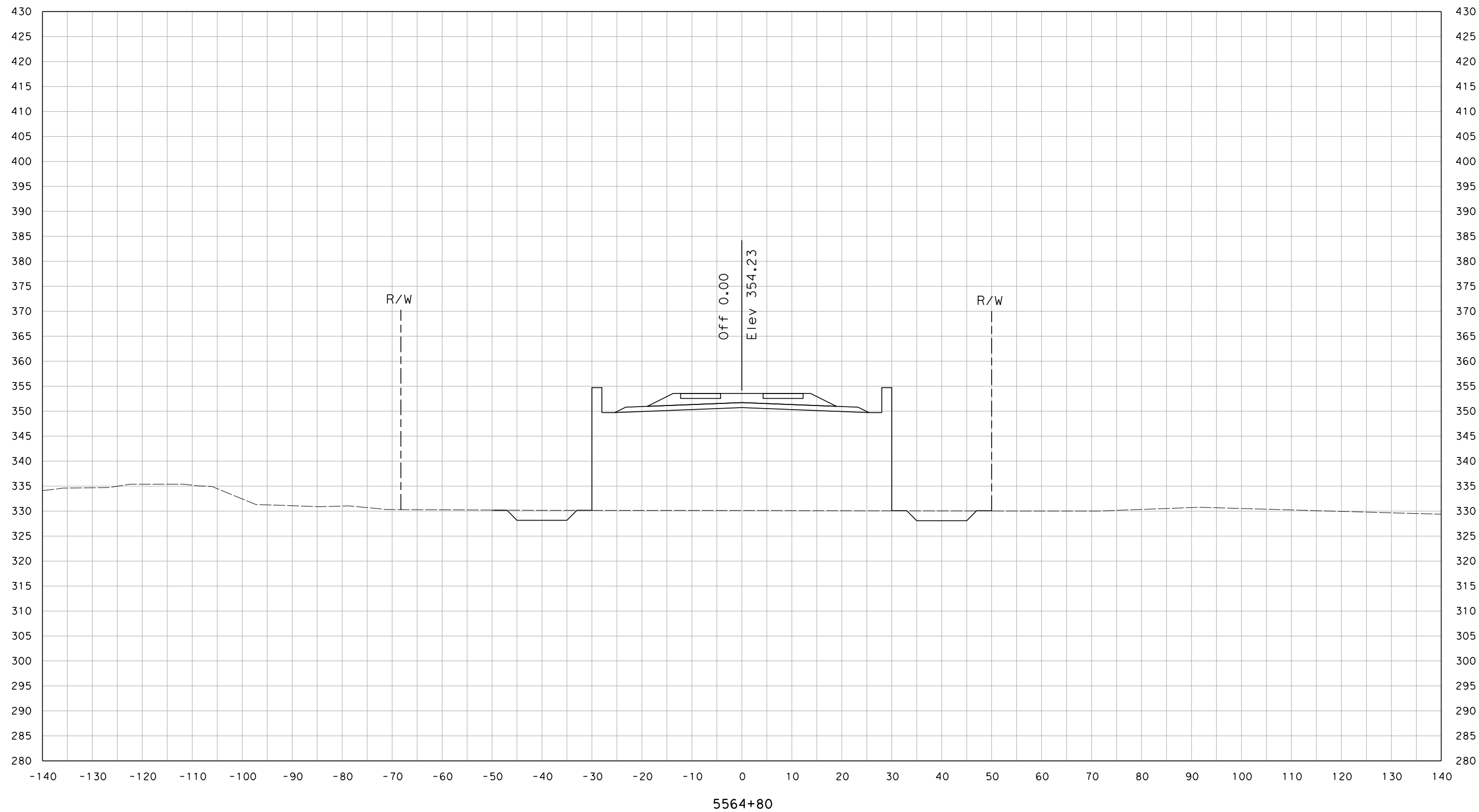
STA. 5550+00 TO STA. 5550+00



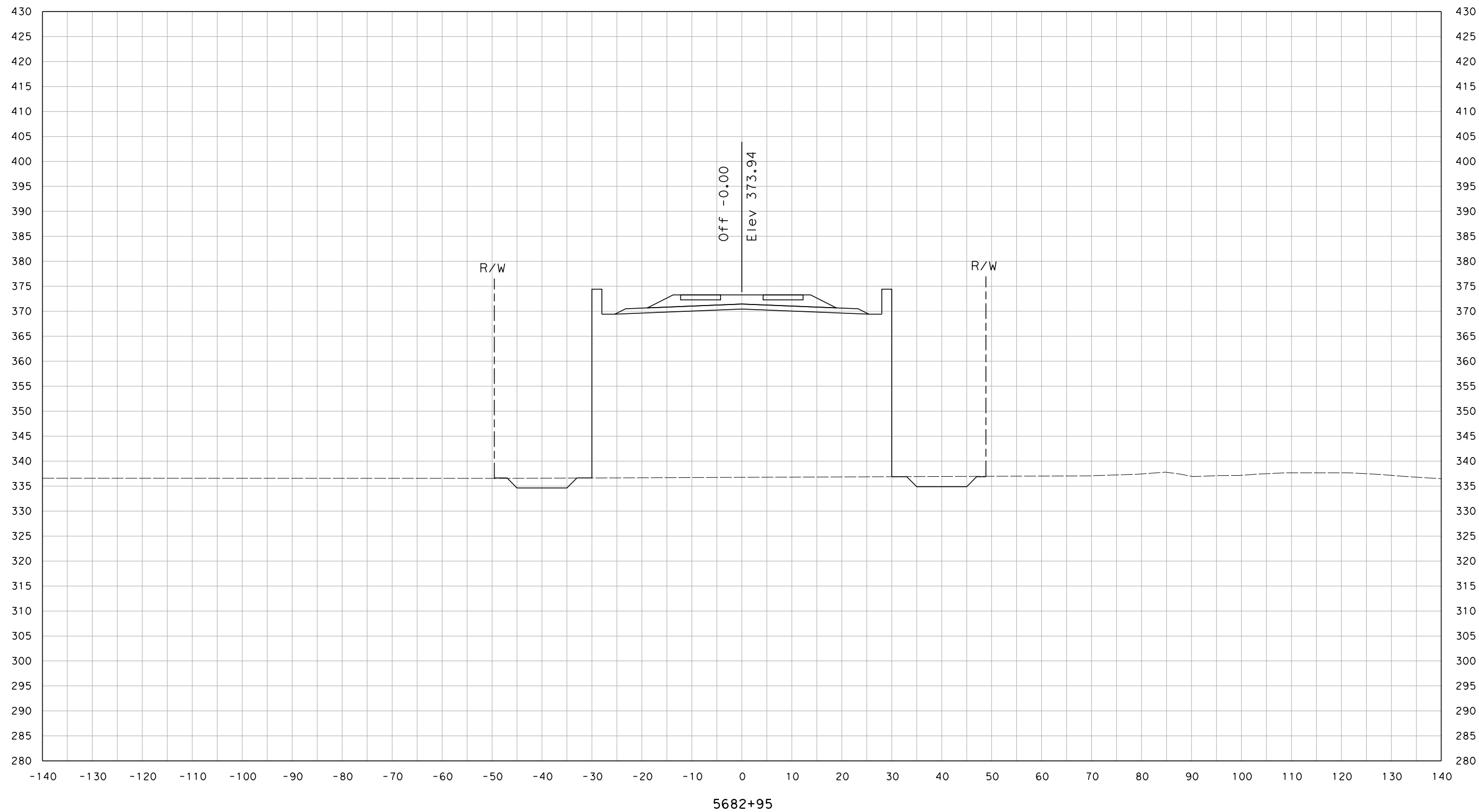
STA. 5555+00 TO STA. 5555+00



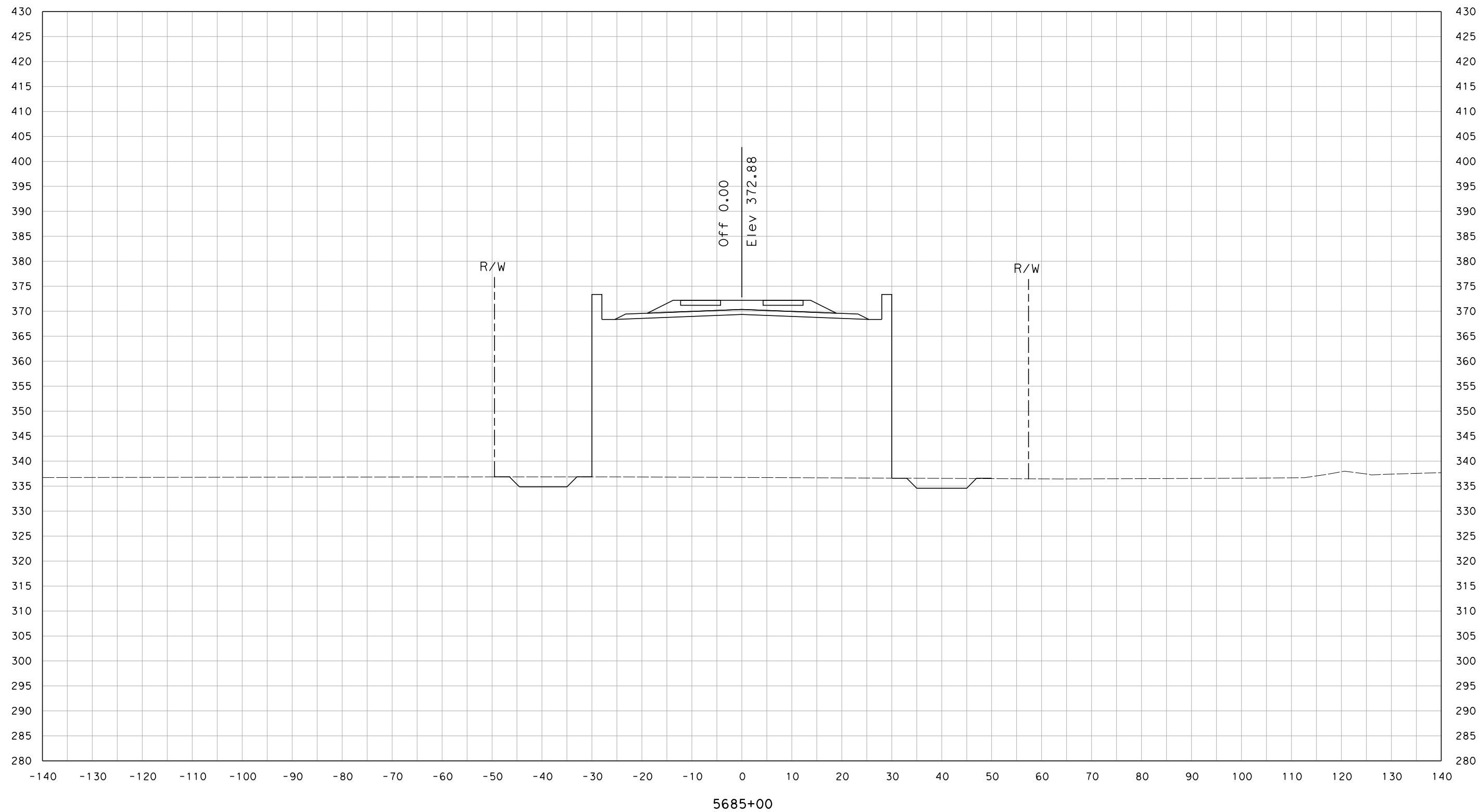
STA. 5560+00 TO STA. 5560+00



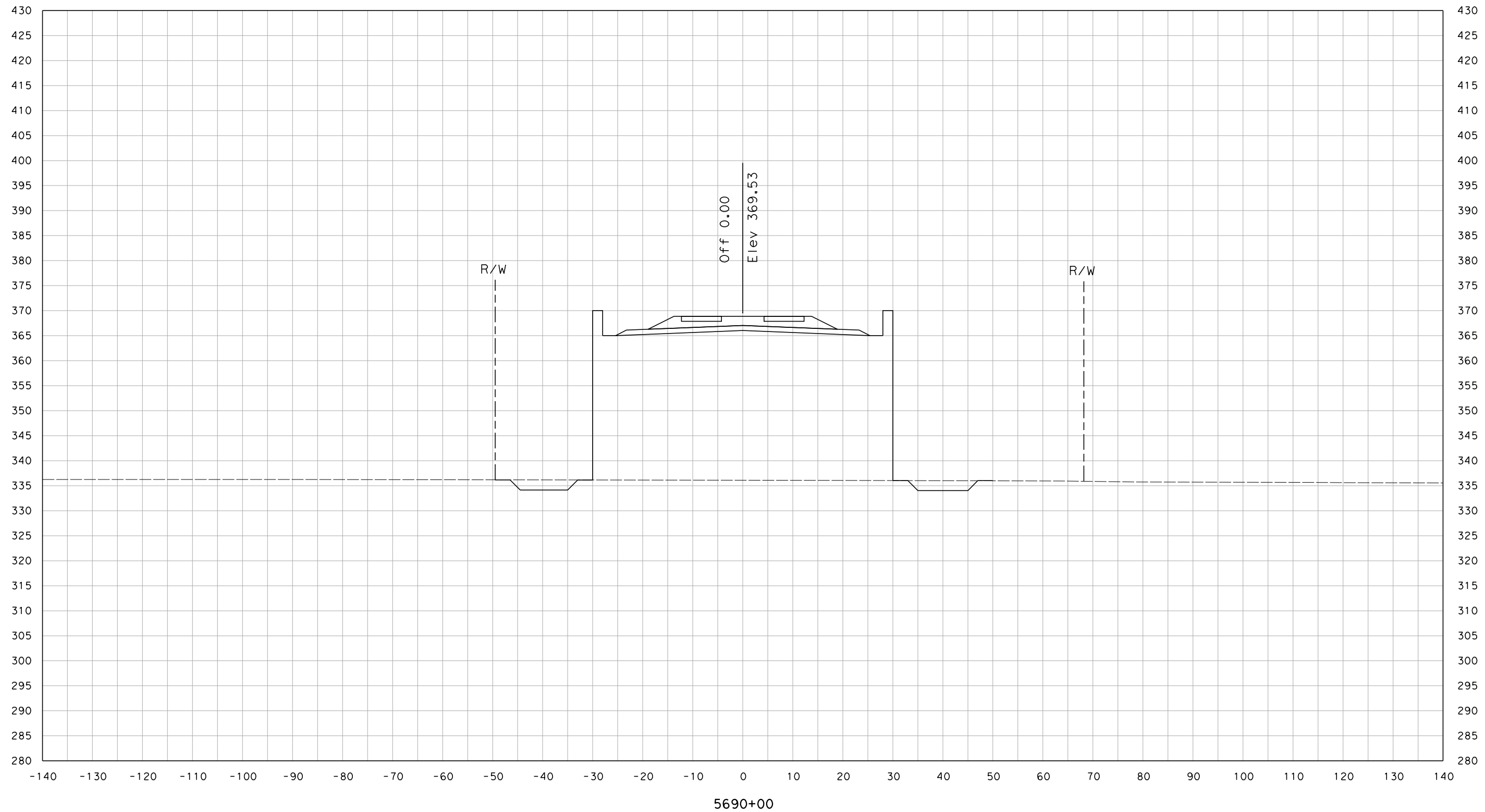
STA. 5564+80 TO STA. 5564+80

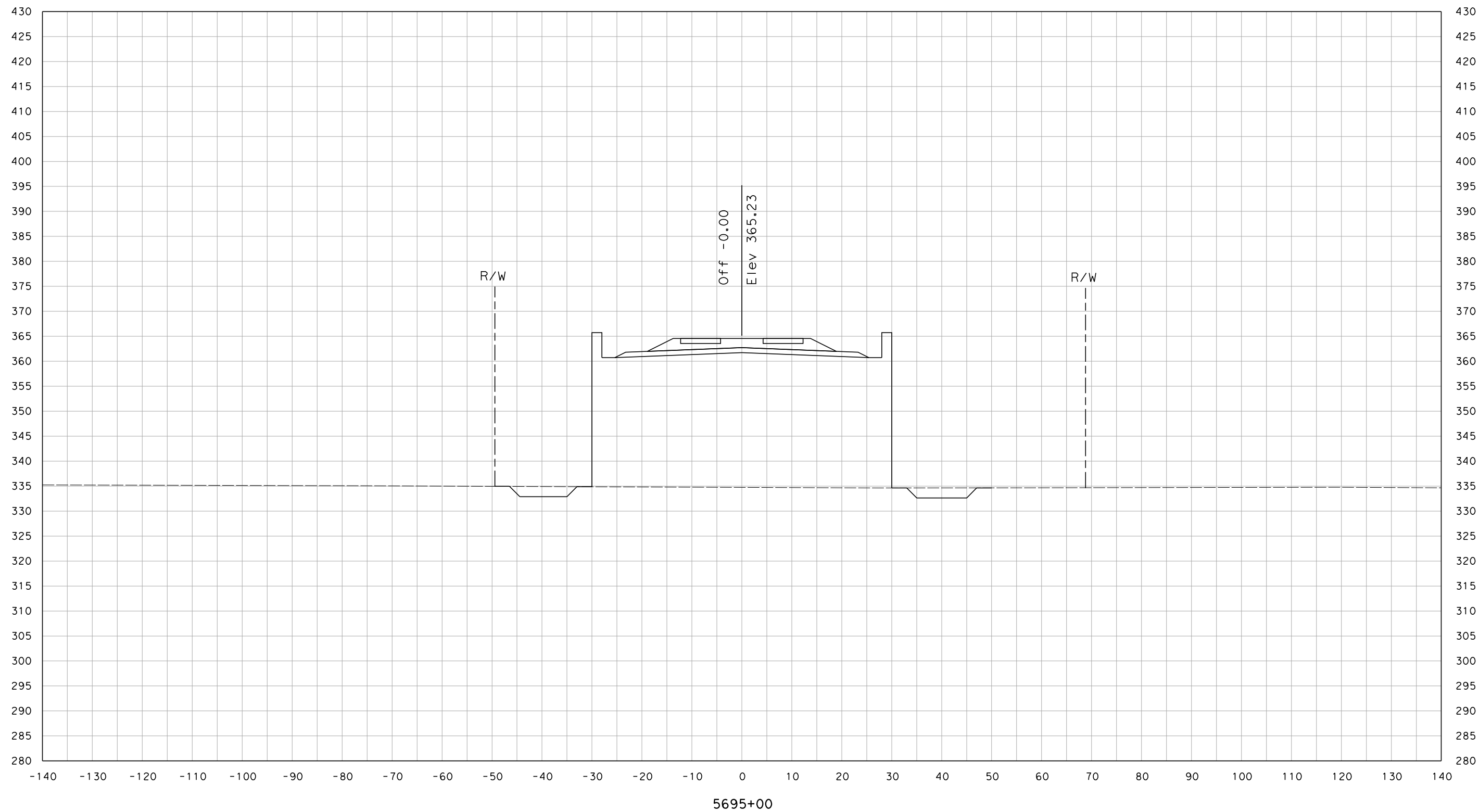


STA. 5682+95 TO STA. 5682+95

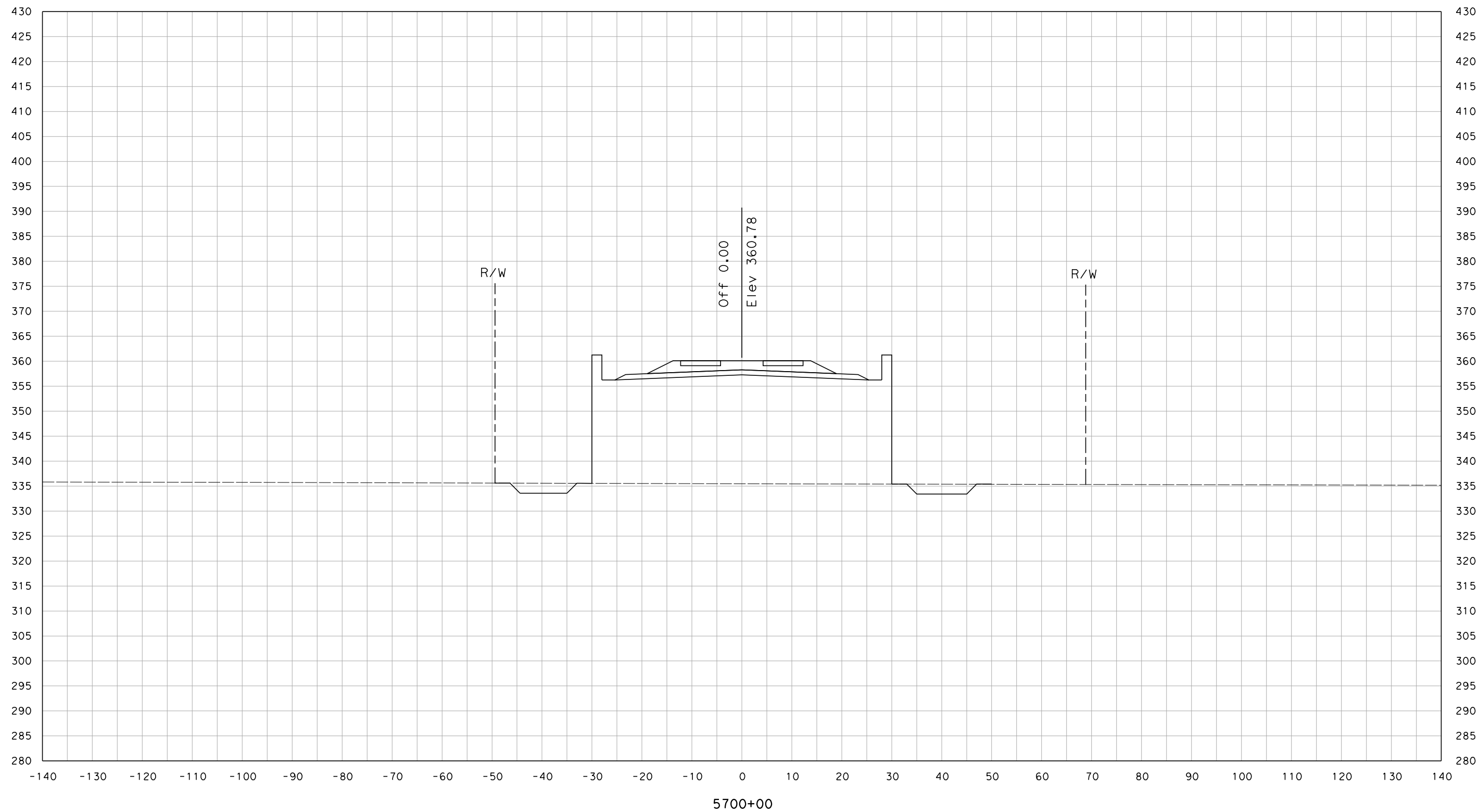


STA. 5685+00 TO STA. 5685+00

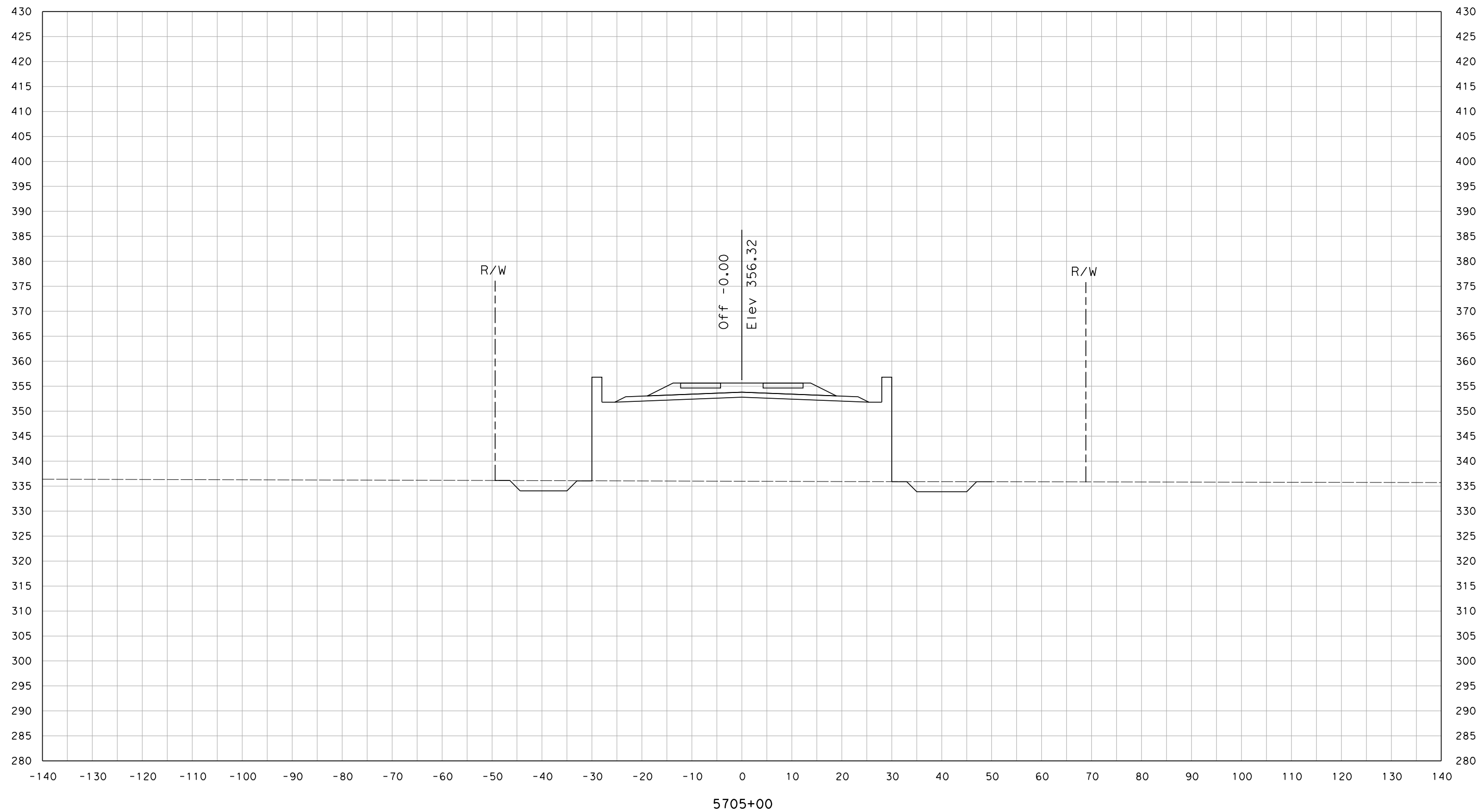




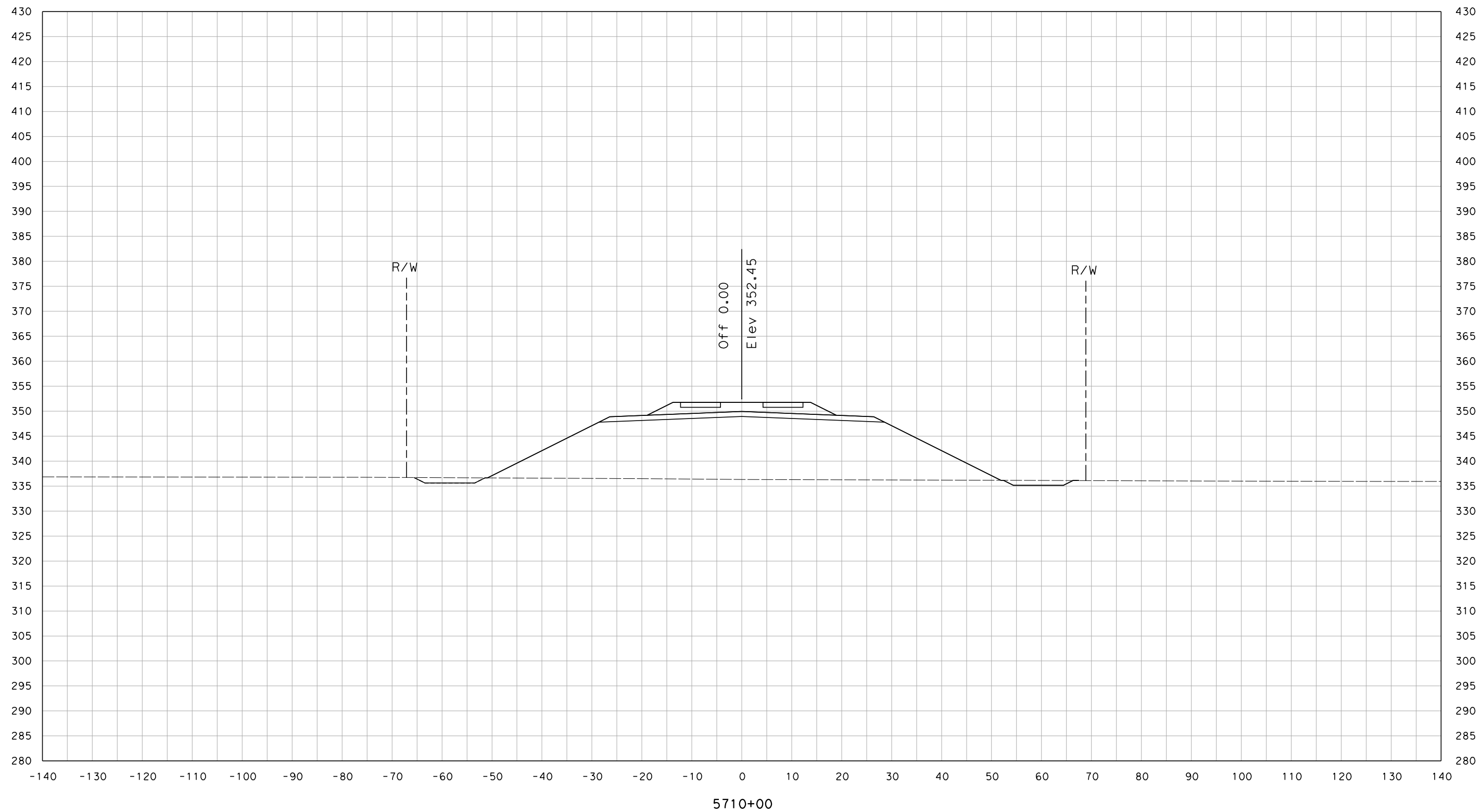
STA. 5695+00 TO STA. 5695+00



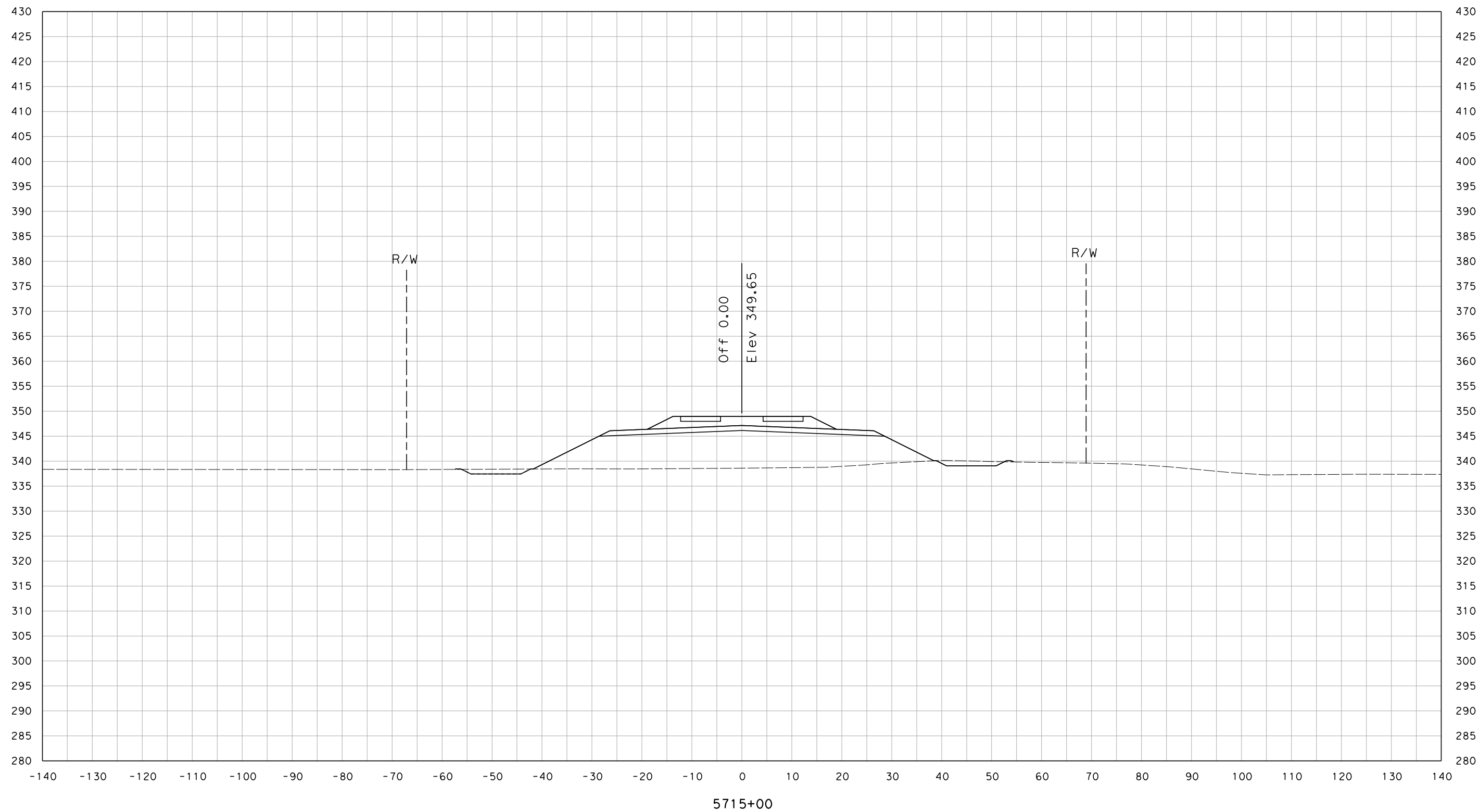
STA. 5700+00 TO STA. 5700+00



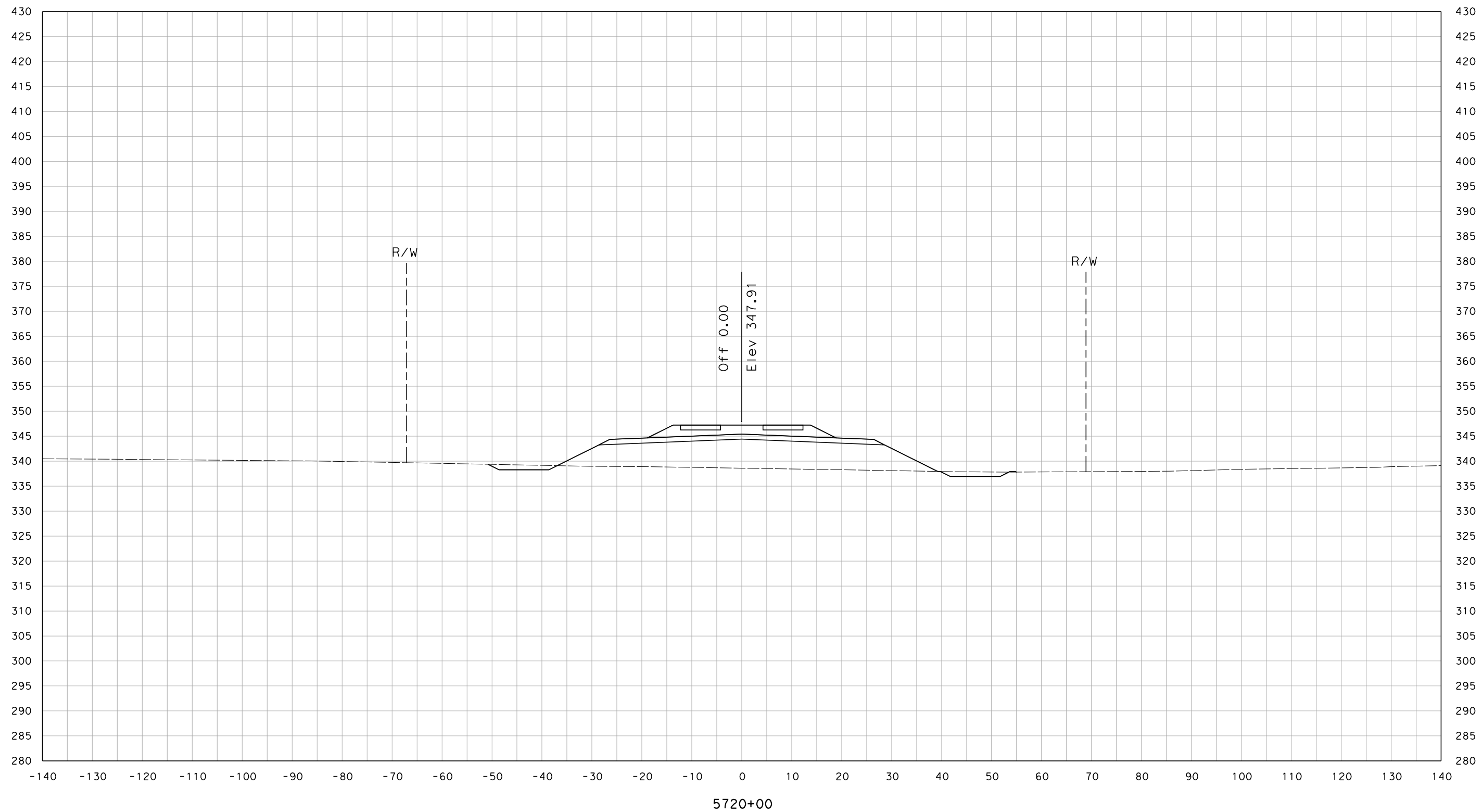
STA. 5705+00 TO STA. 5705+00



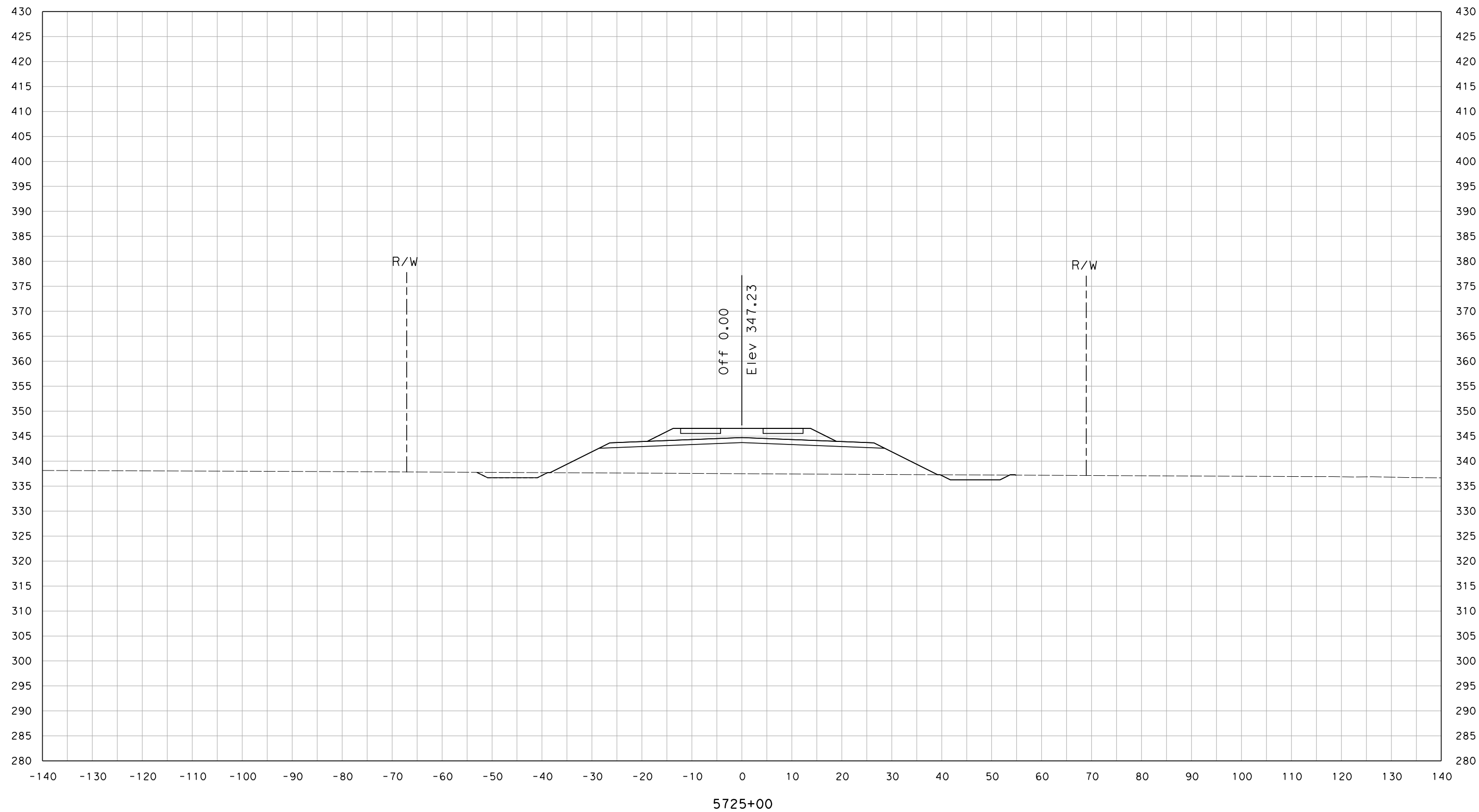
STA. 5710+00 TO STA. 5710+00



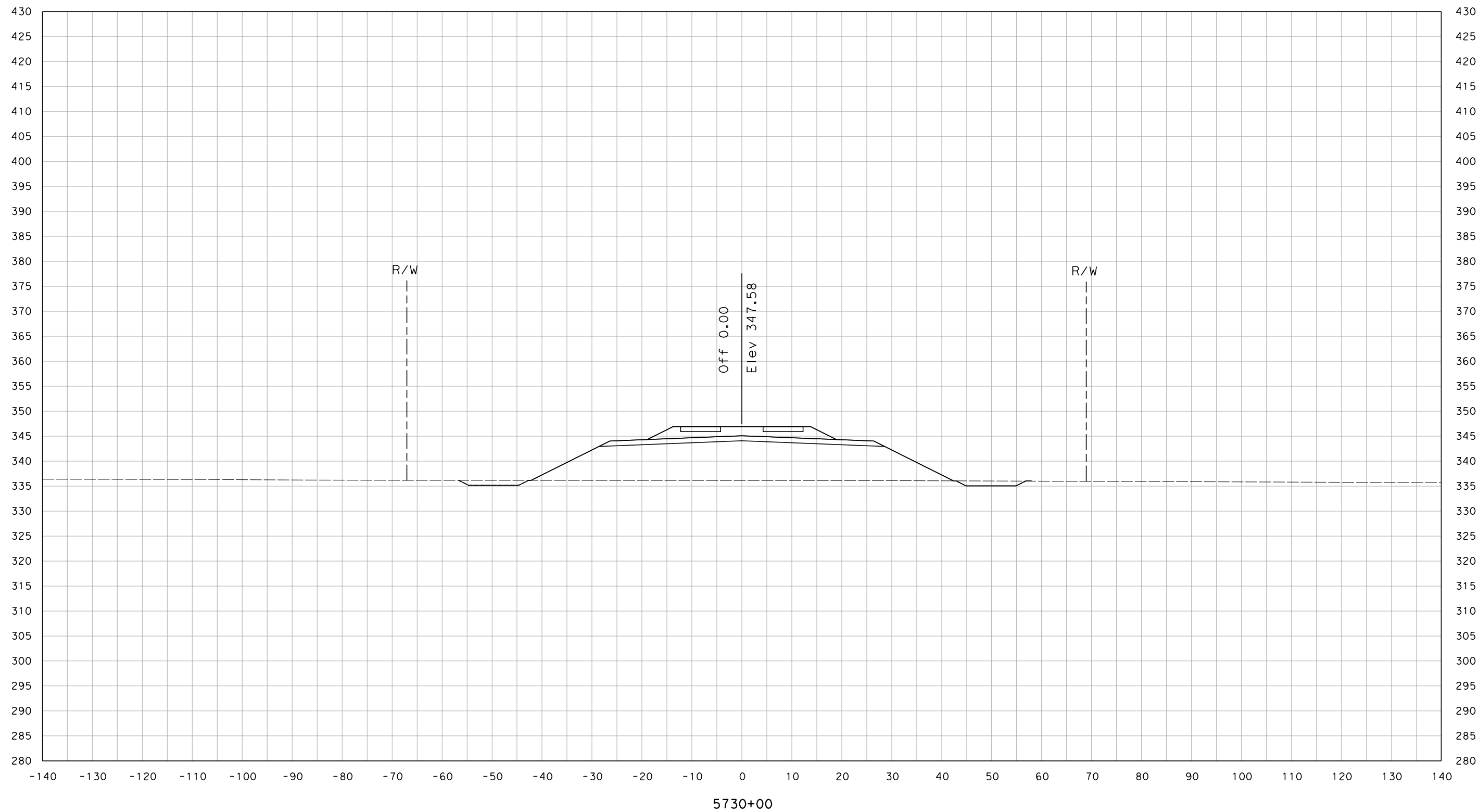
STA. 5715+00 TO STA. 5715+00



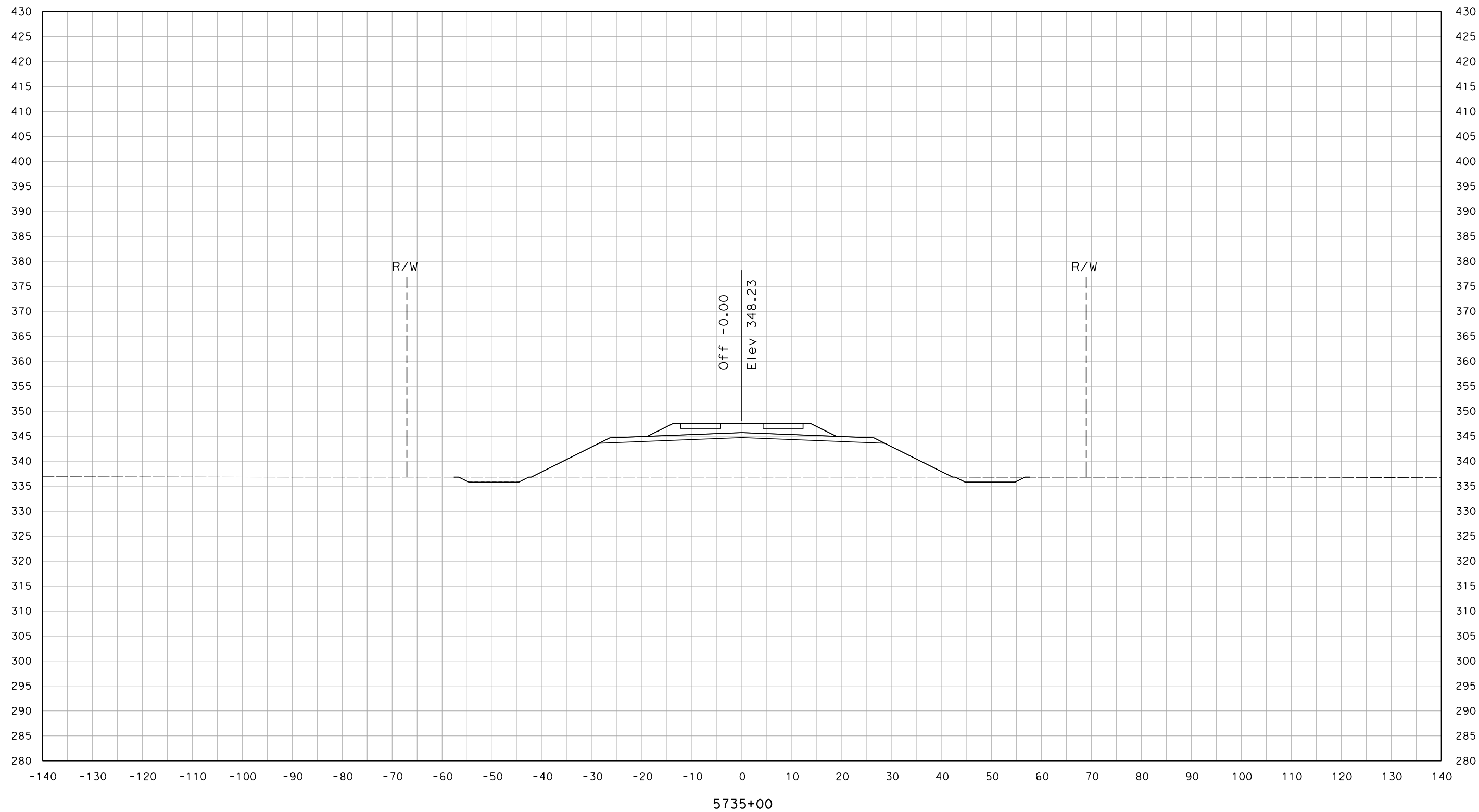
STA. 5720+00 TO STA. 5720+00



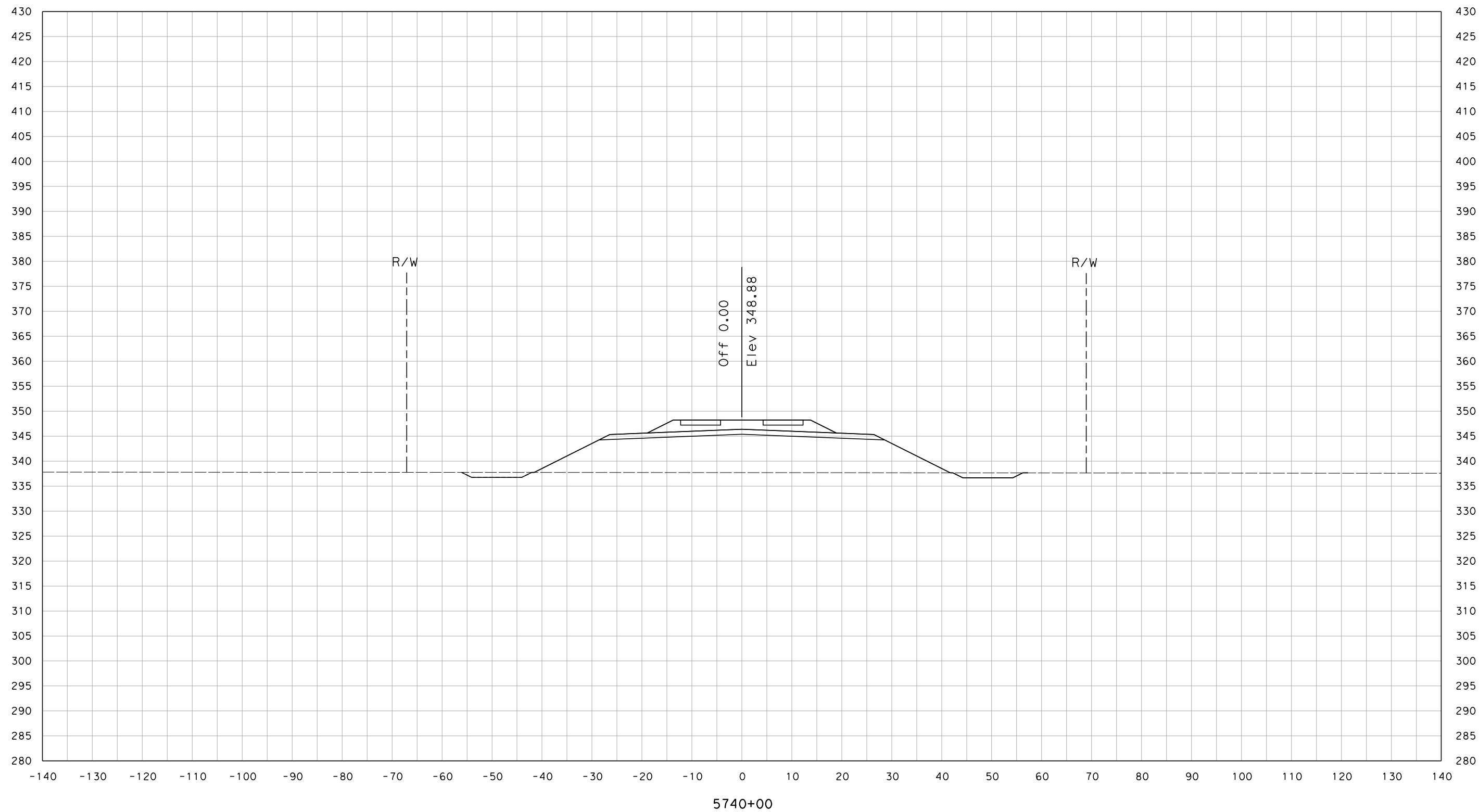
STA. 5725+00 TO STA. 5725+00



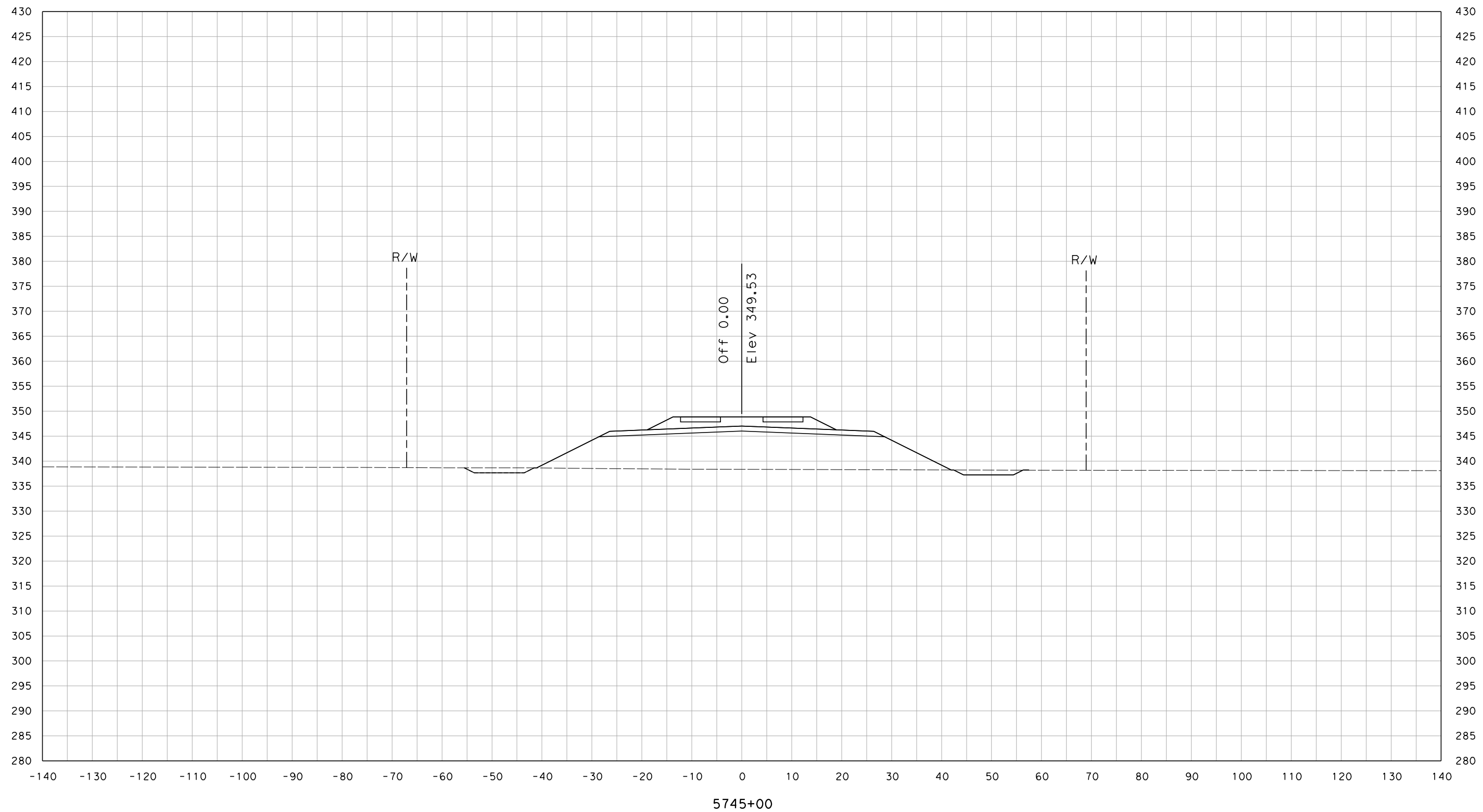
STA. 5730+00 TO STA. 5730+00



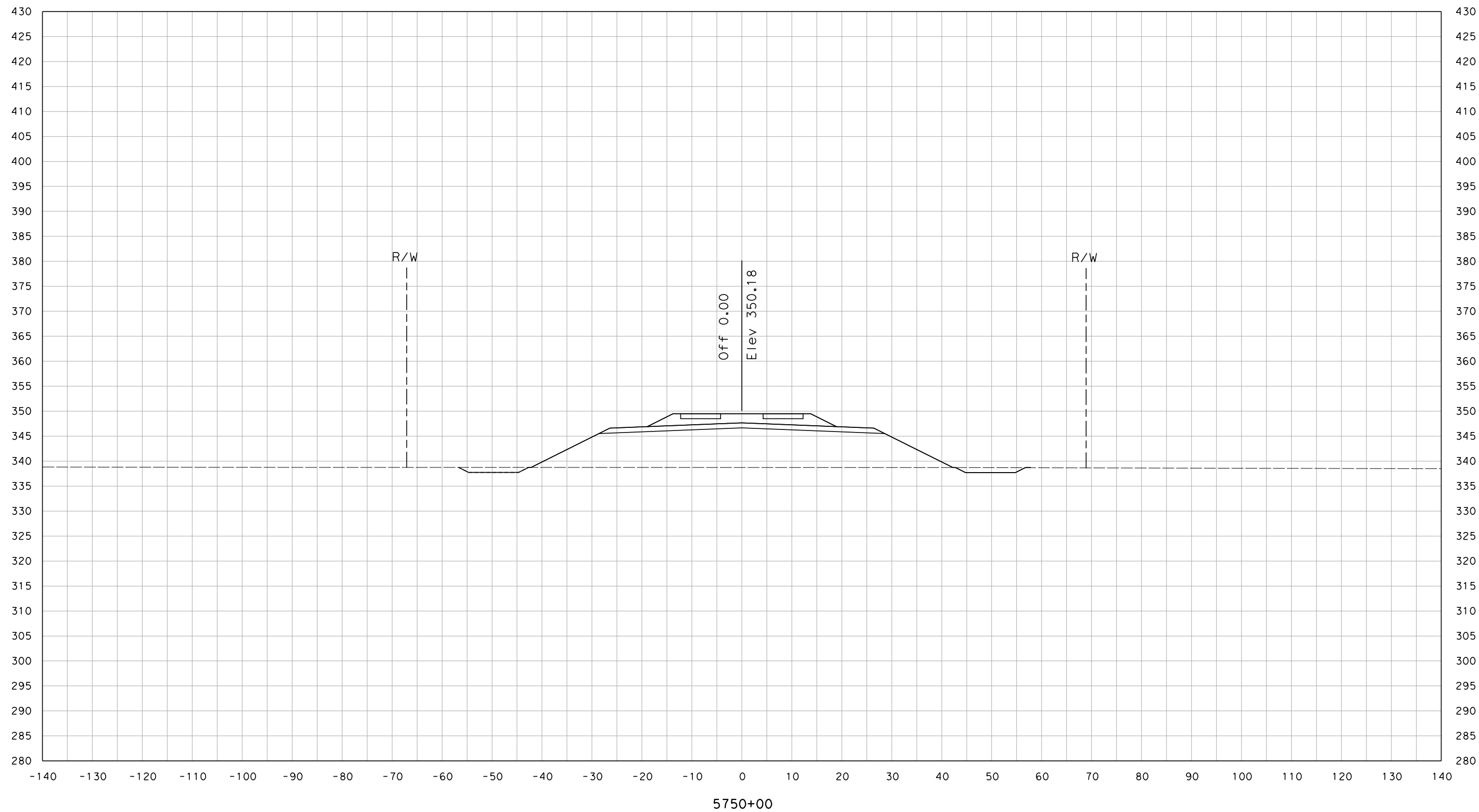
STA. 5735+00 TO STA. 5735+00



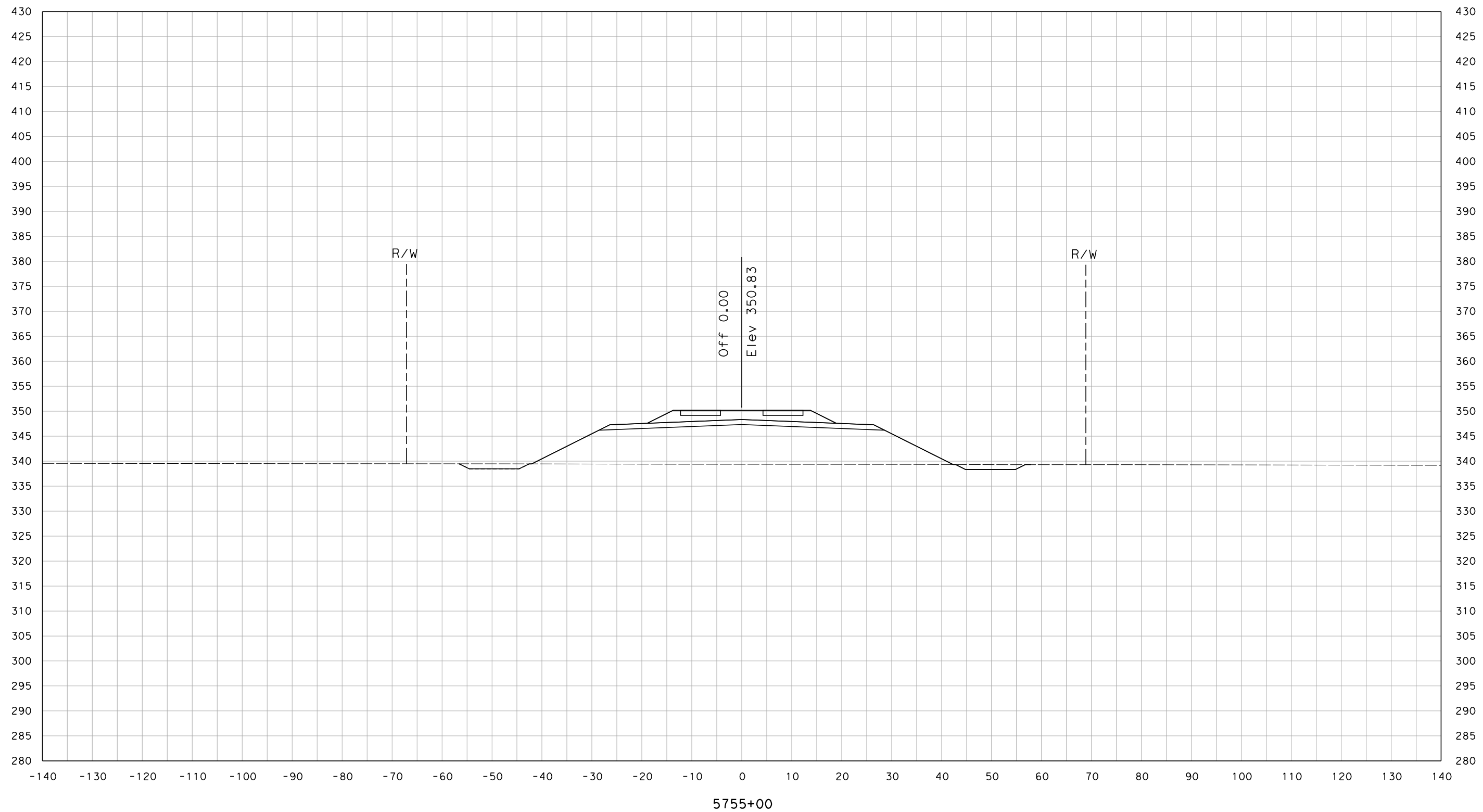
STA. 5740+00 TO STA. 5740+00



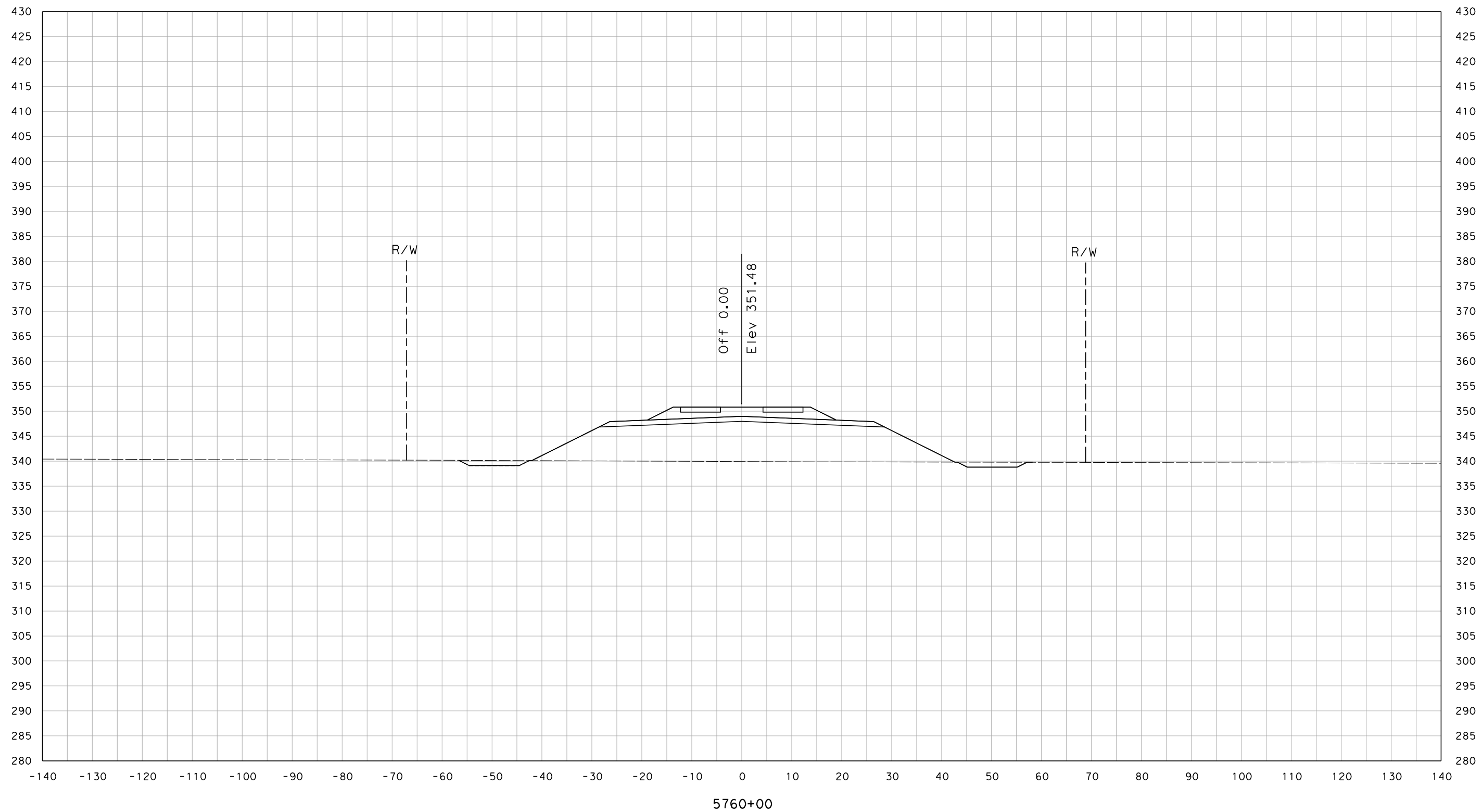
STA. 5745+00 TO STA. 5745+00



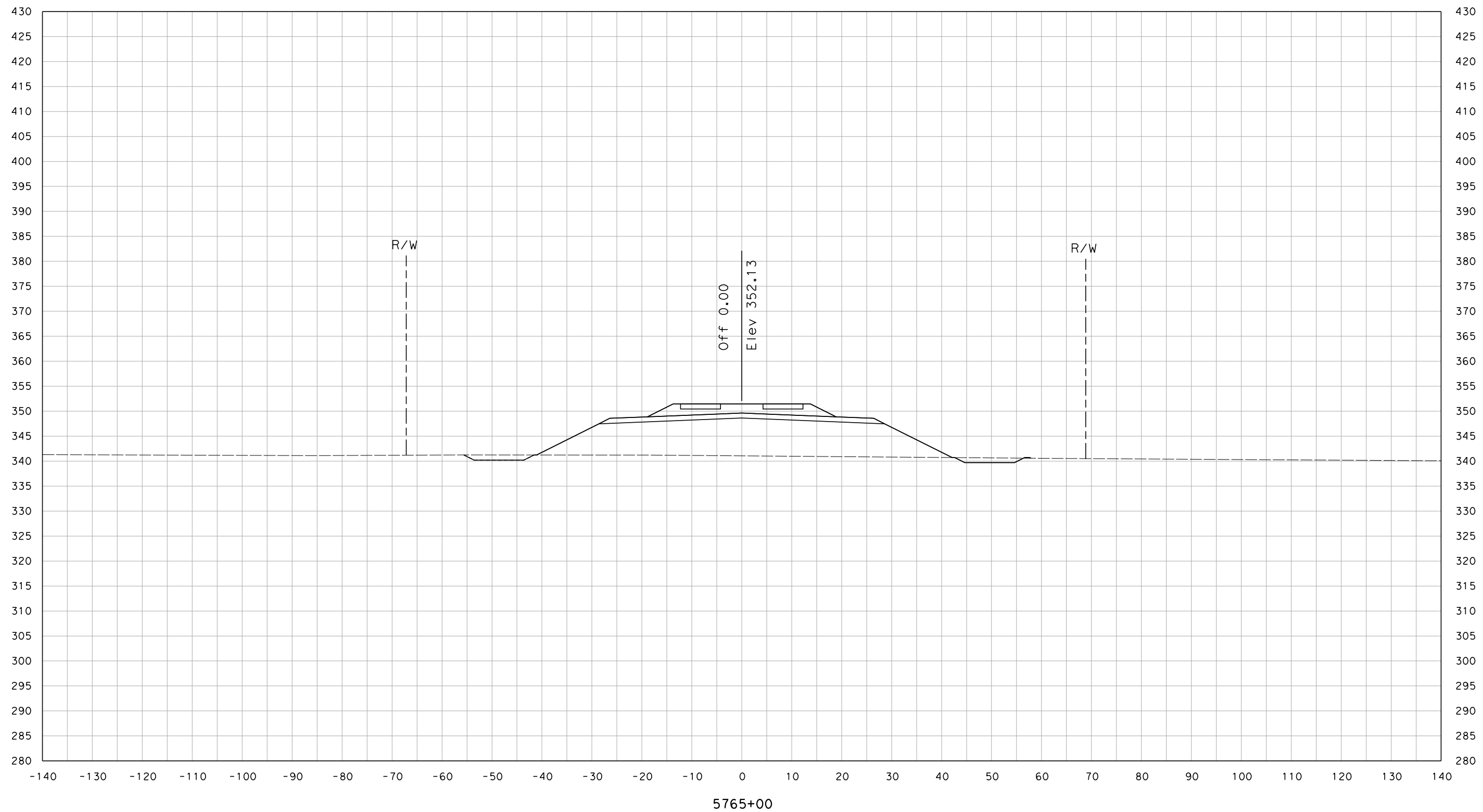
STA. 5750+00 TO STA. 5750+00



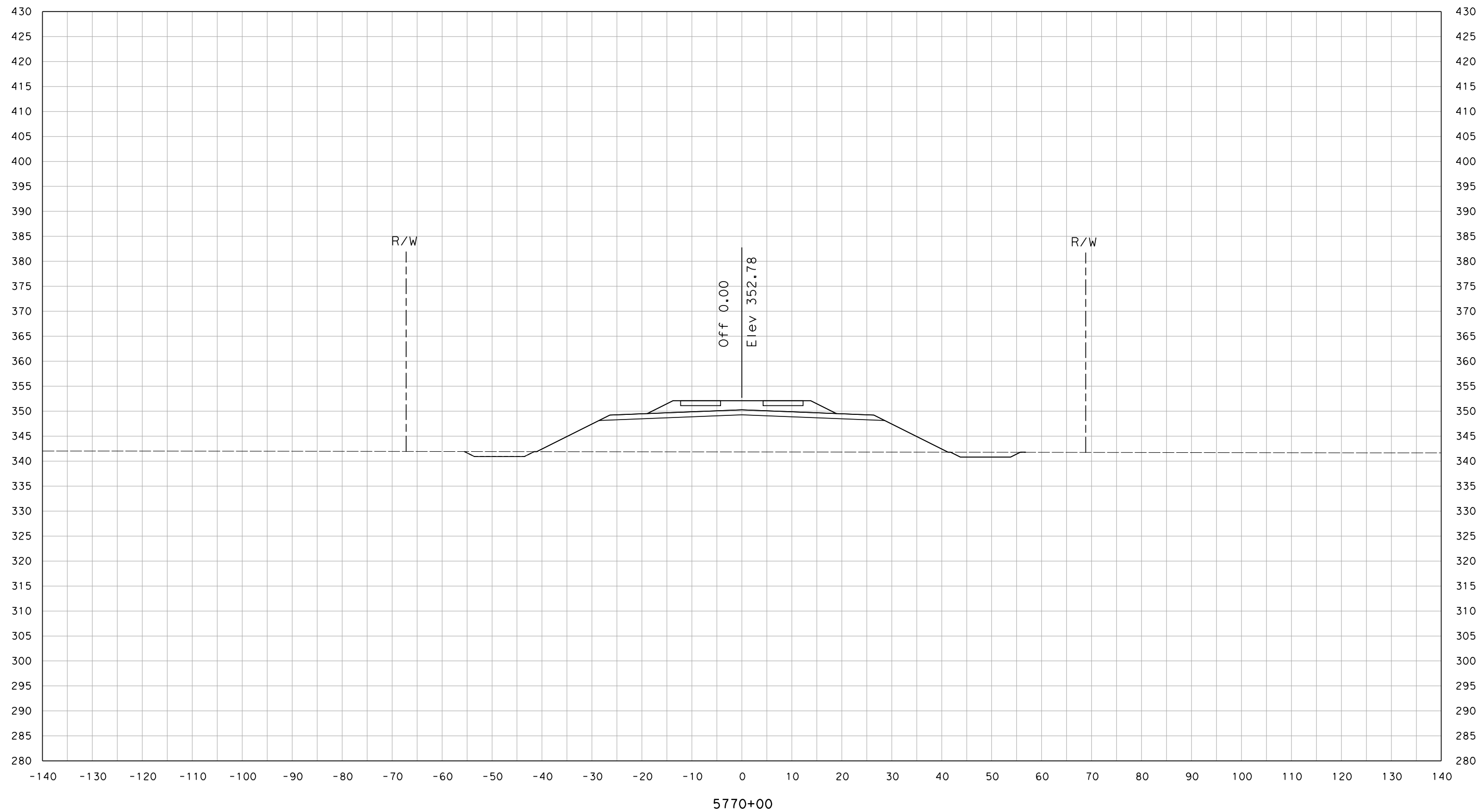
STA. 5755+00 TO STA. 5755+00



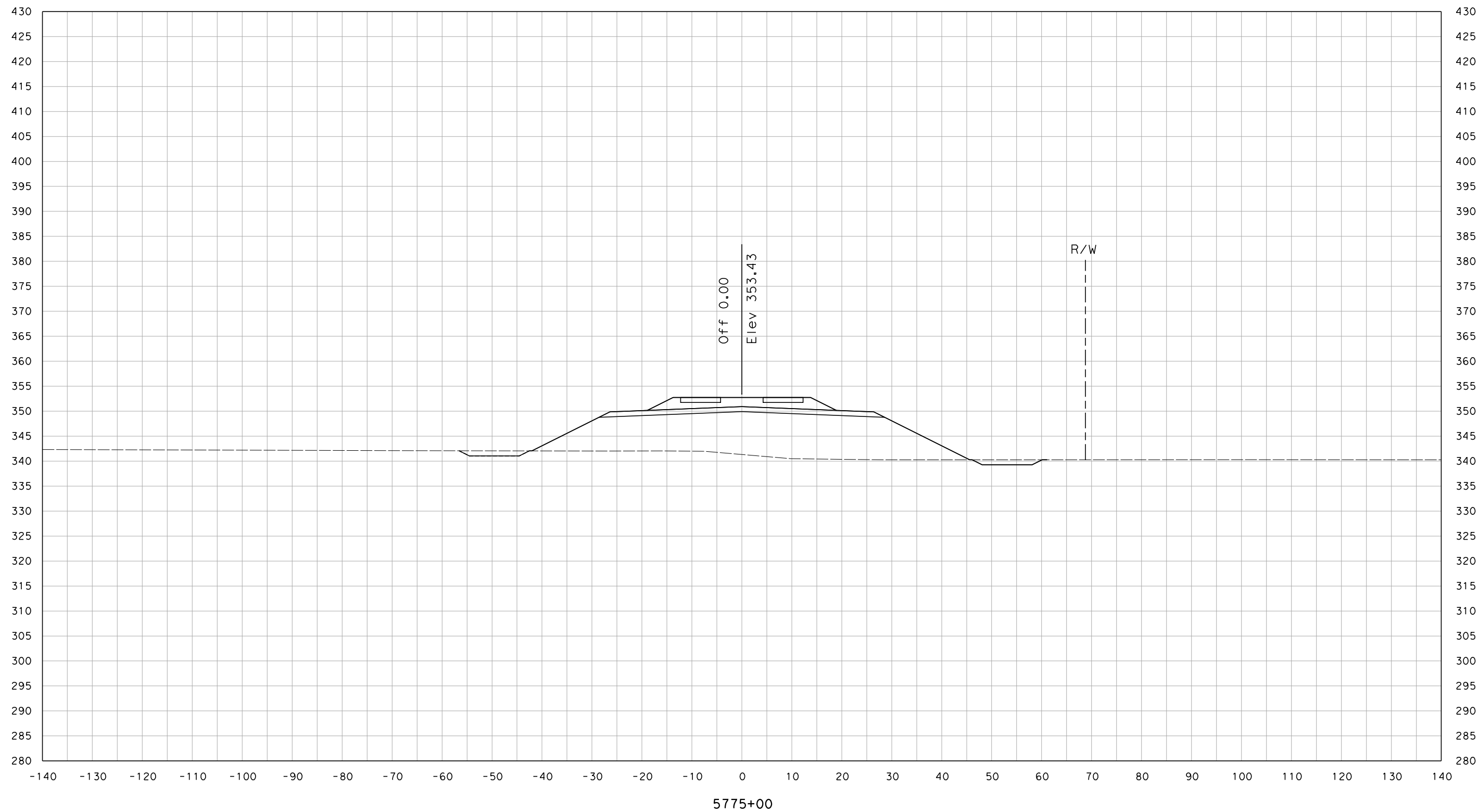
STA. 5760+00 TO STA. 5760+00



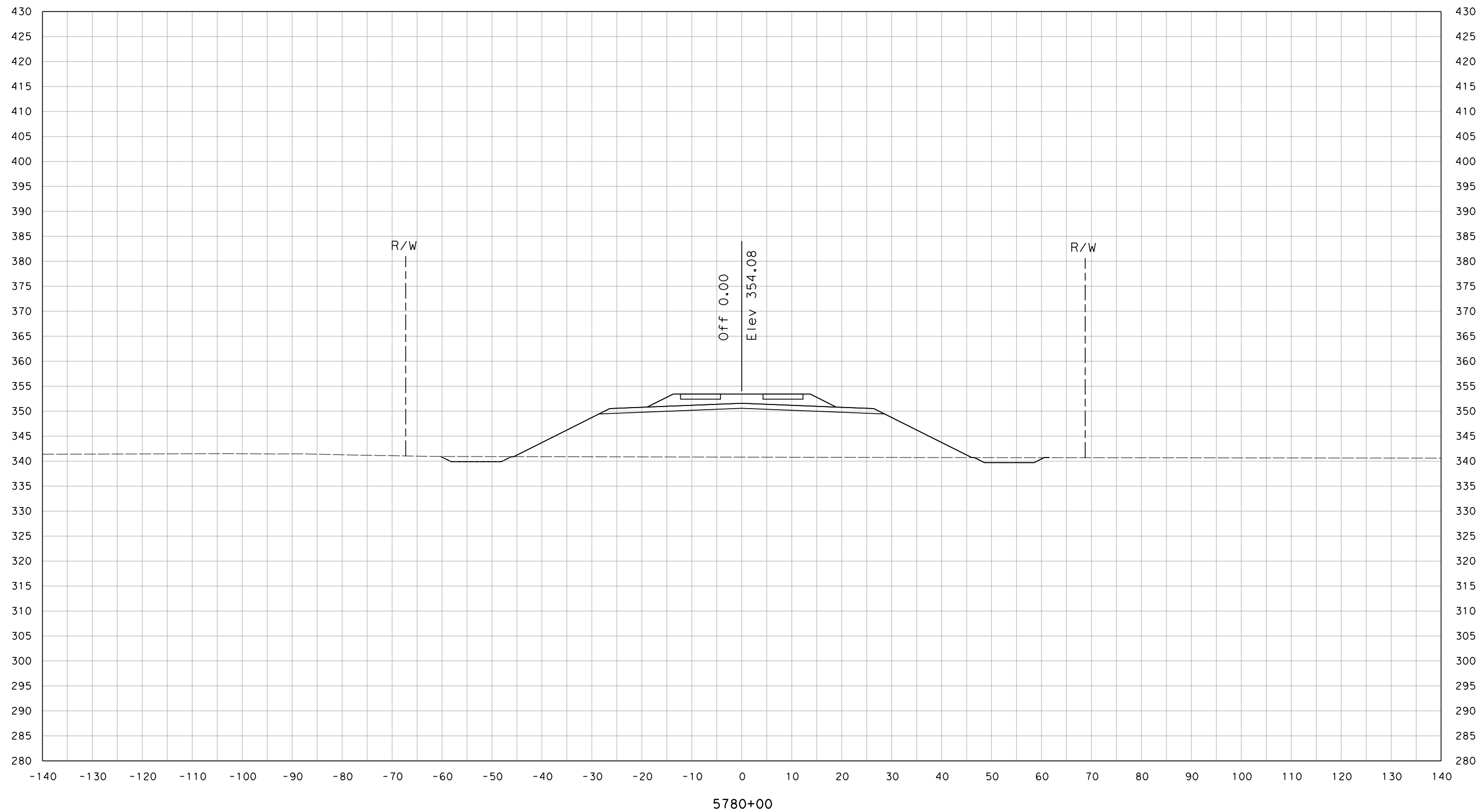
STA. 5765+00 TO STA. 5765+00



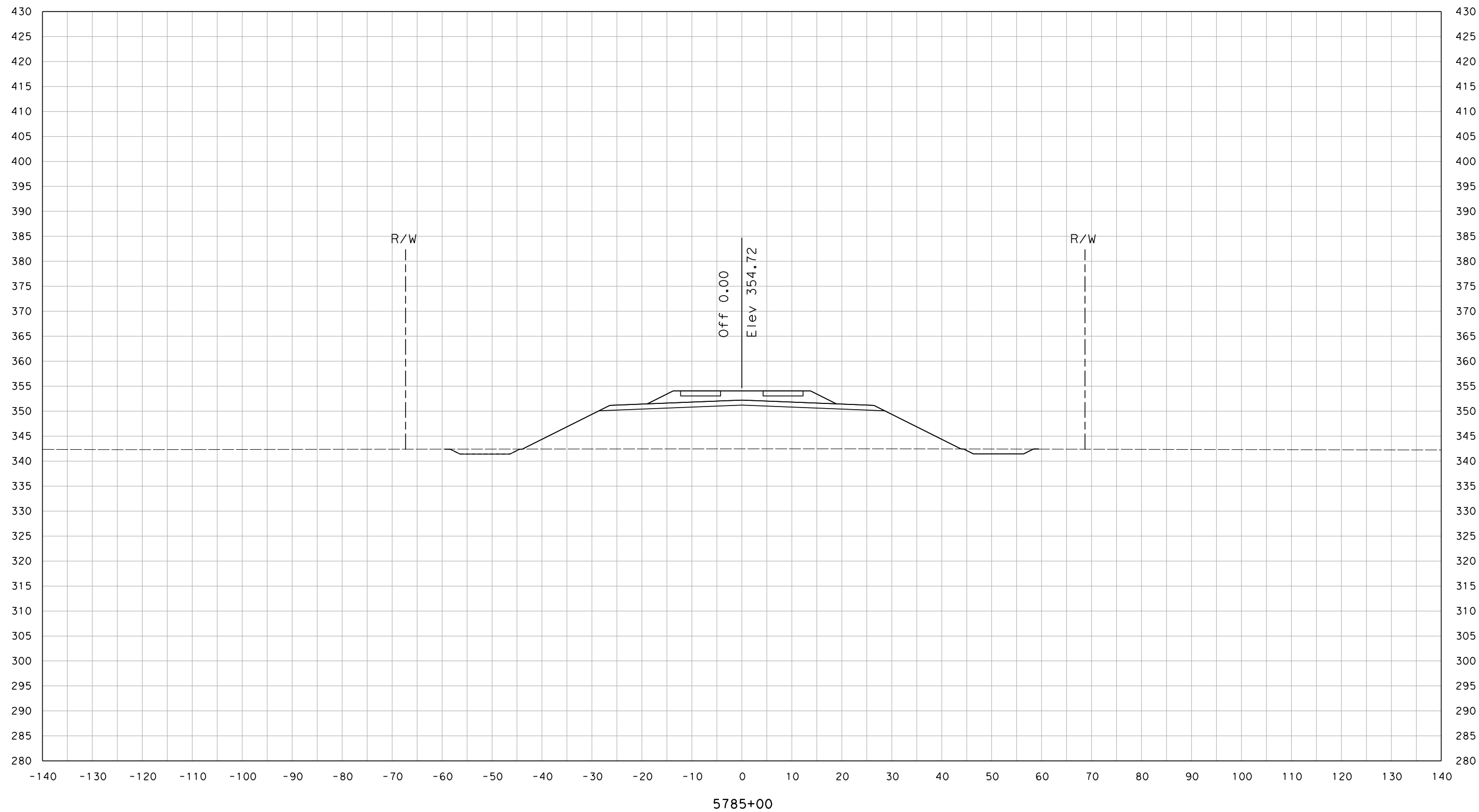
STA. 5770+00 TO STA. 5770+00



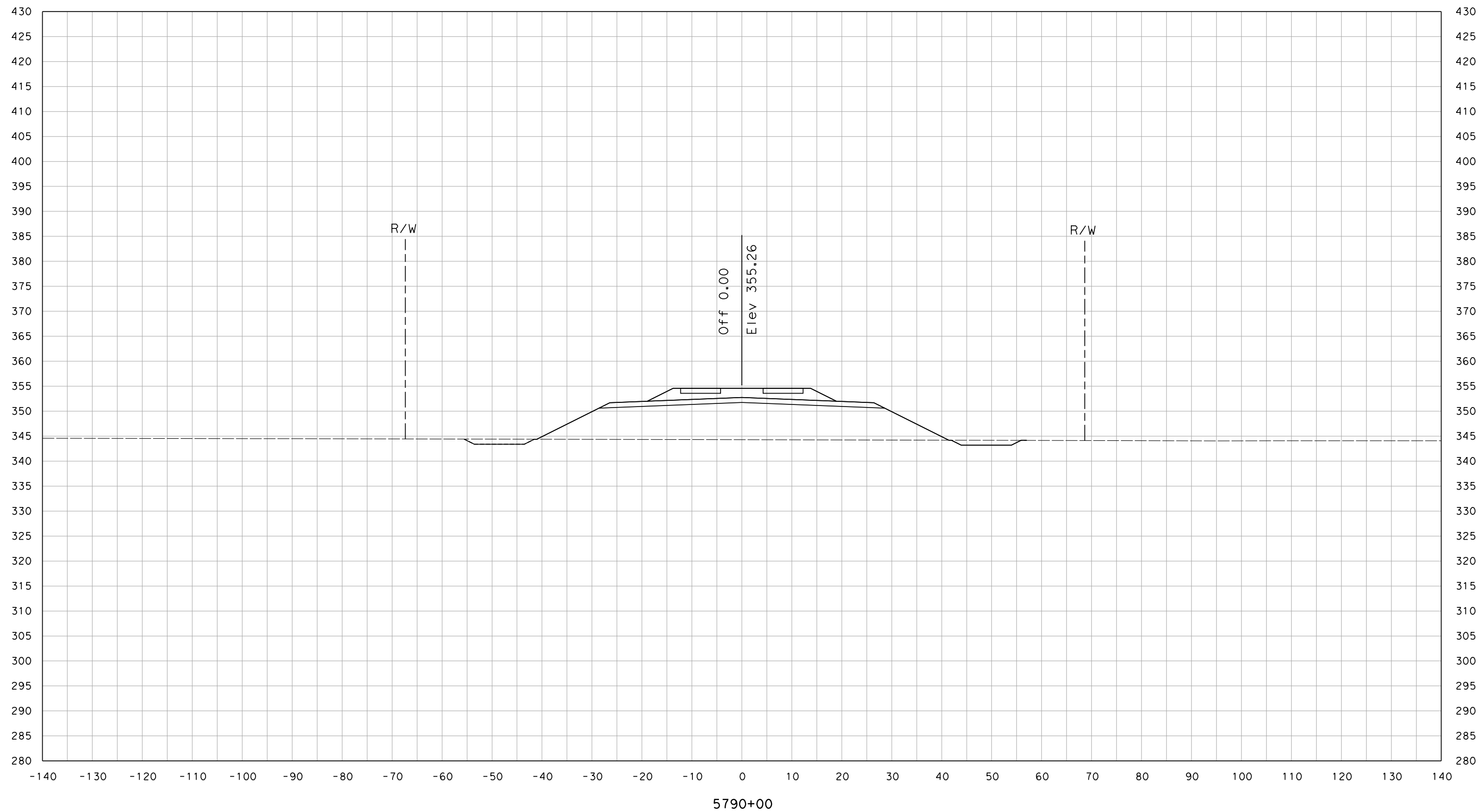
STA. 5775+00 TO STA. 5775+00



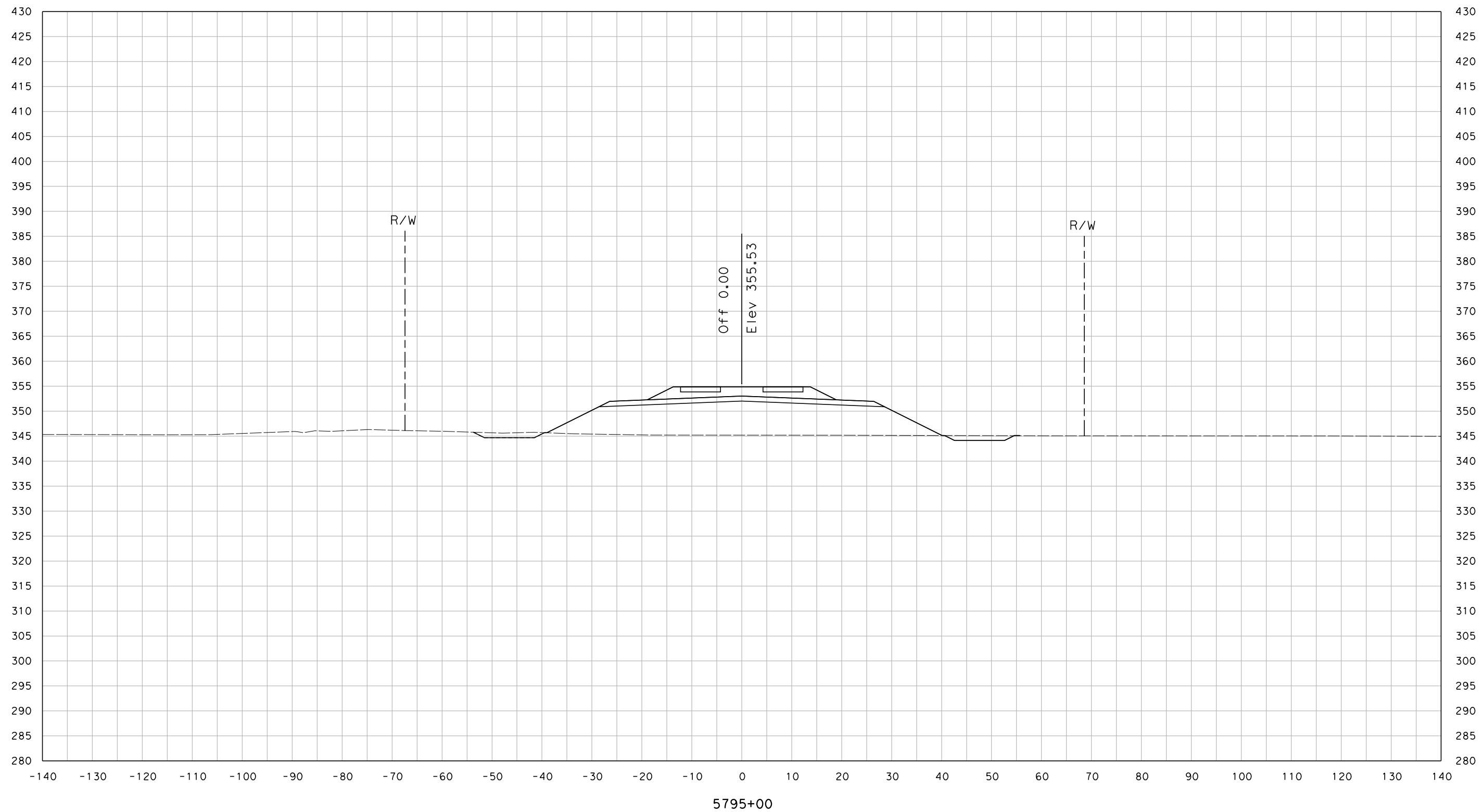
STA. 5780+00 TO STA. 5780+00



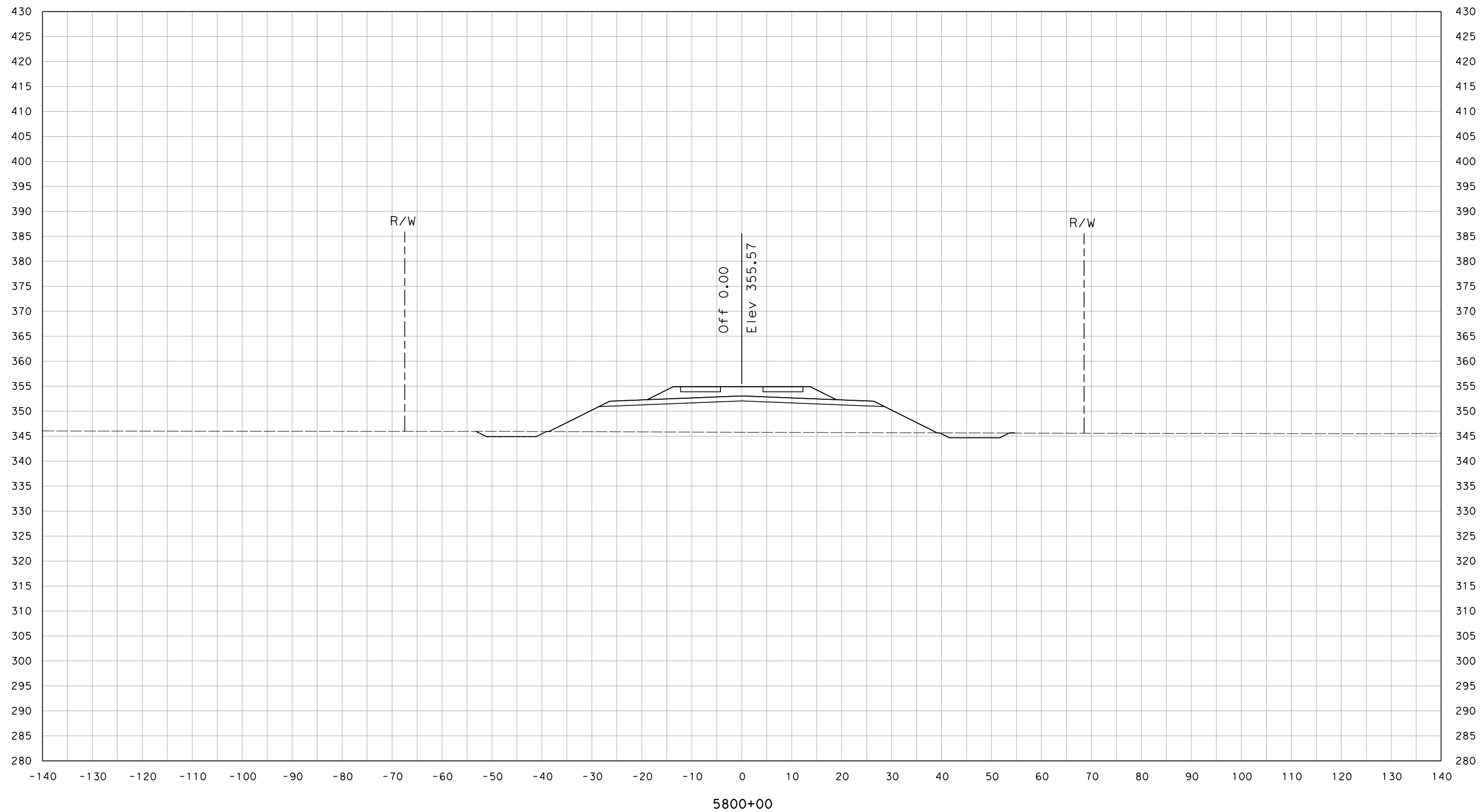
STA. 5785+00 TO STA. 5785+00



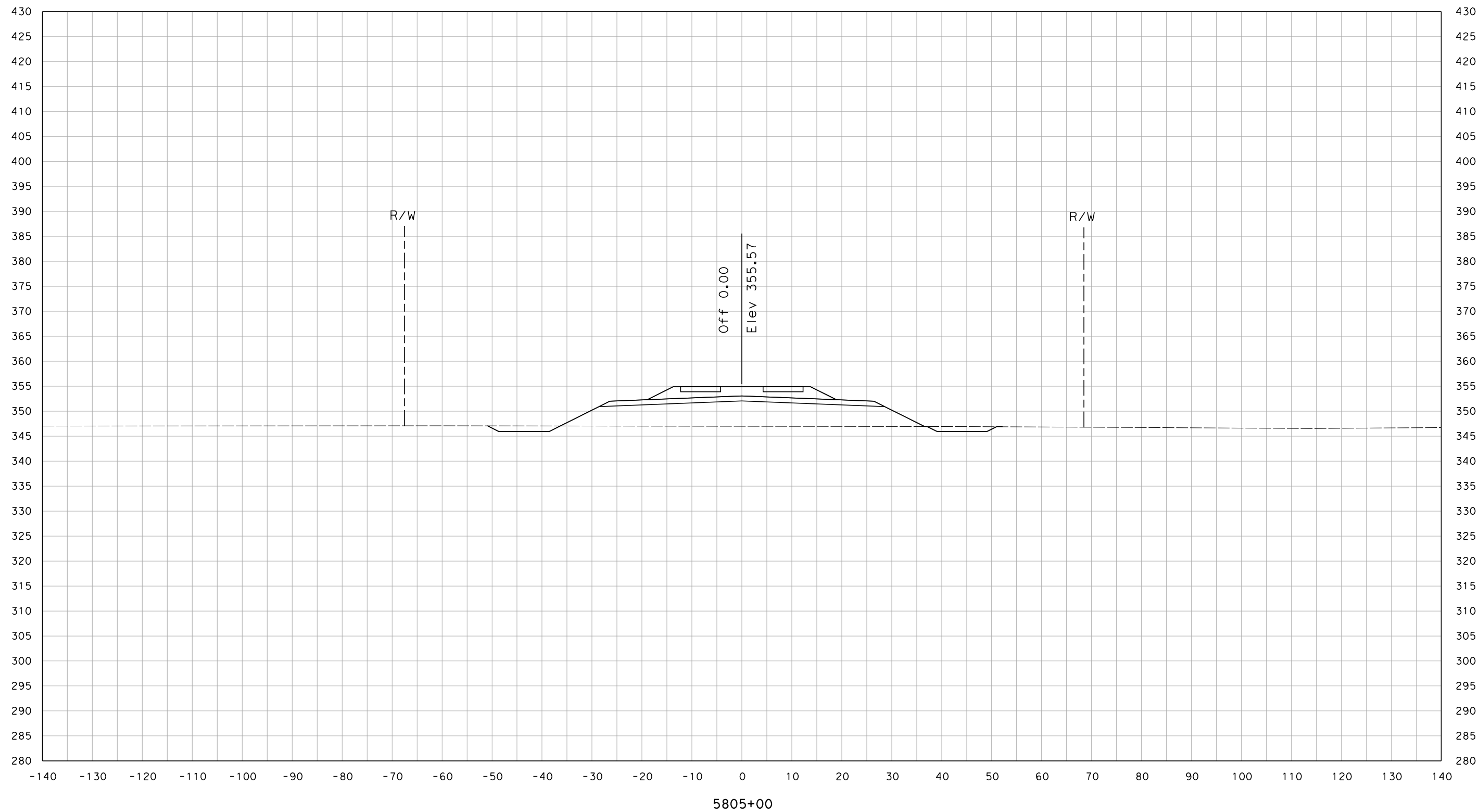
STA. 5790+00 TO STA. 5790+00



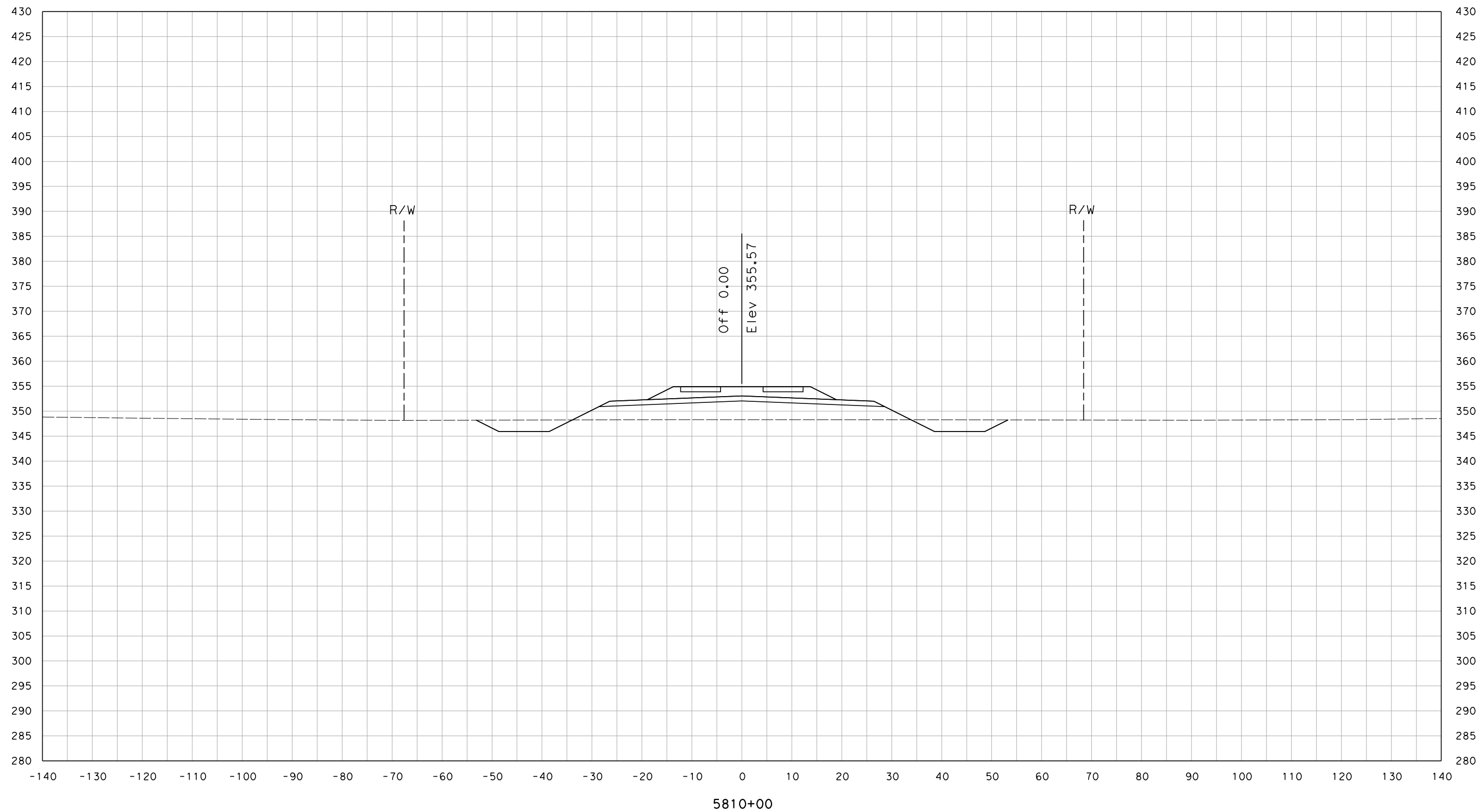
STA. 5795+00 TO STA. 5795+00



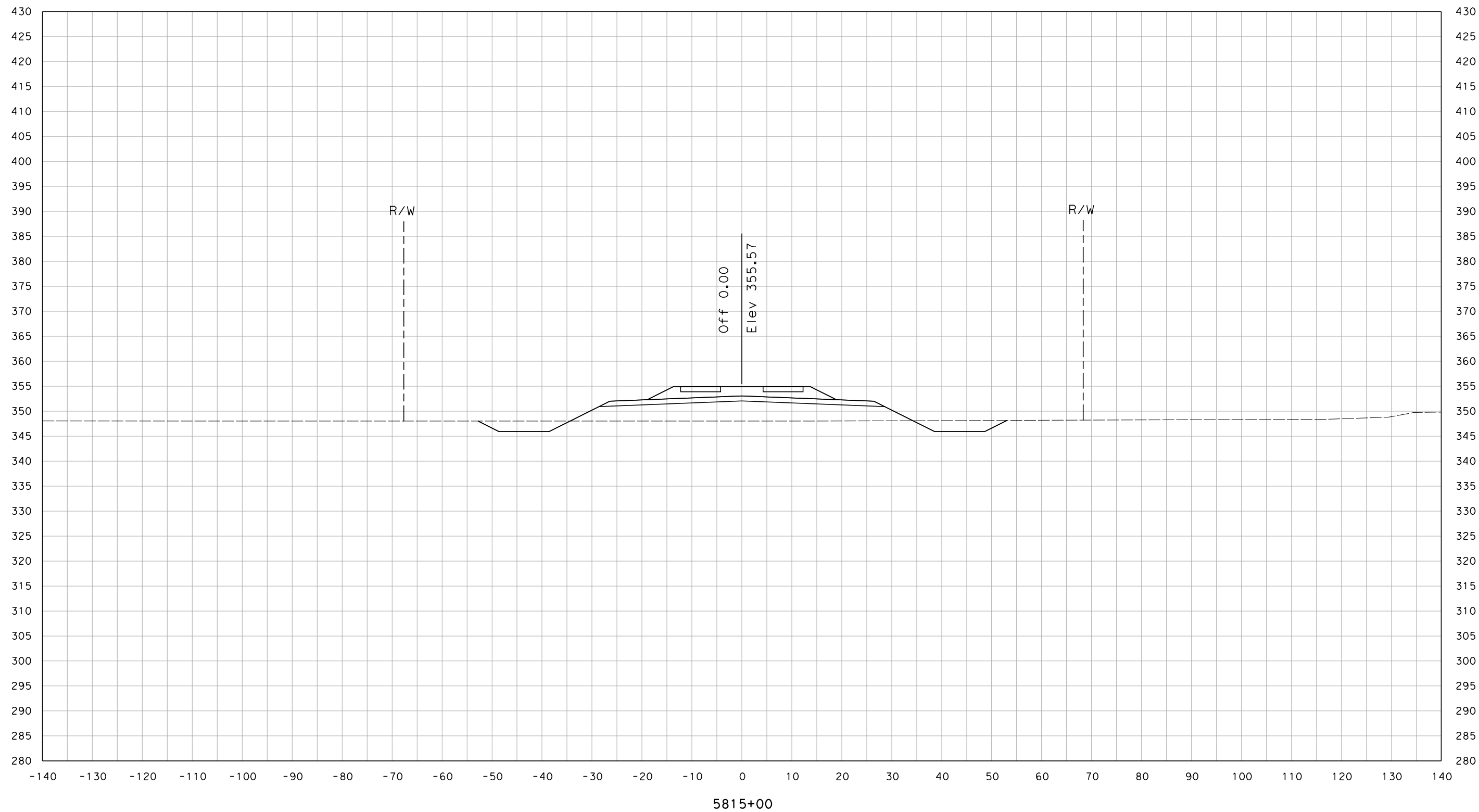
STA. 5800+00 TO STA. 5800+00



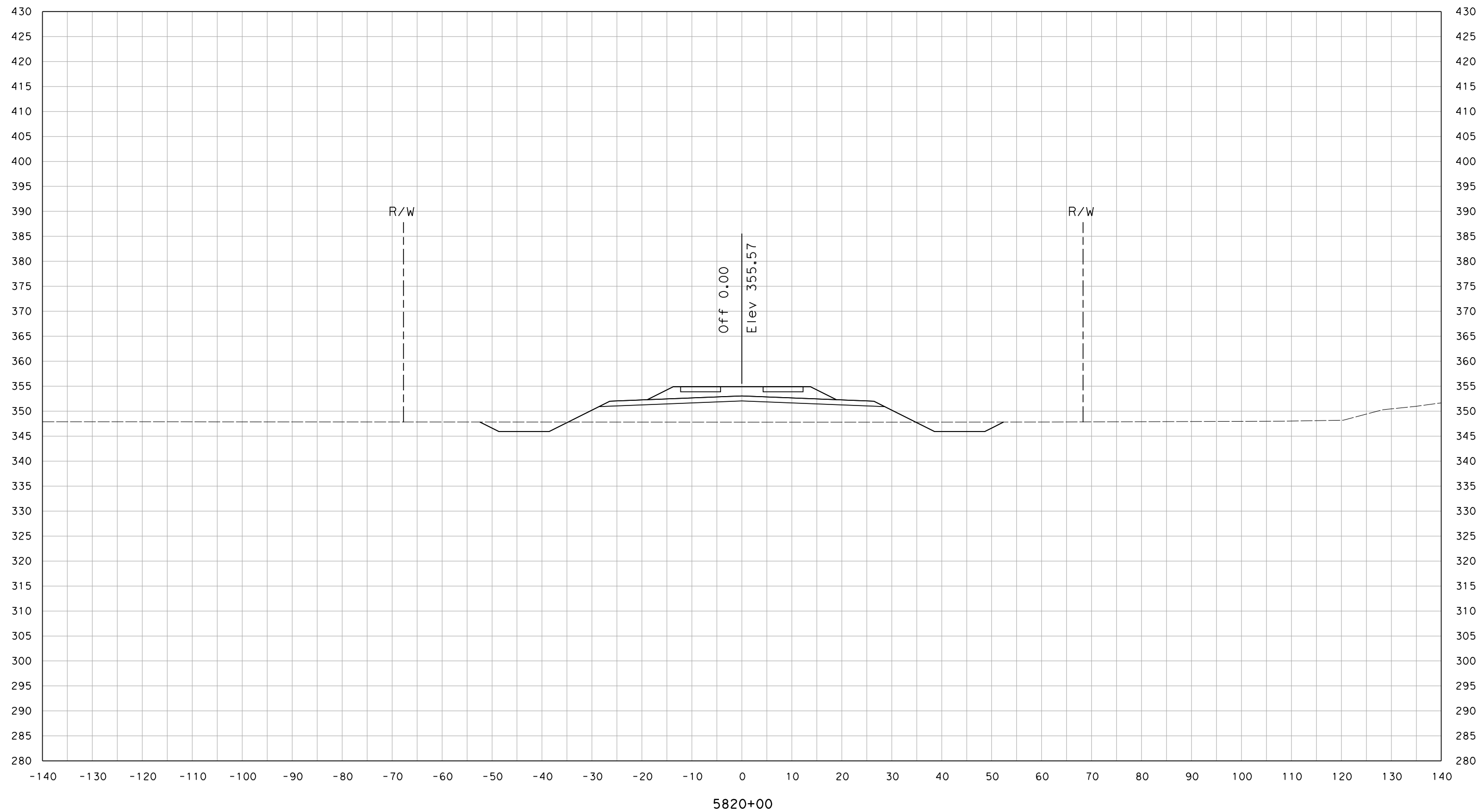
STA. 5805+00 TO STA. 5805+00



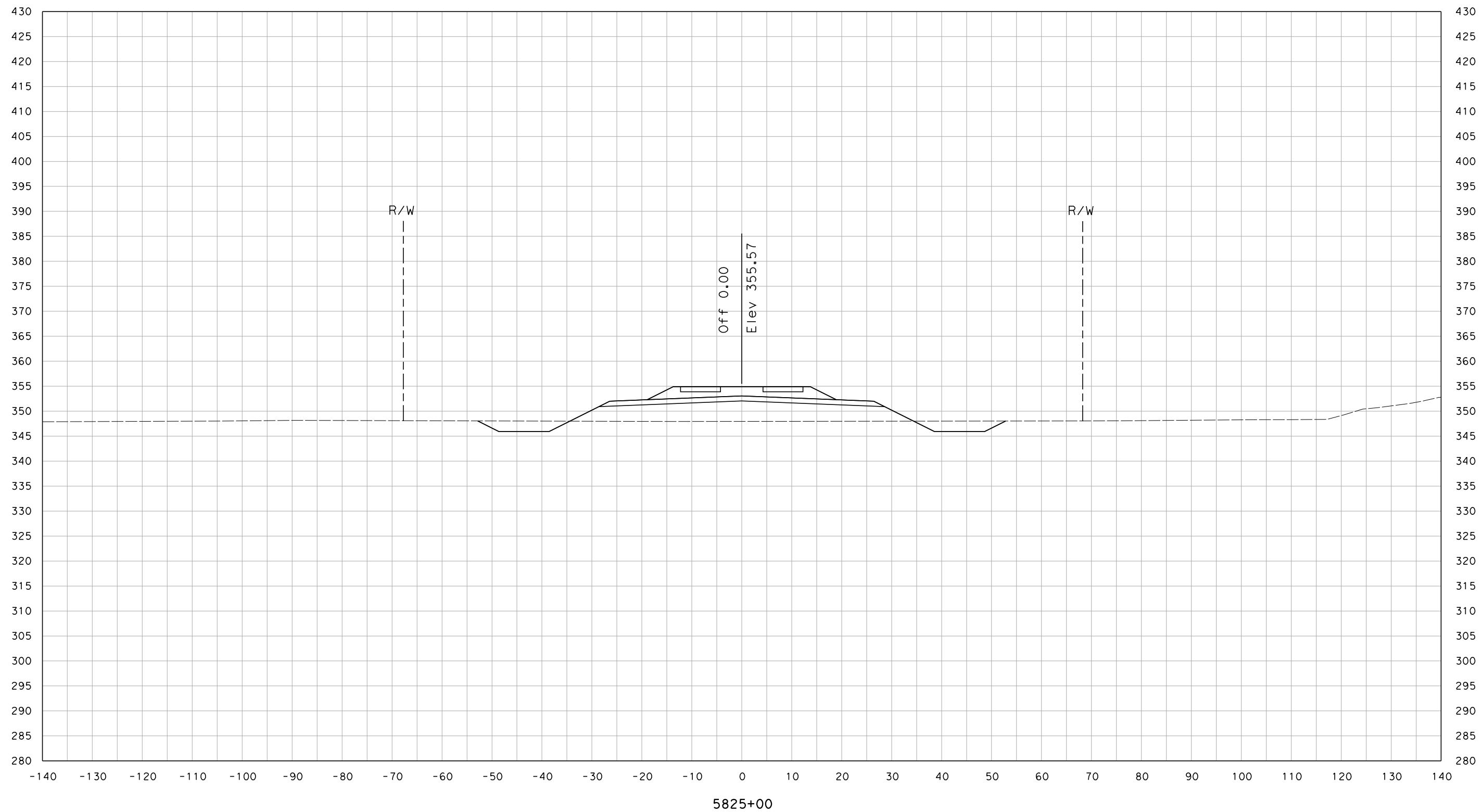
STA. 5810+00 TO STA. 5810+00



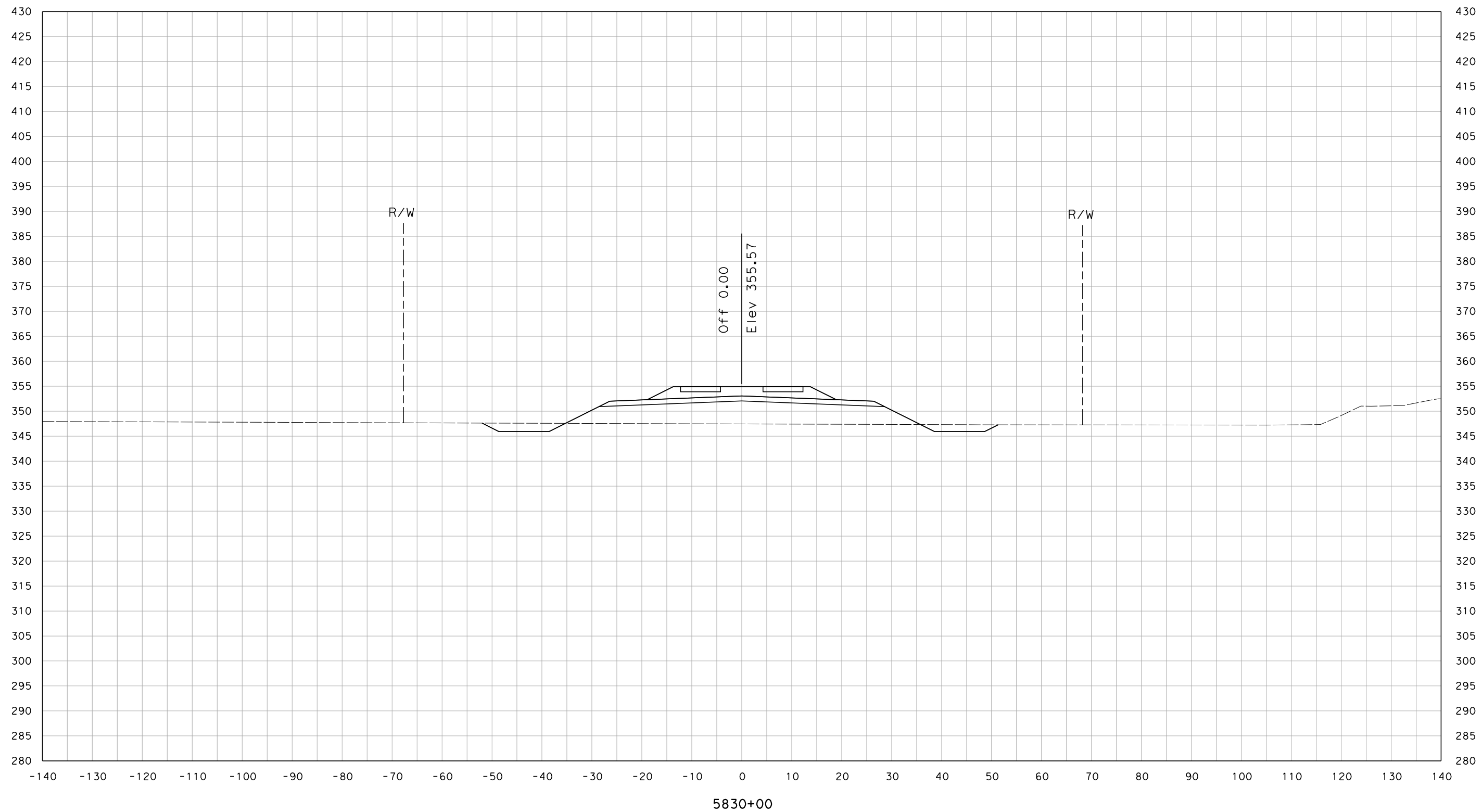
STA. 5815+00 TO STA. 5815+00



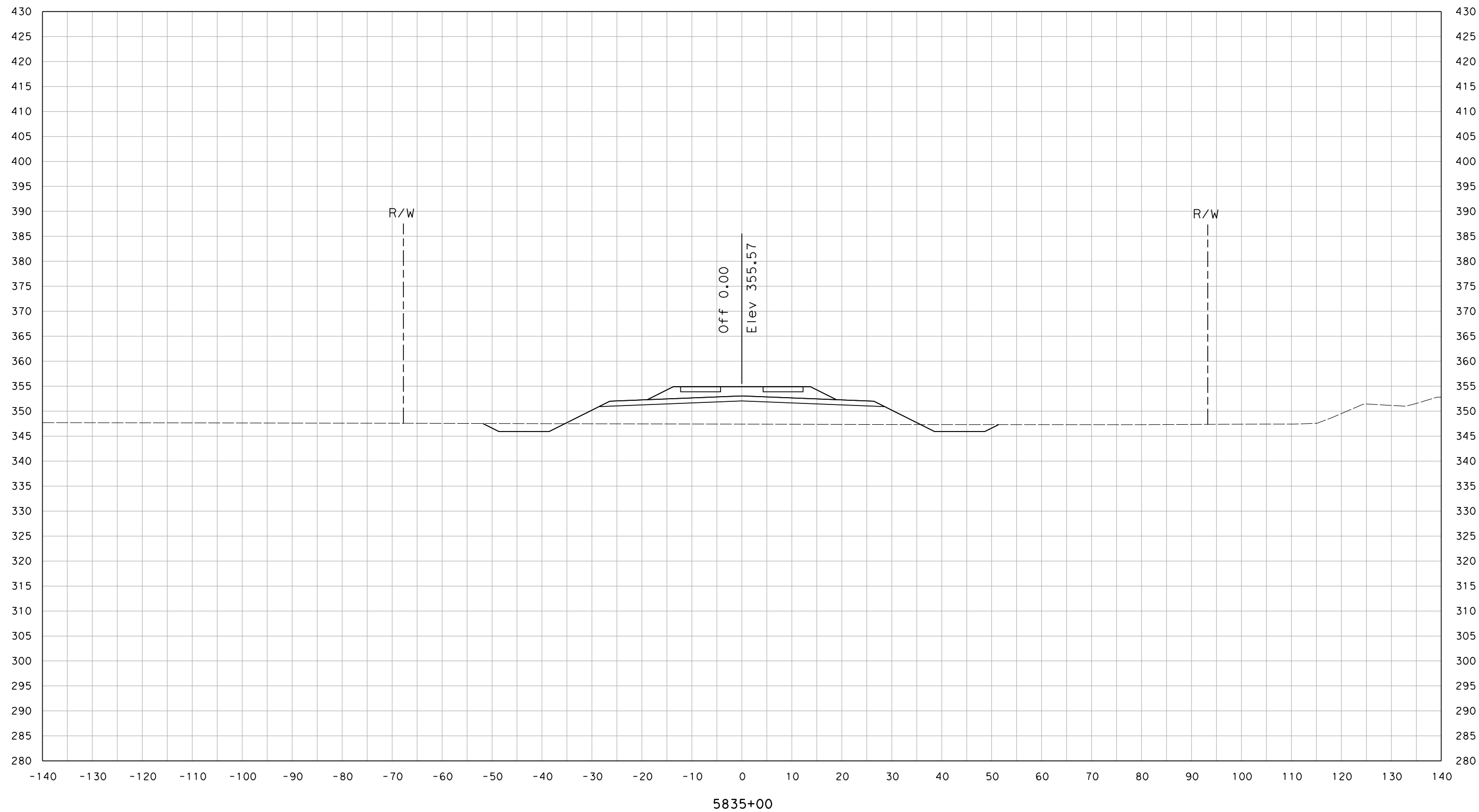
STA. 5820+00 TO STA. 5820+00



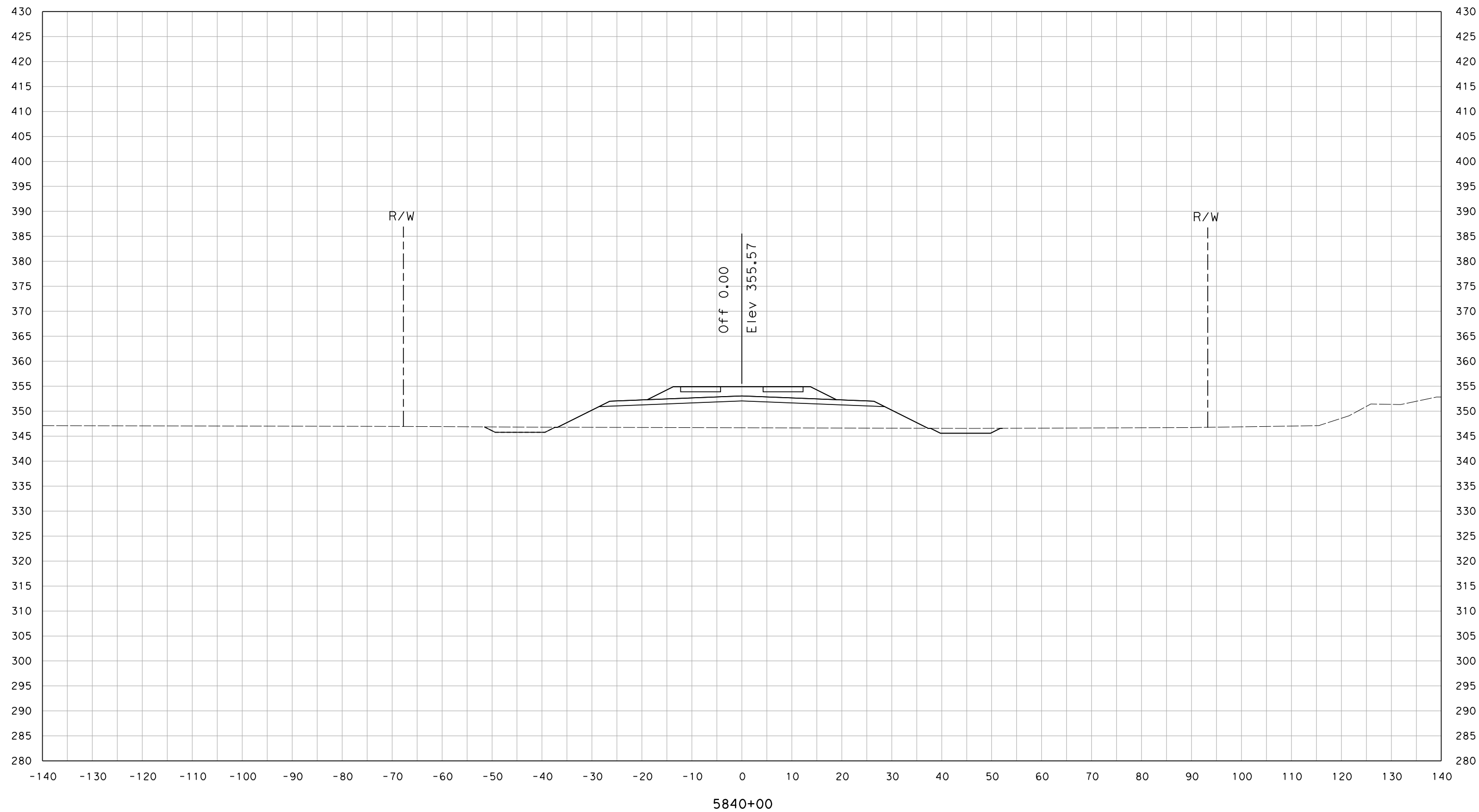
STA. 5825+00 TO STA. 5825+00



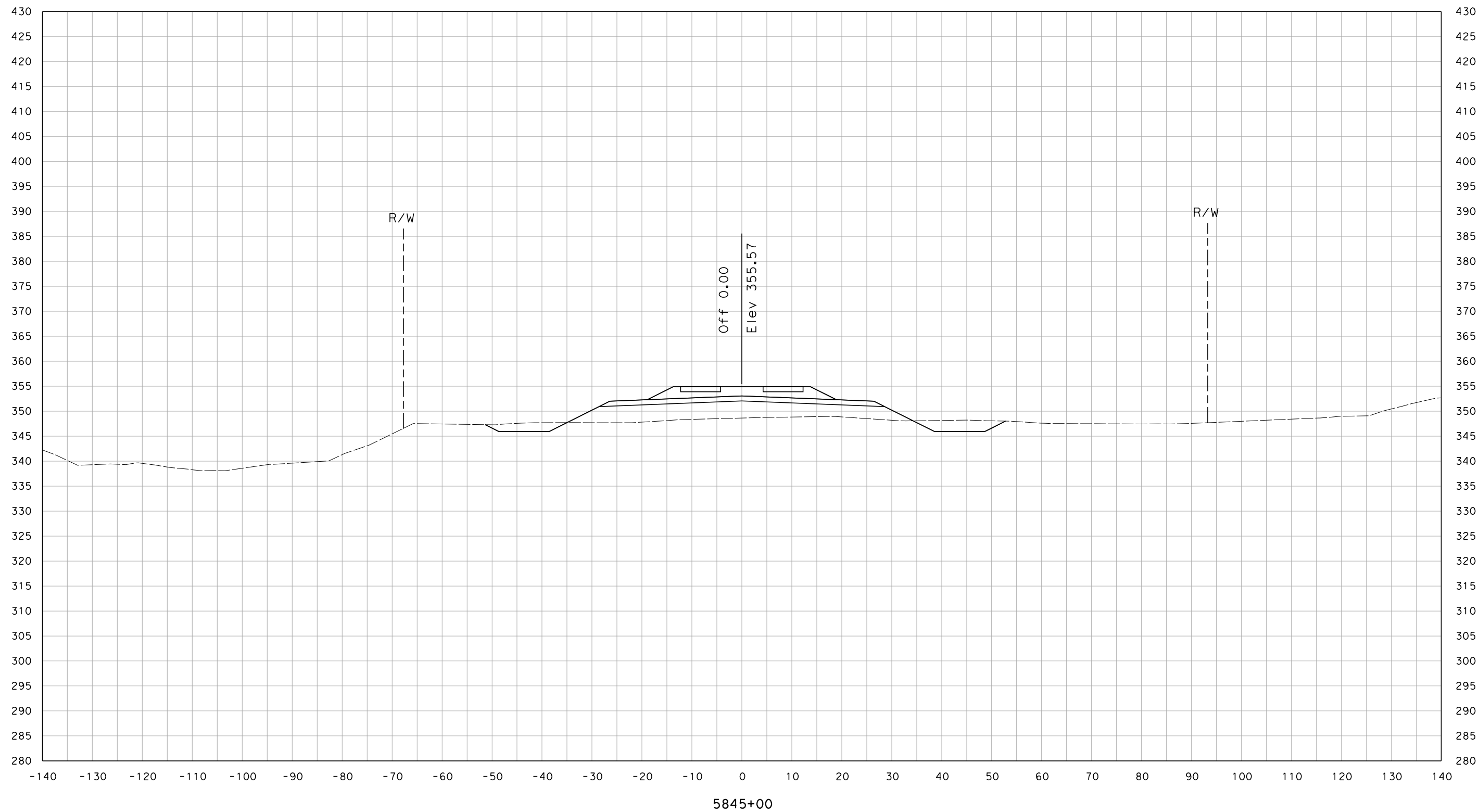
STA. 5830+00 TO STA. 5830+00



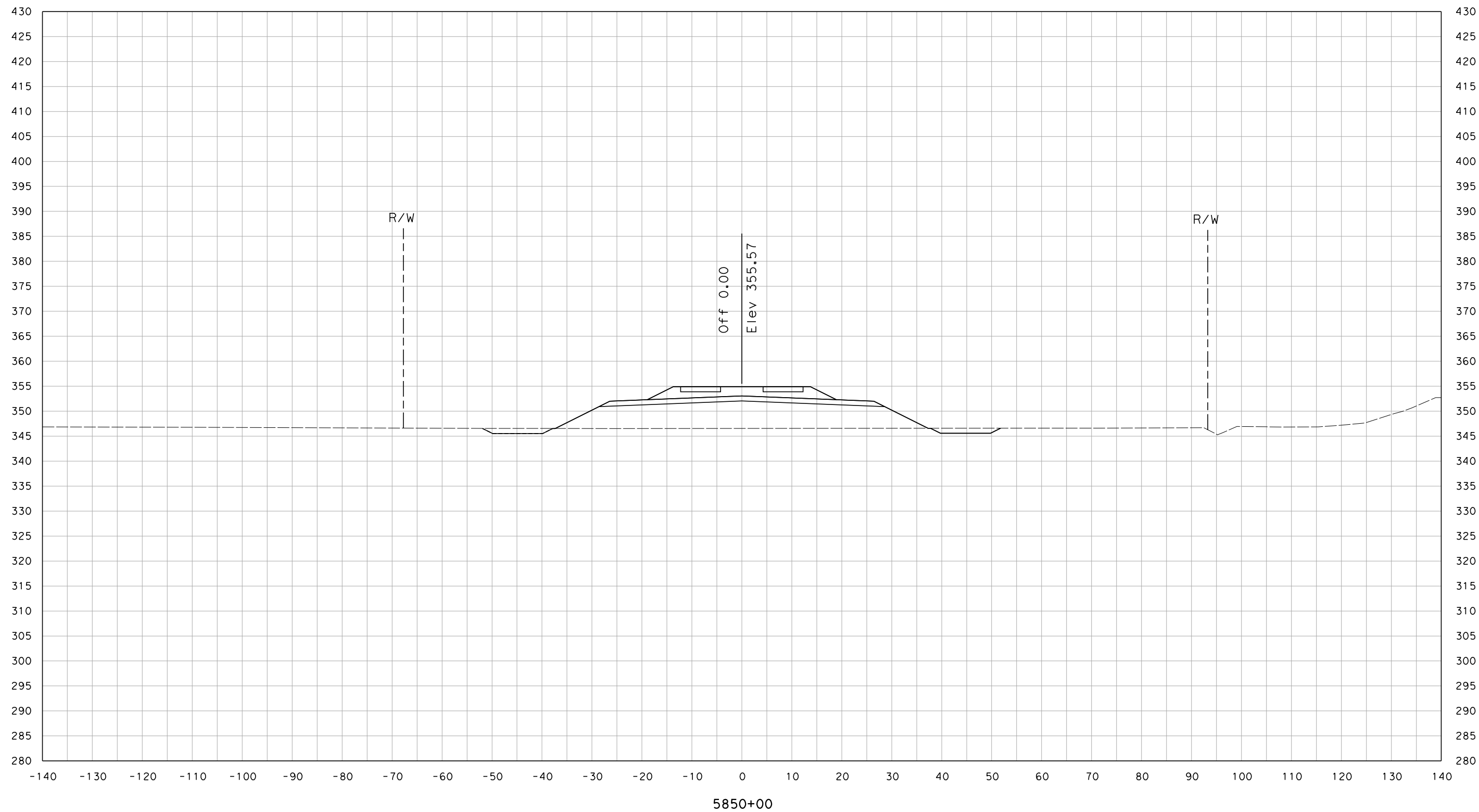
STA. 5835+00 TO STA. 5835+00



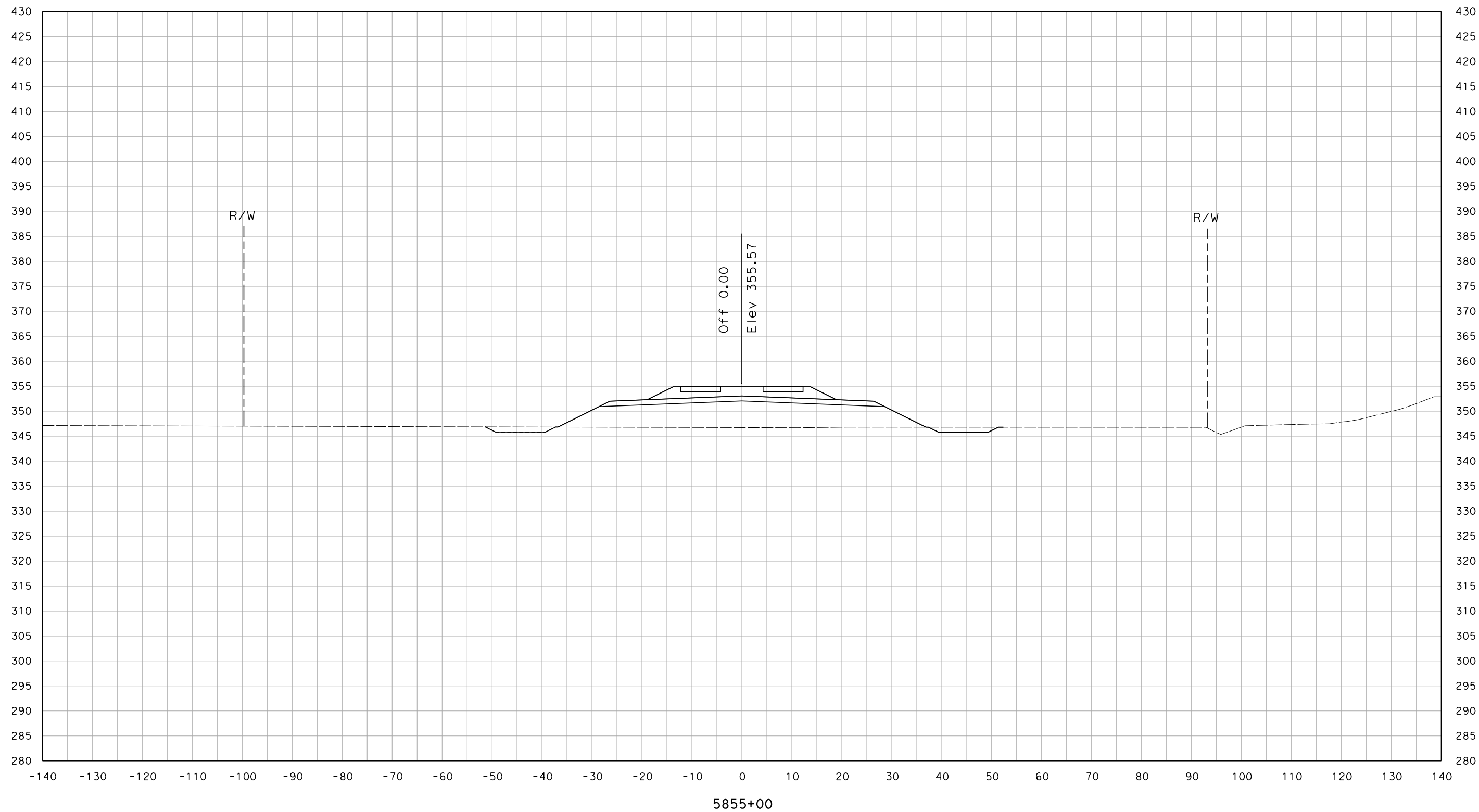
STA. 5840+00 TO STA. 5840+00



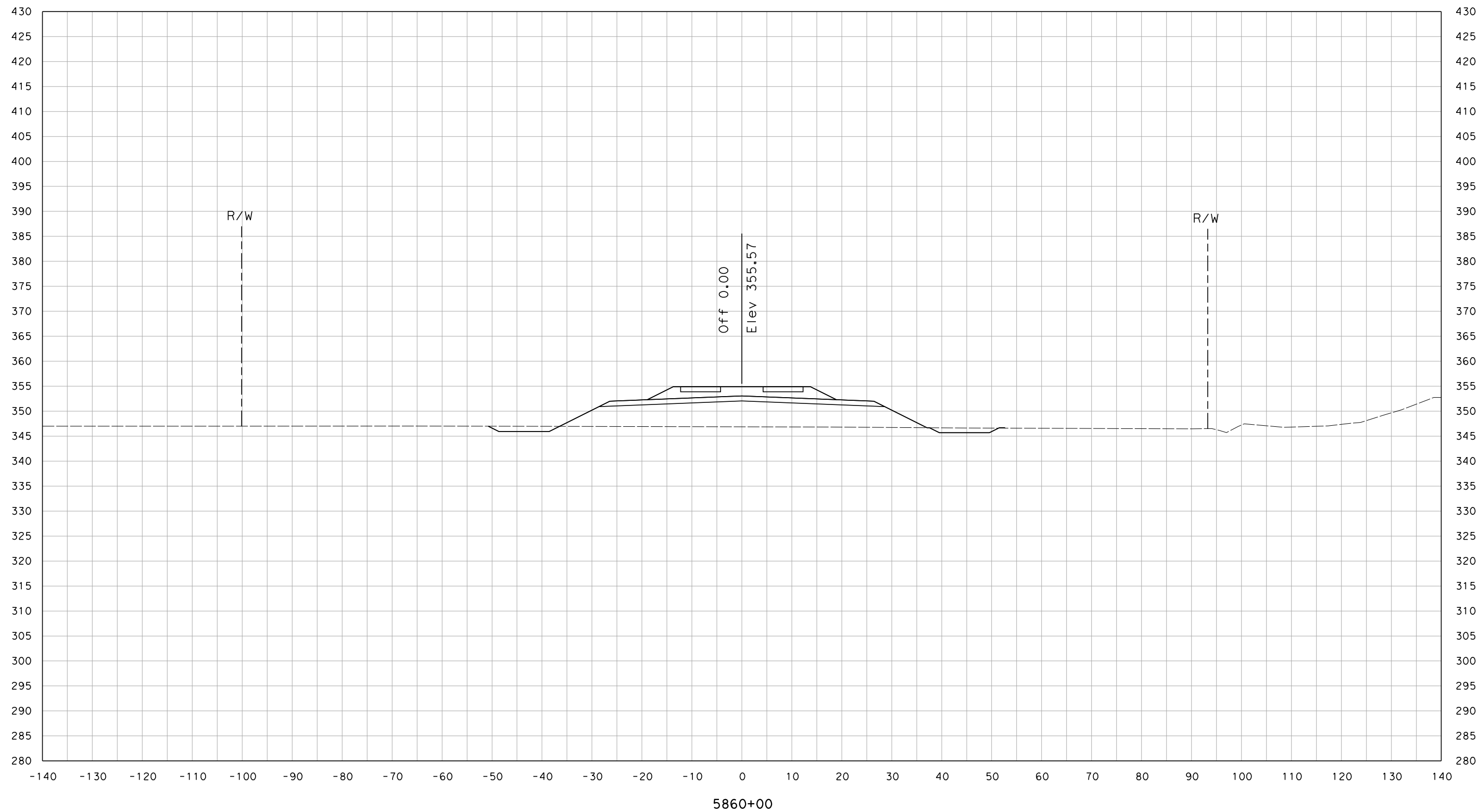
STA. 5845+00 TO STA. 5845+00



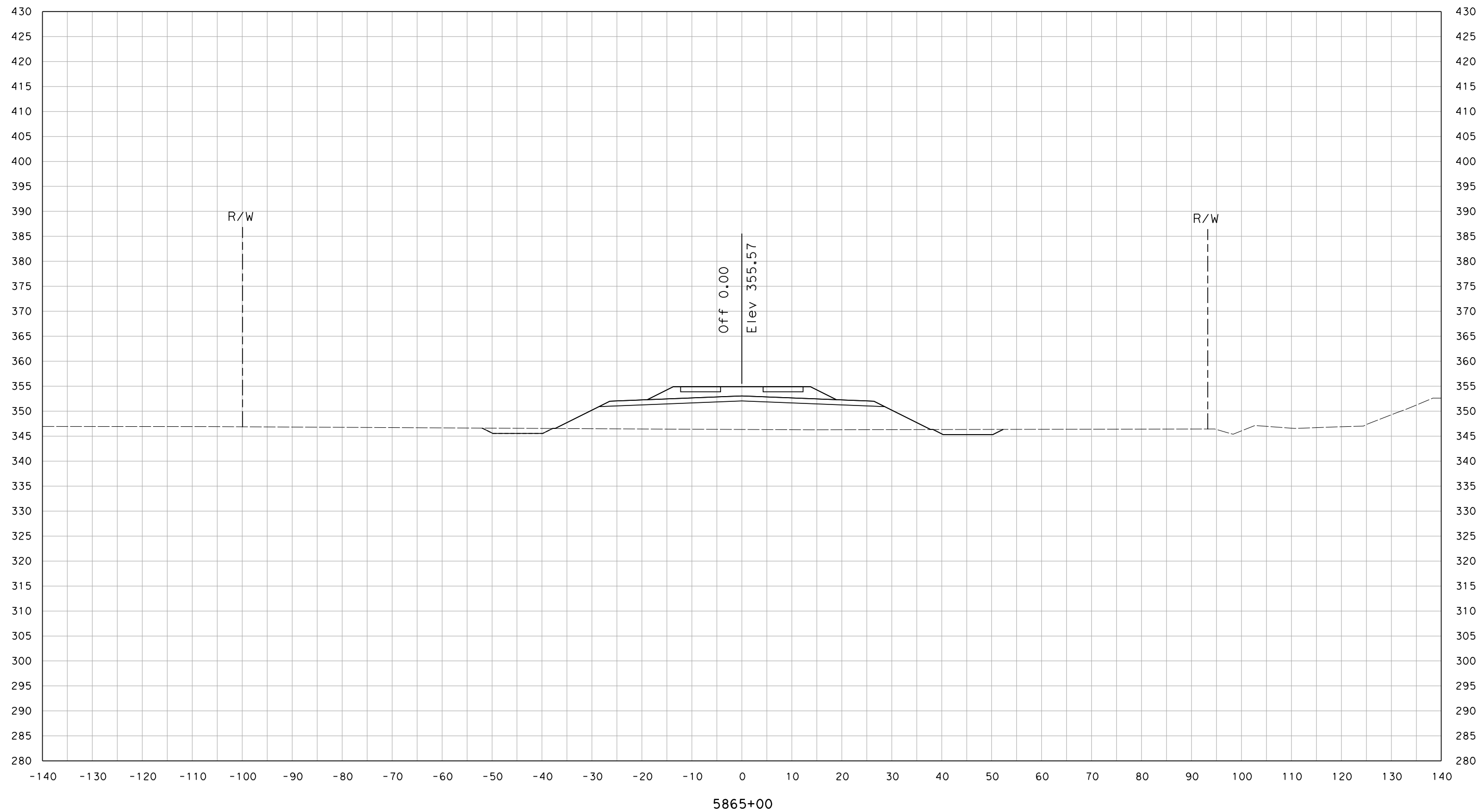
STA. 5850+00 TO STA. 5850+00



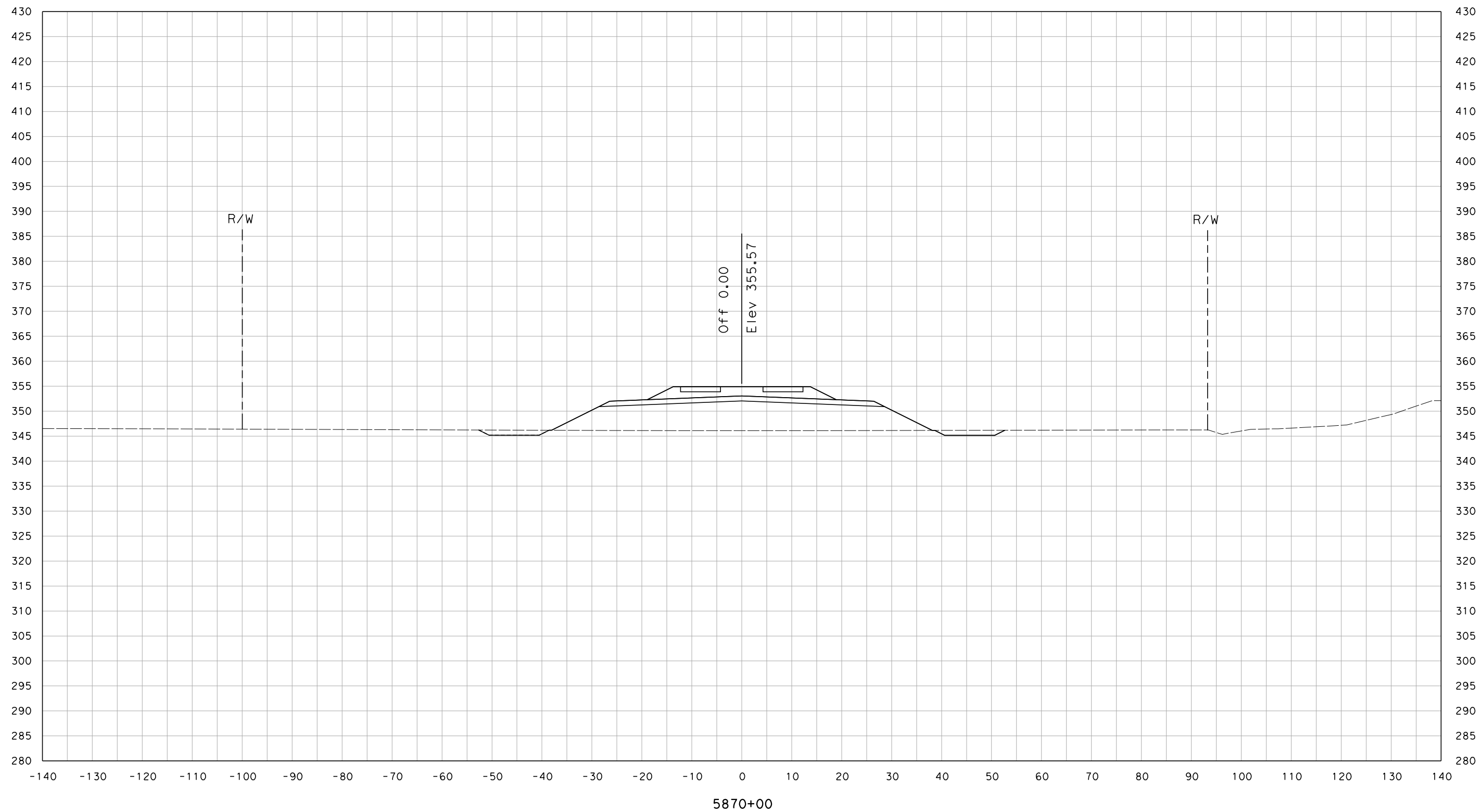
STA. 5855+00 TO STA. 5855+00



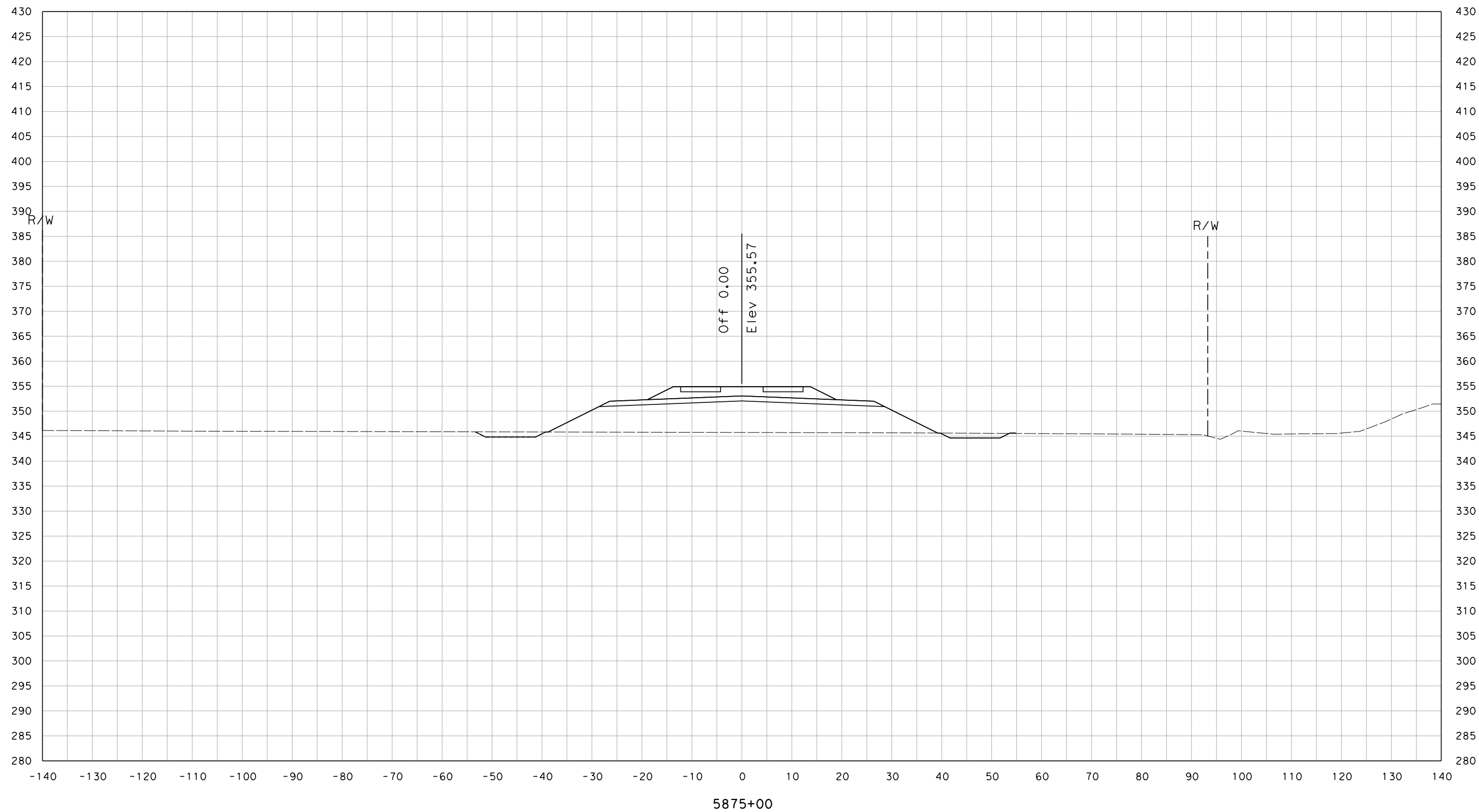
STA. 5860+00 TO STA. 5860+00



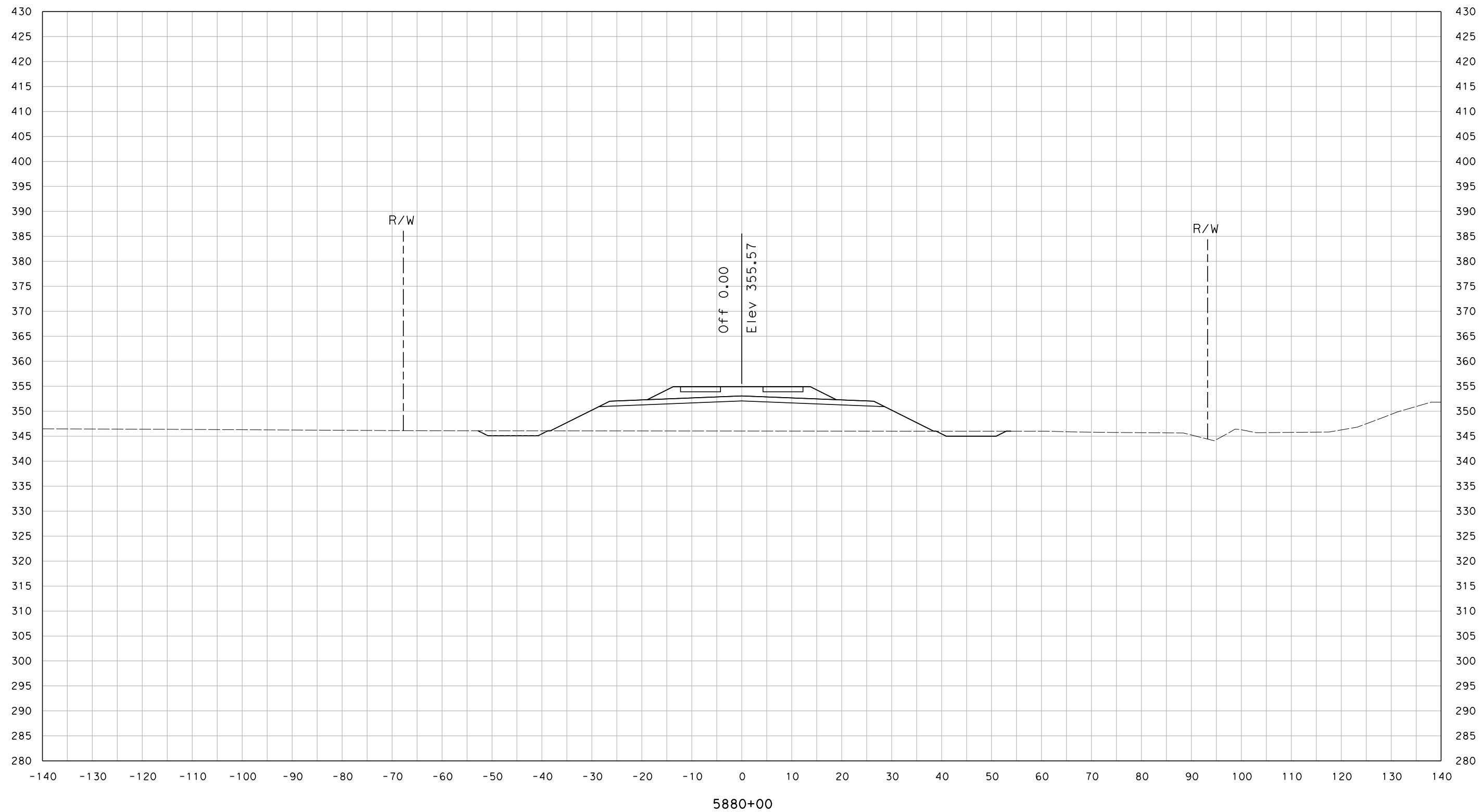
STA. 5865+00 TO STA. 5865+00



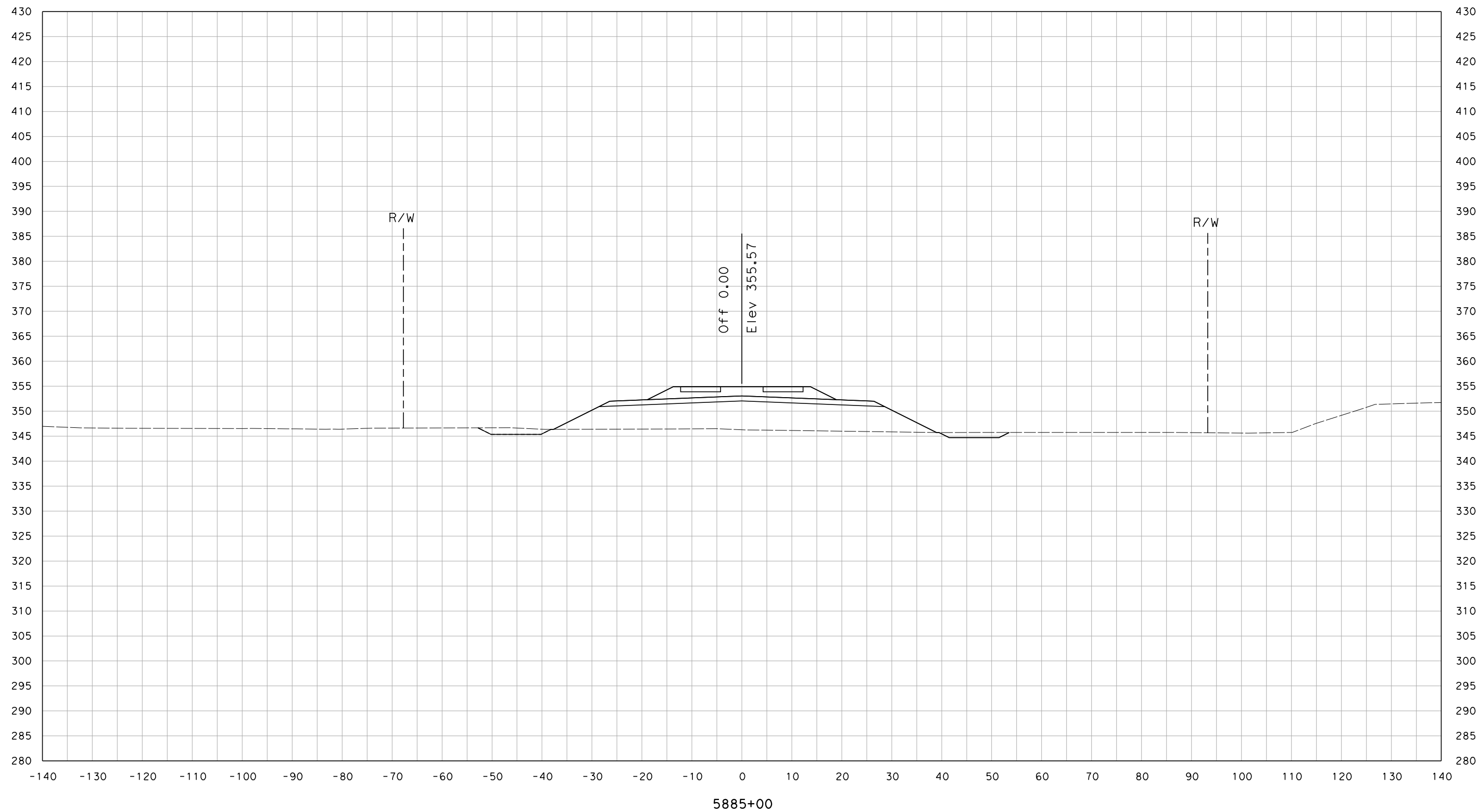
STA. 5870+00 TO STA. 5870+00



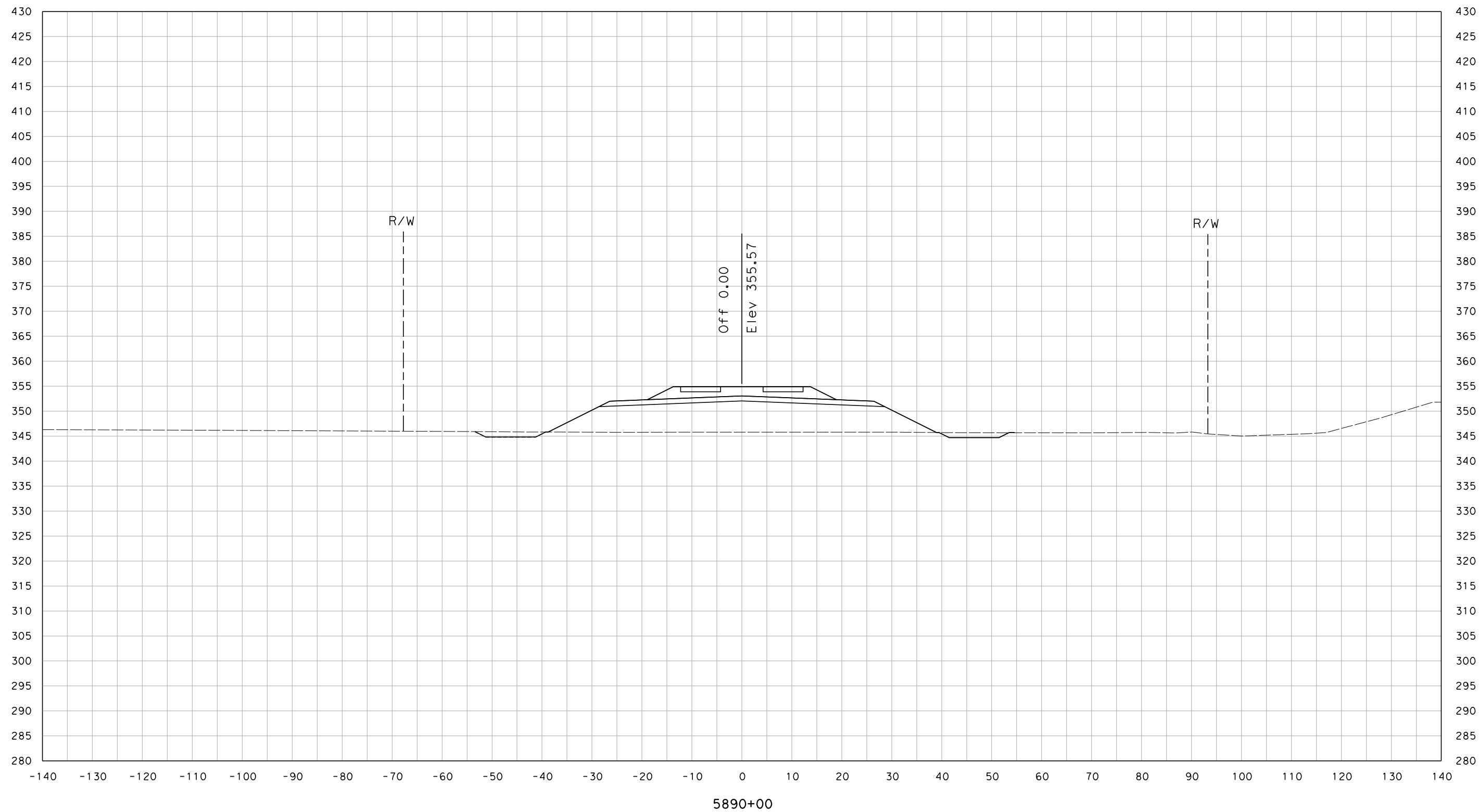
STA. 5875+00 TO STA. 5875+00



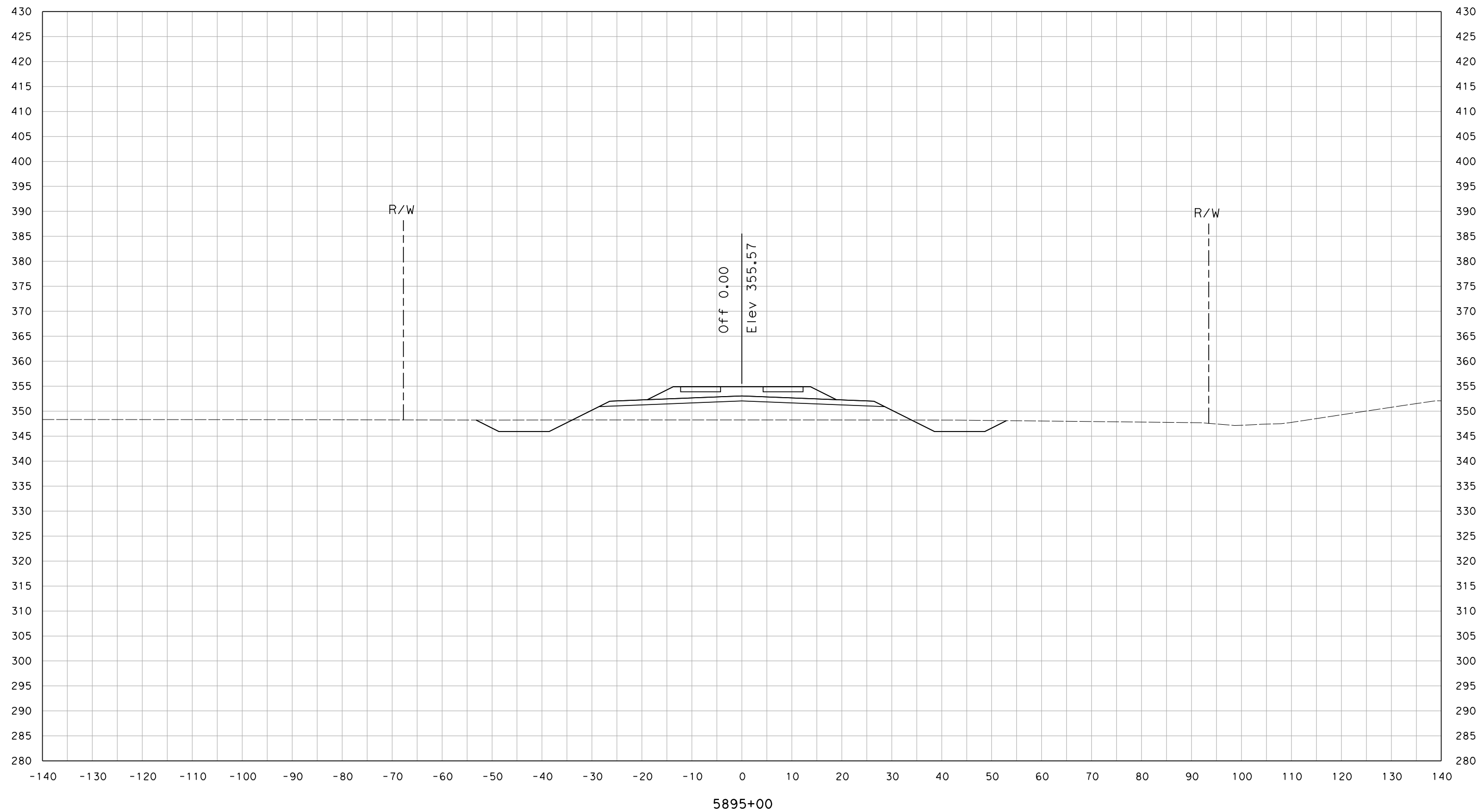
STA. 5880+00 TO STA. 5880+00



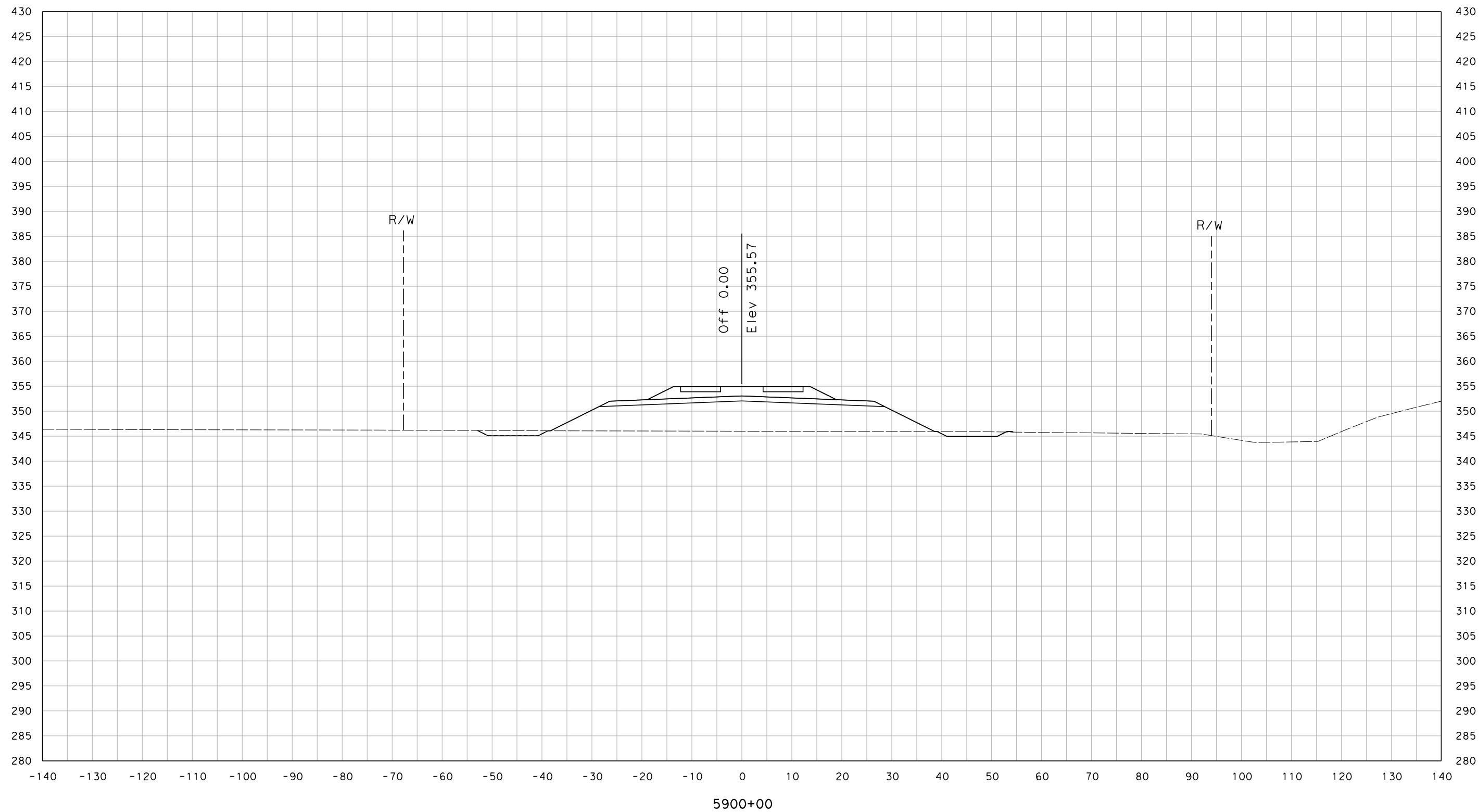
STA. 5885+00 TO STA. 5885+00



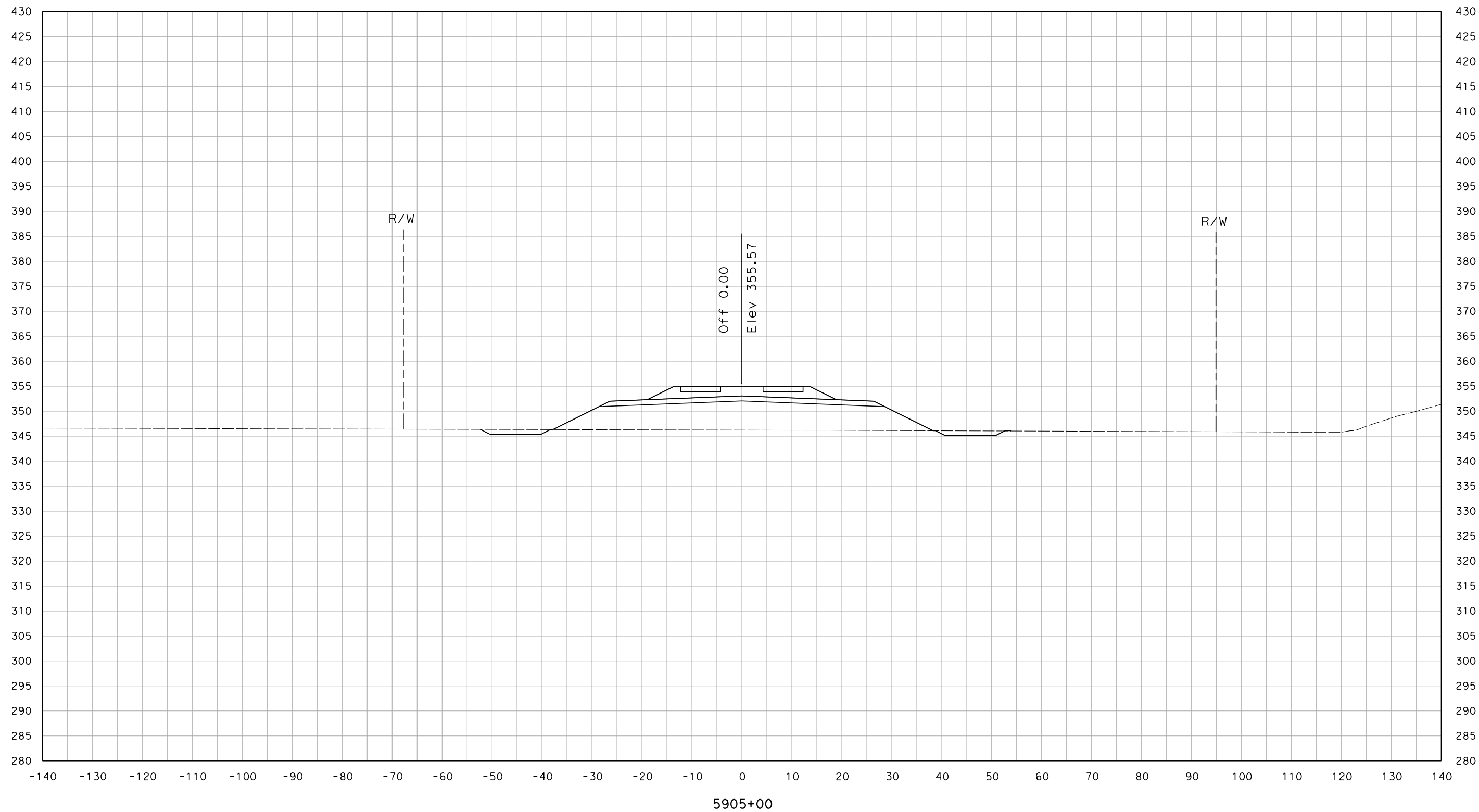
STA. 5890+00 TO STA. 5890+00



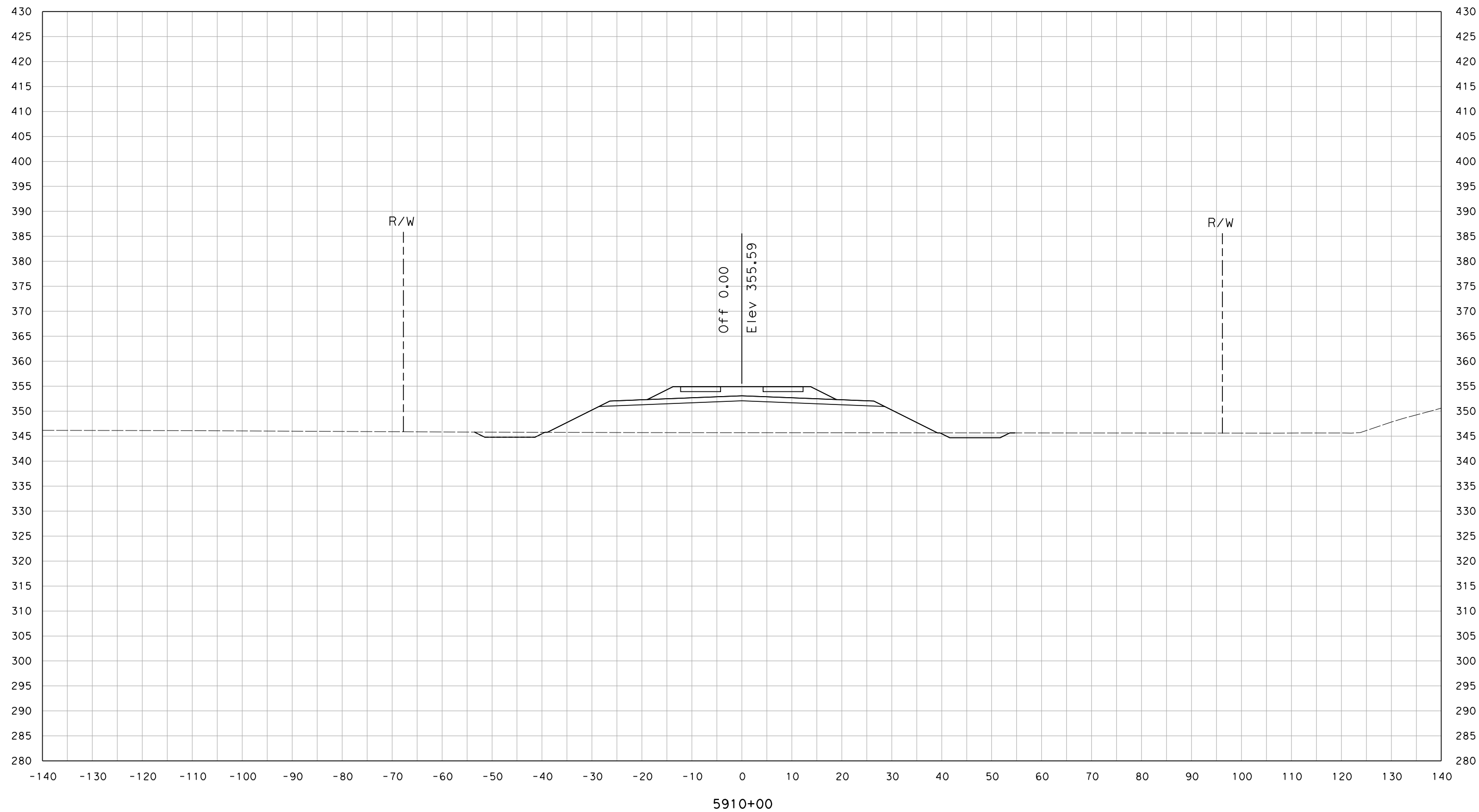
STA. 5895+00 TO STA. 5895+00



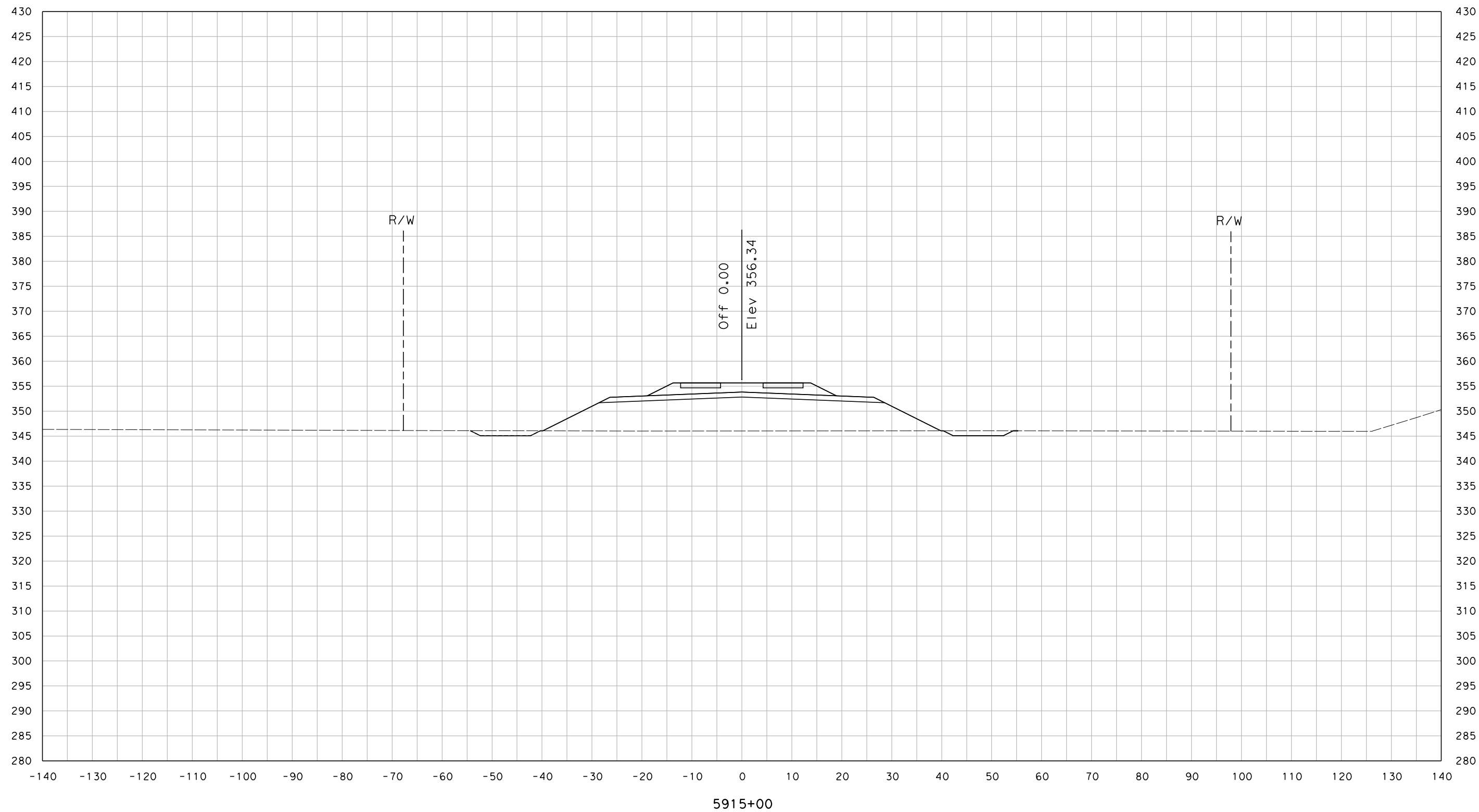
STA. 5900+00 TO STA. 5900+00



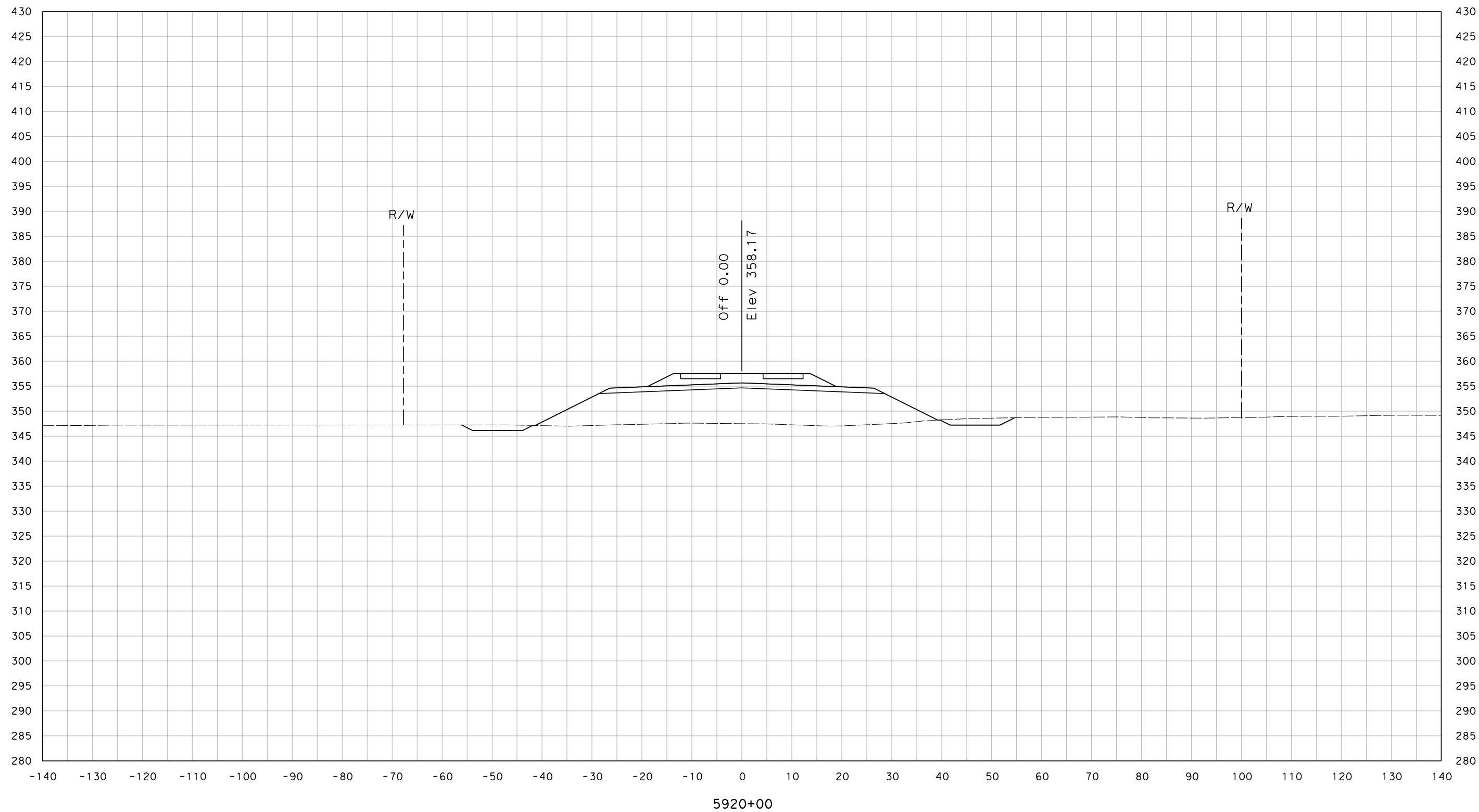
STA. 5905+00 TO STA. 5905+00



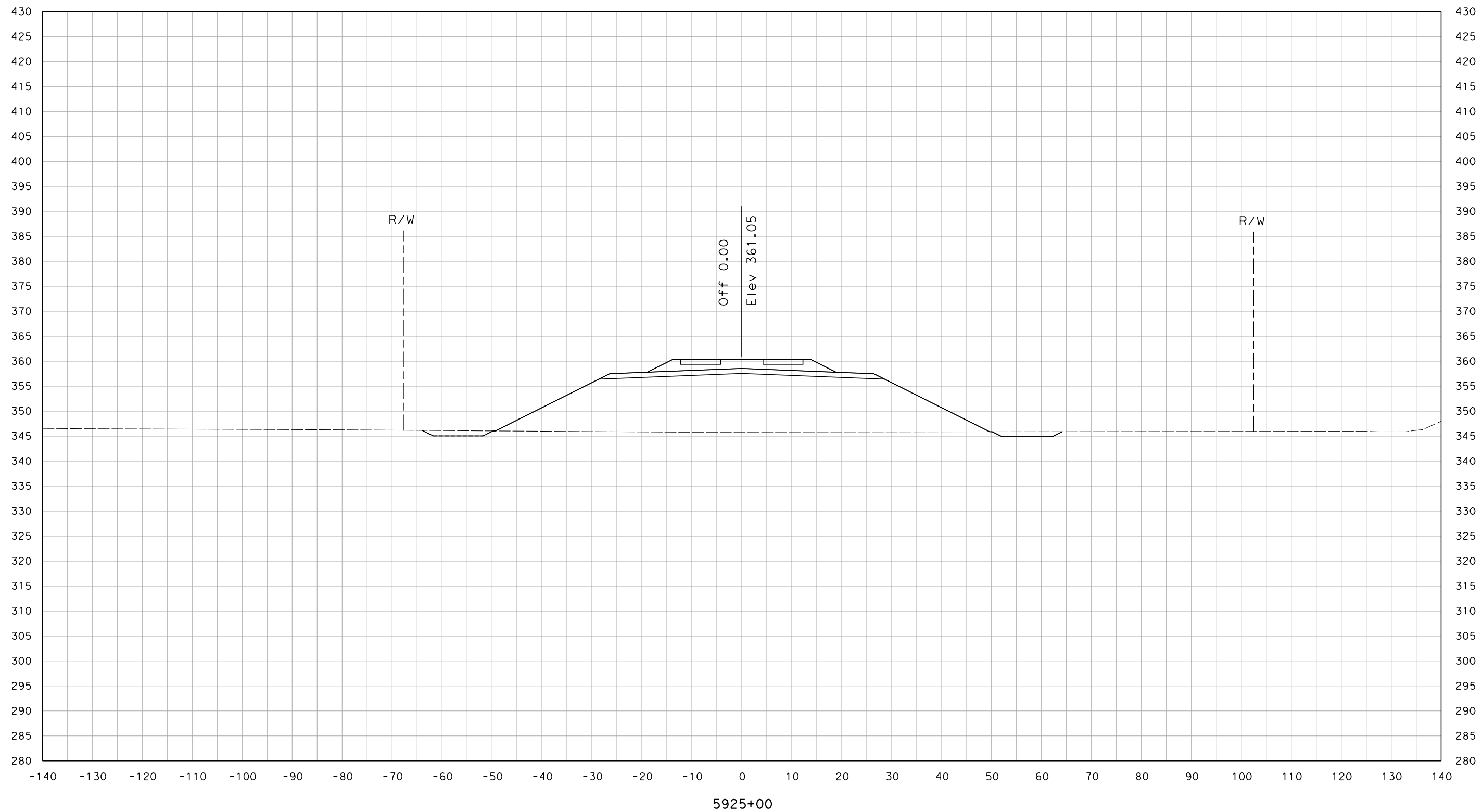
STA. 5910+00 TO STA. 5910+00



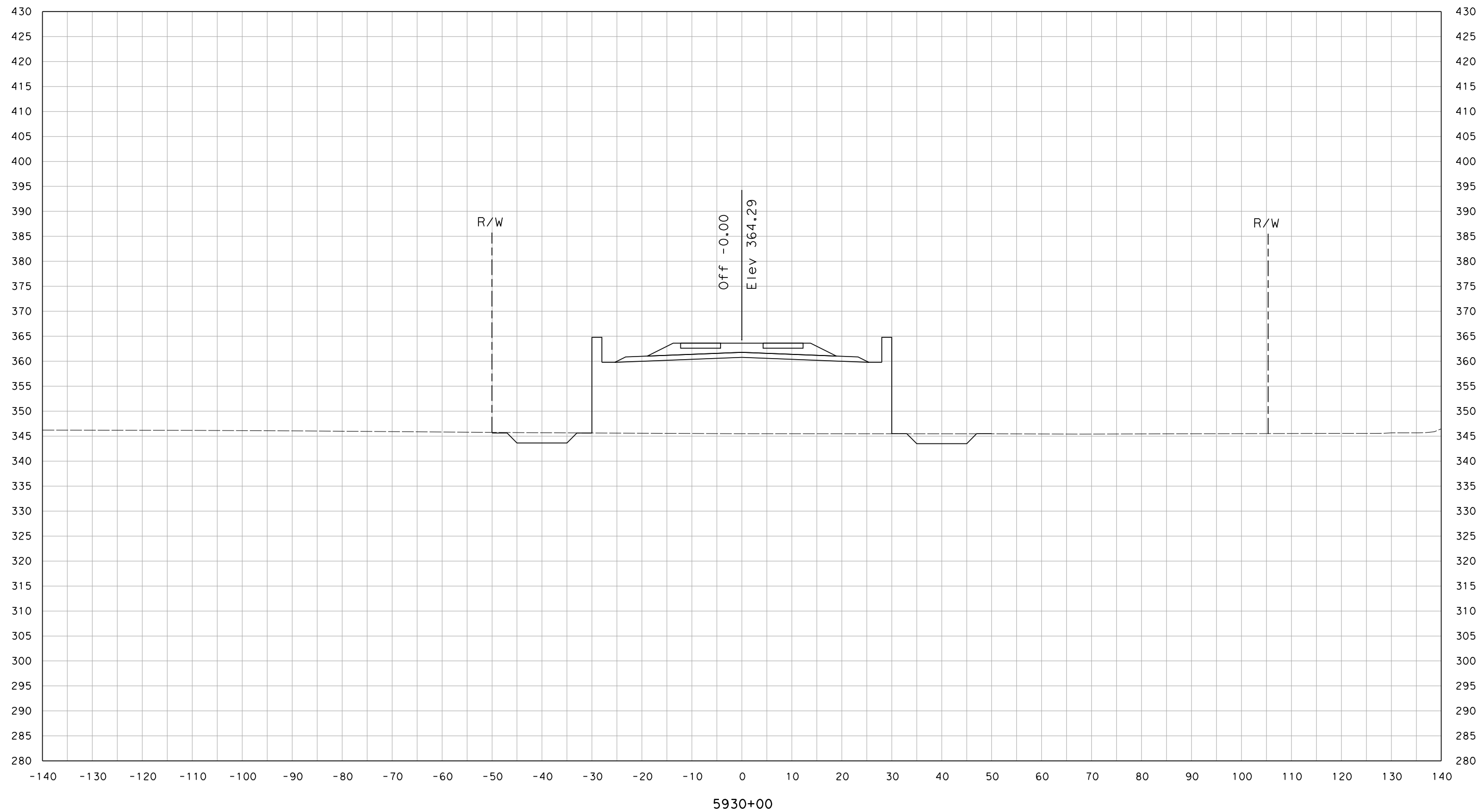
STA. 5915+00 TO STA. 5915+00



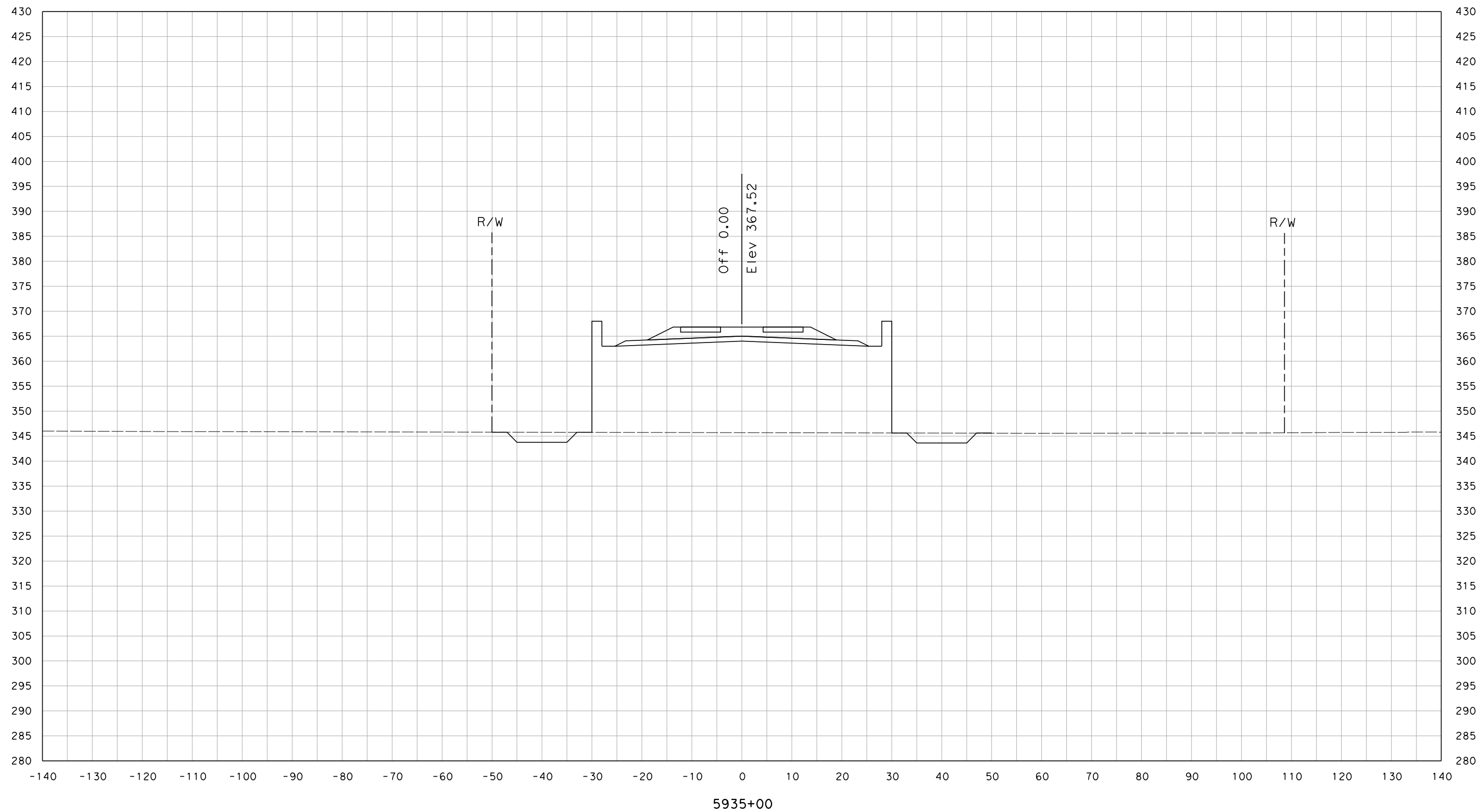
STA. 5920+00 TO STA. 5920+00



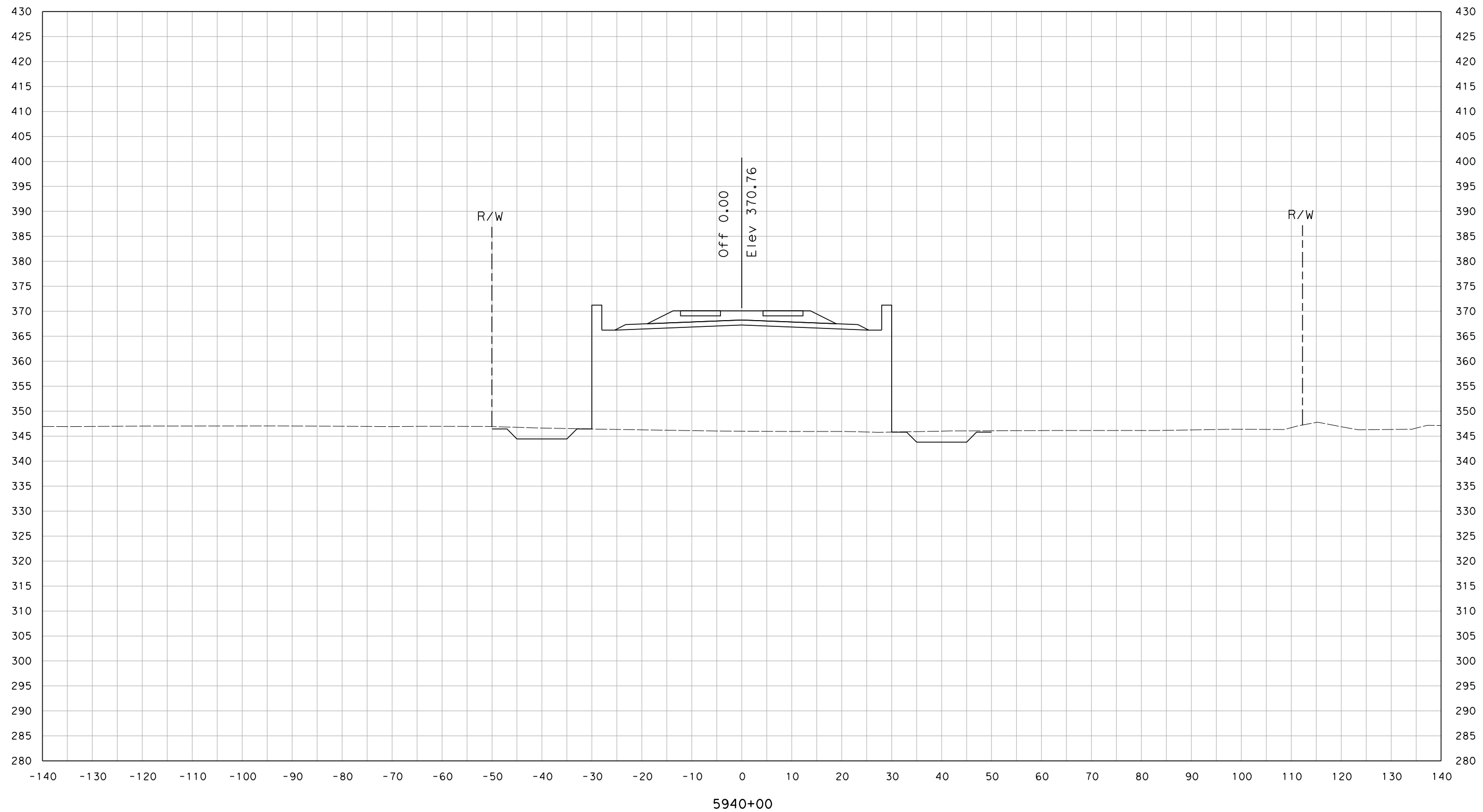
STA. 5925+00 TO STA. 5925+00



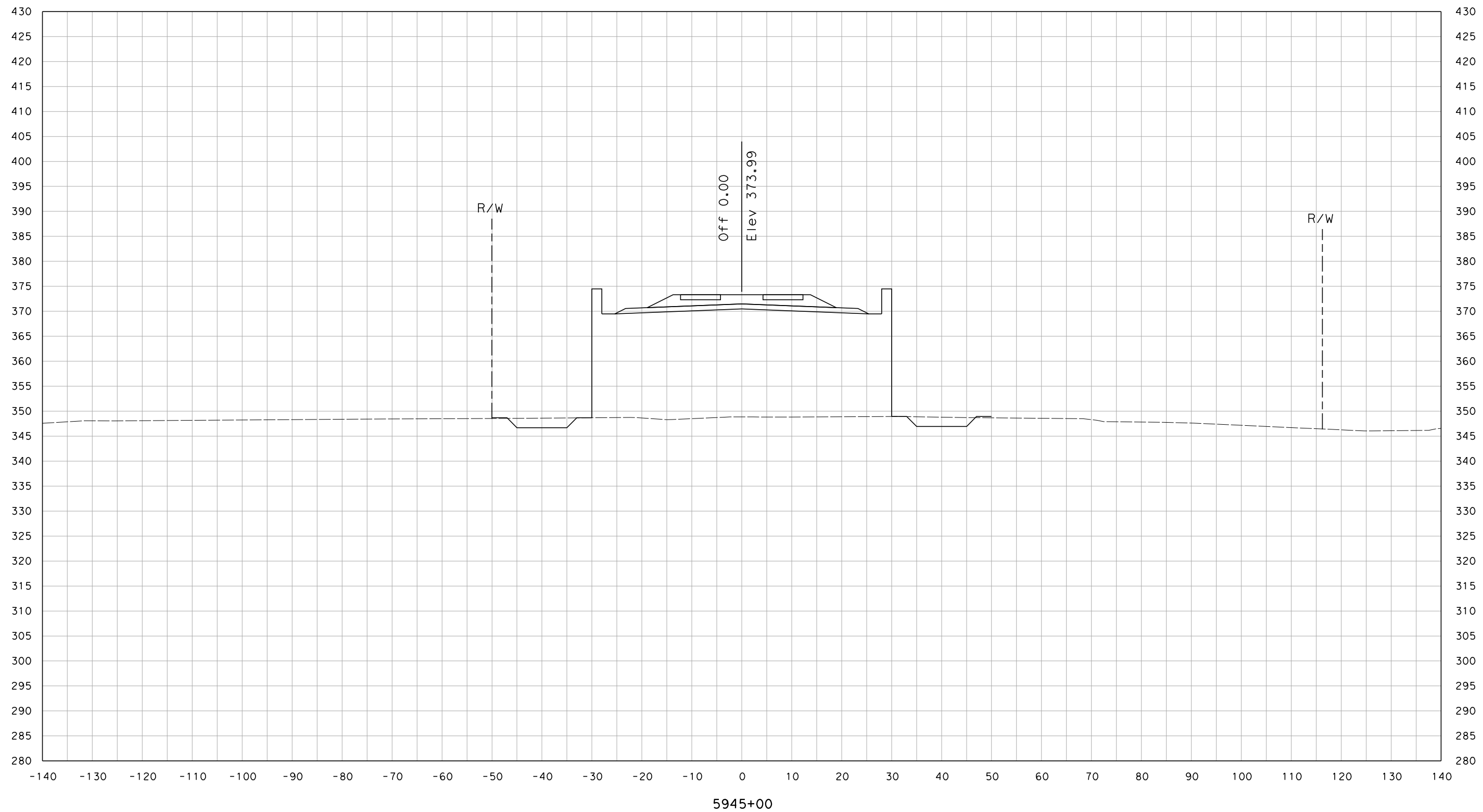
STA. 5930+00 TO STA. 5930+00



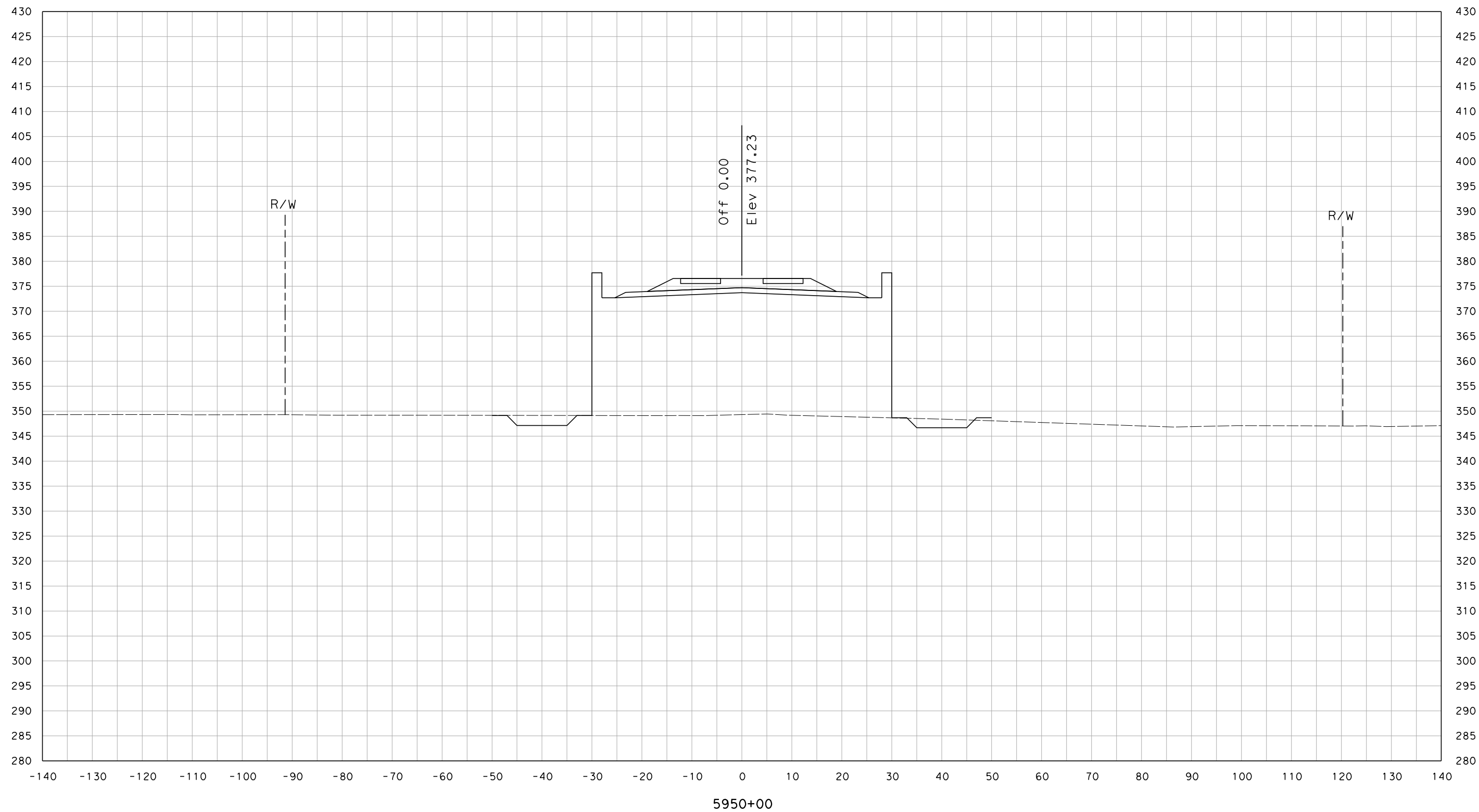
STA. 5935+00 TO STA. 5935+00



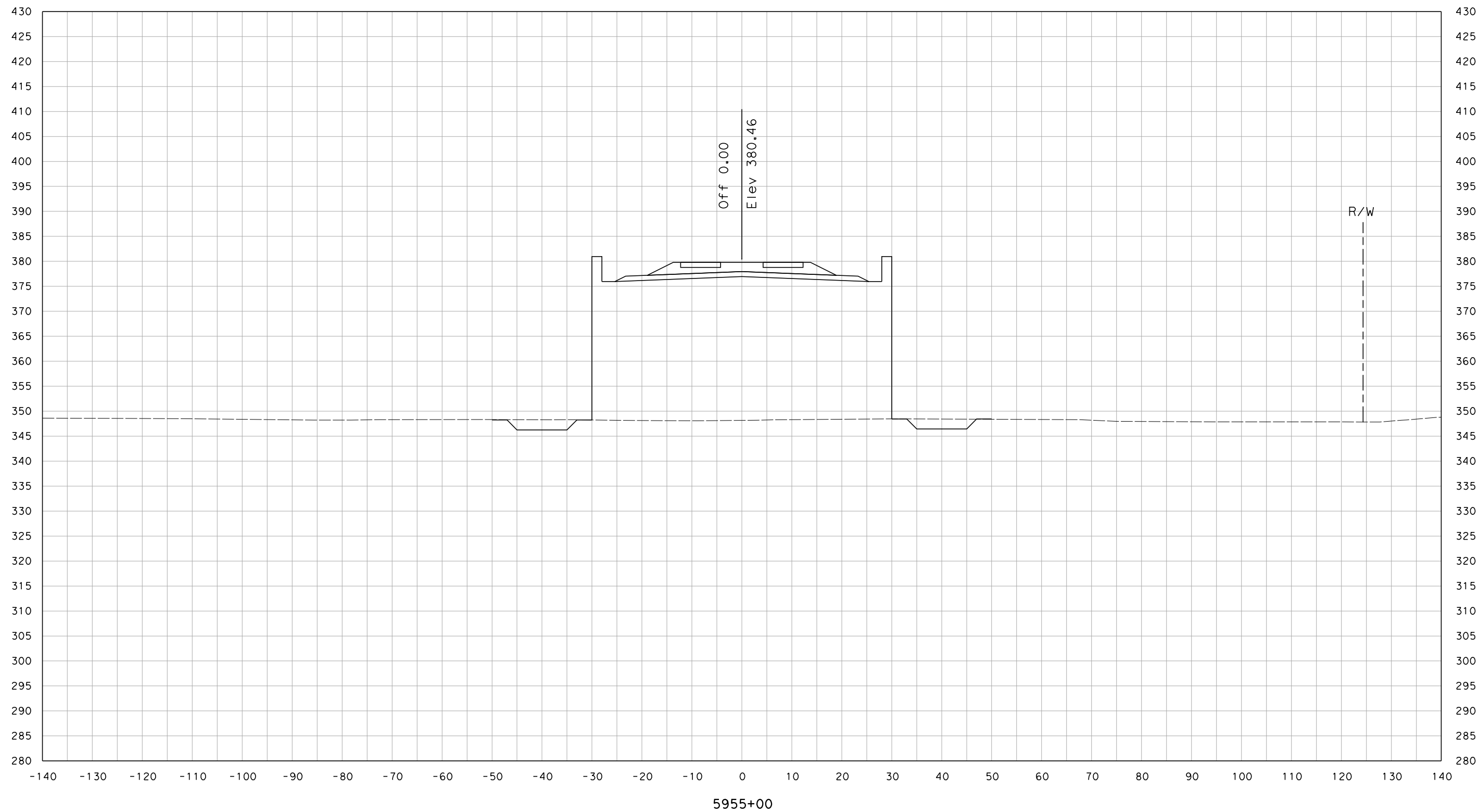
STA. 5940+00 TO STA. 5940+00



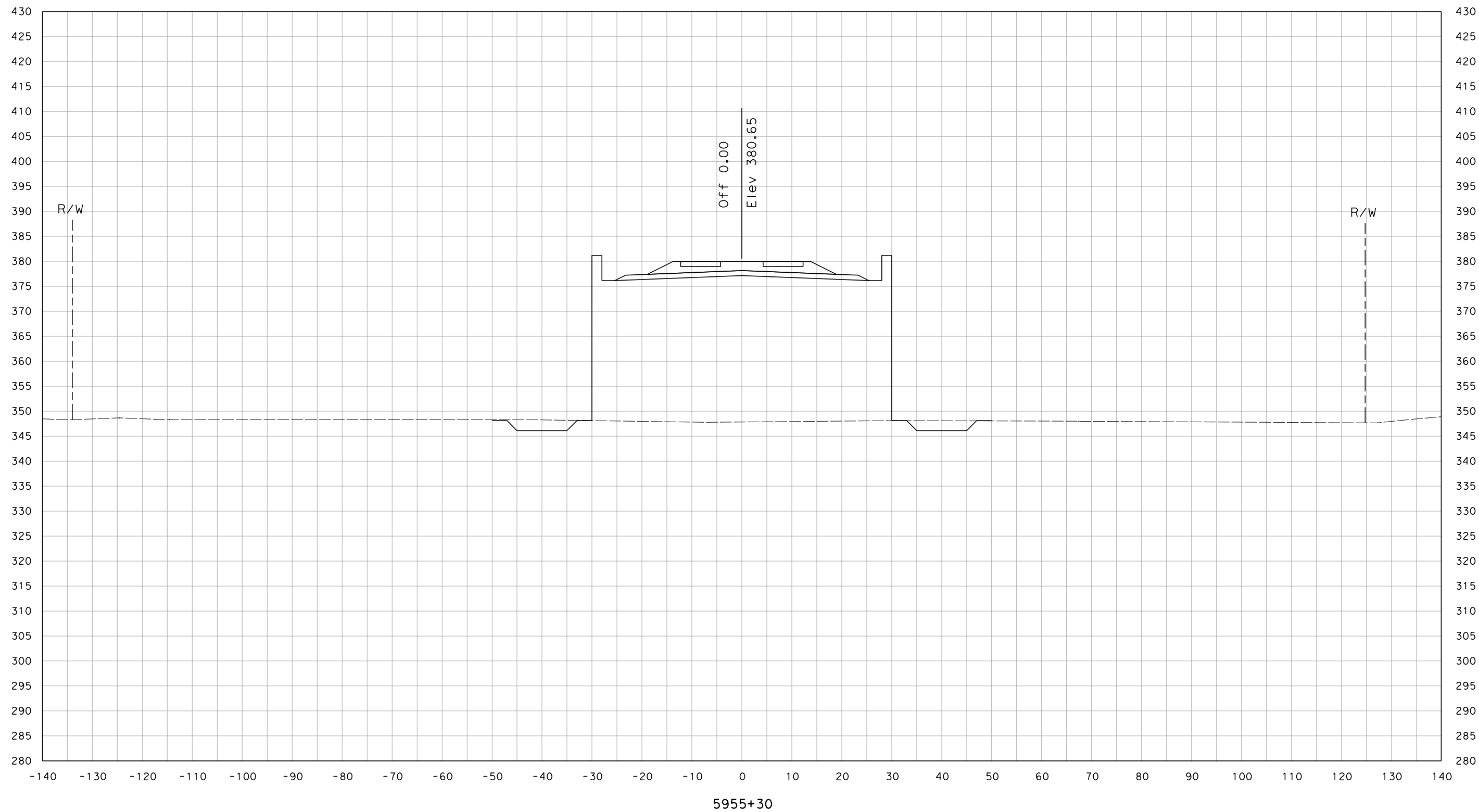
STA. 5945+00 TO STA. 5945+00



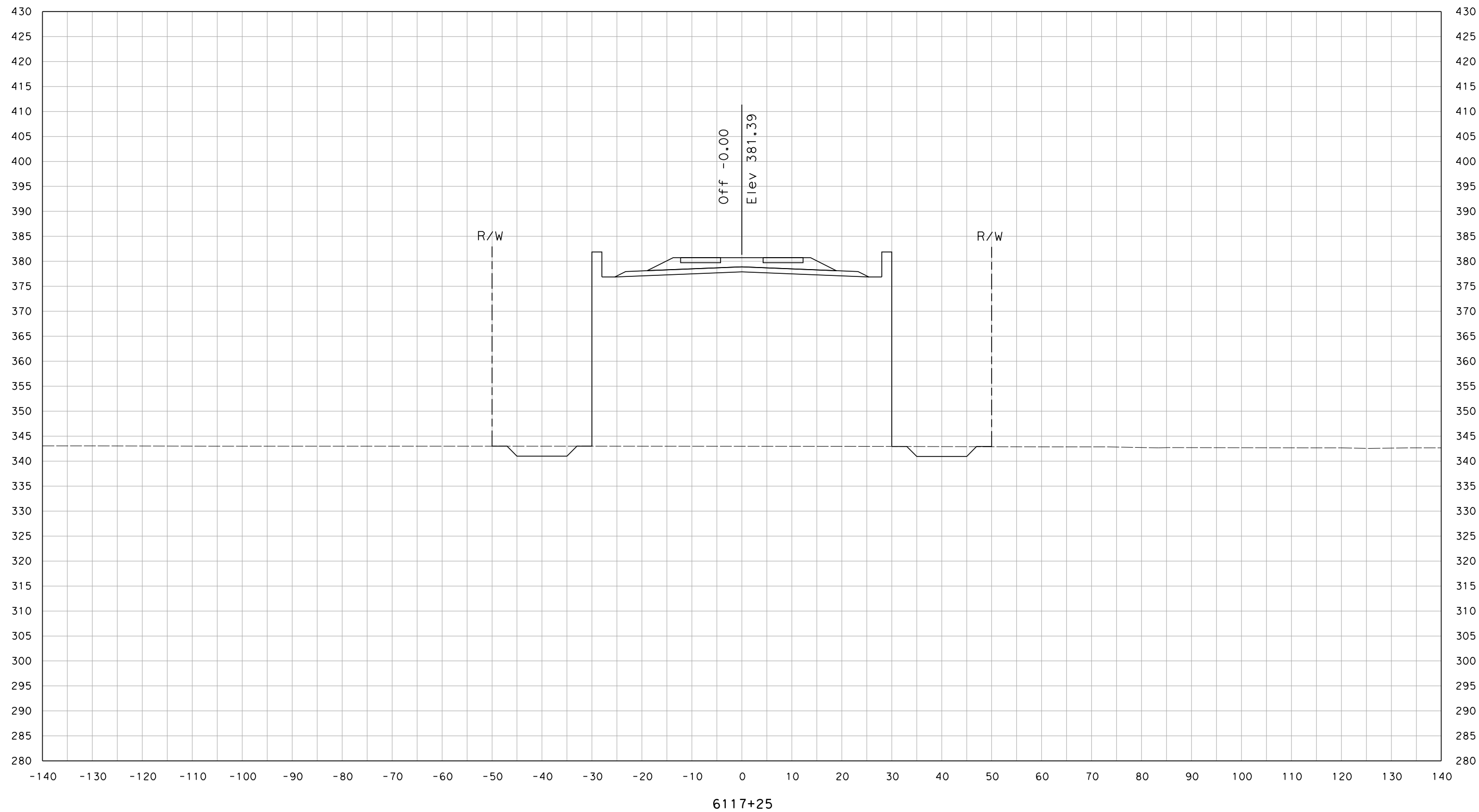
STA. 5950+00 TO STA. 5950+00



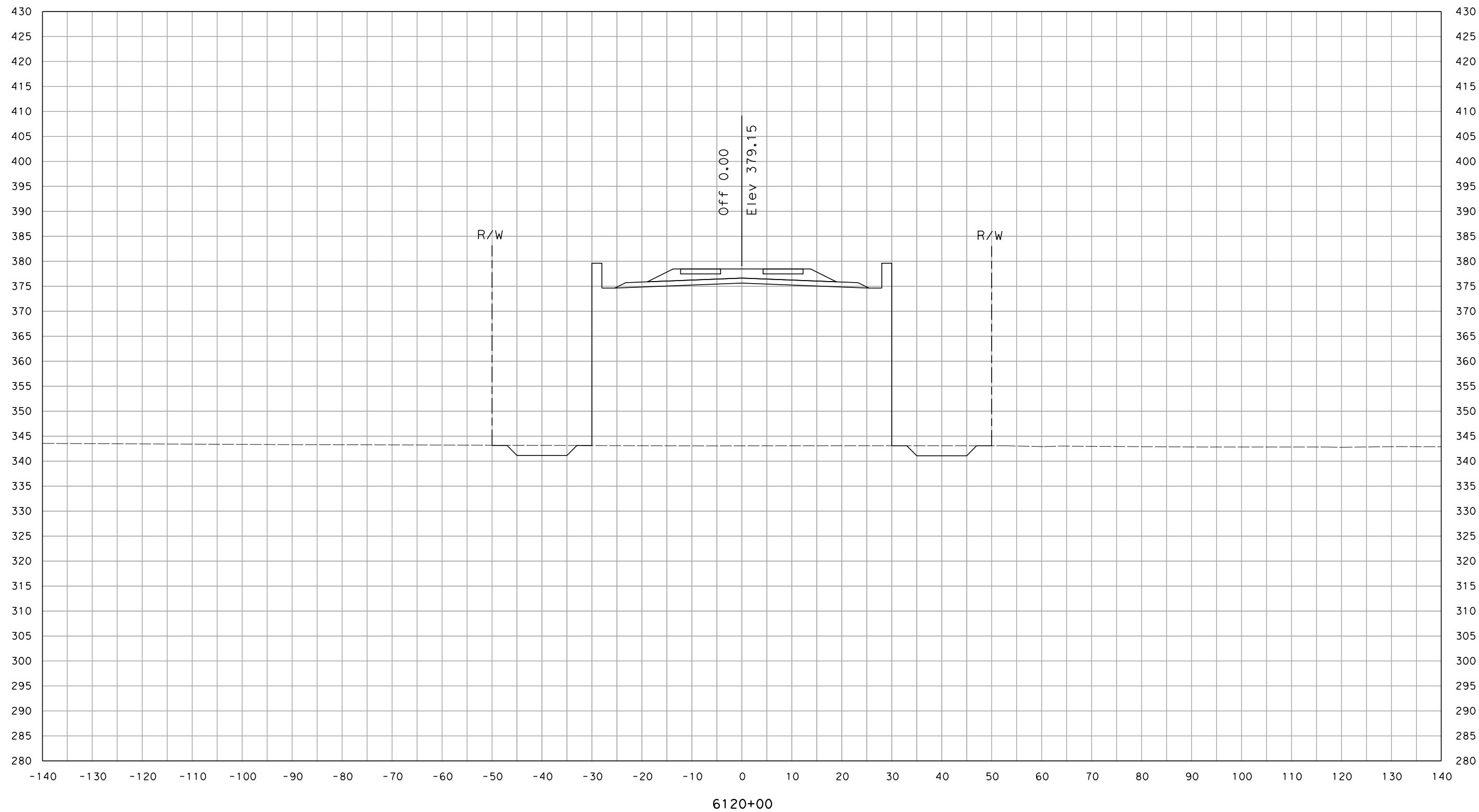
STA. 5955+00 TO STA. 5955+00



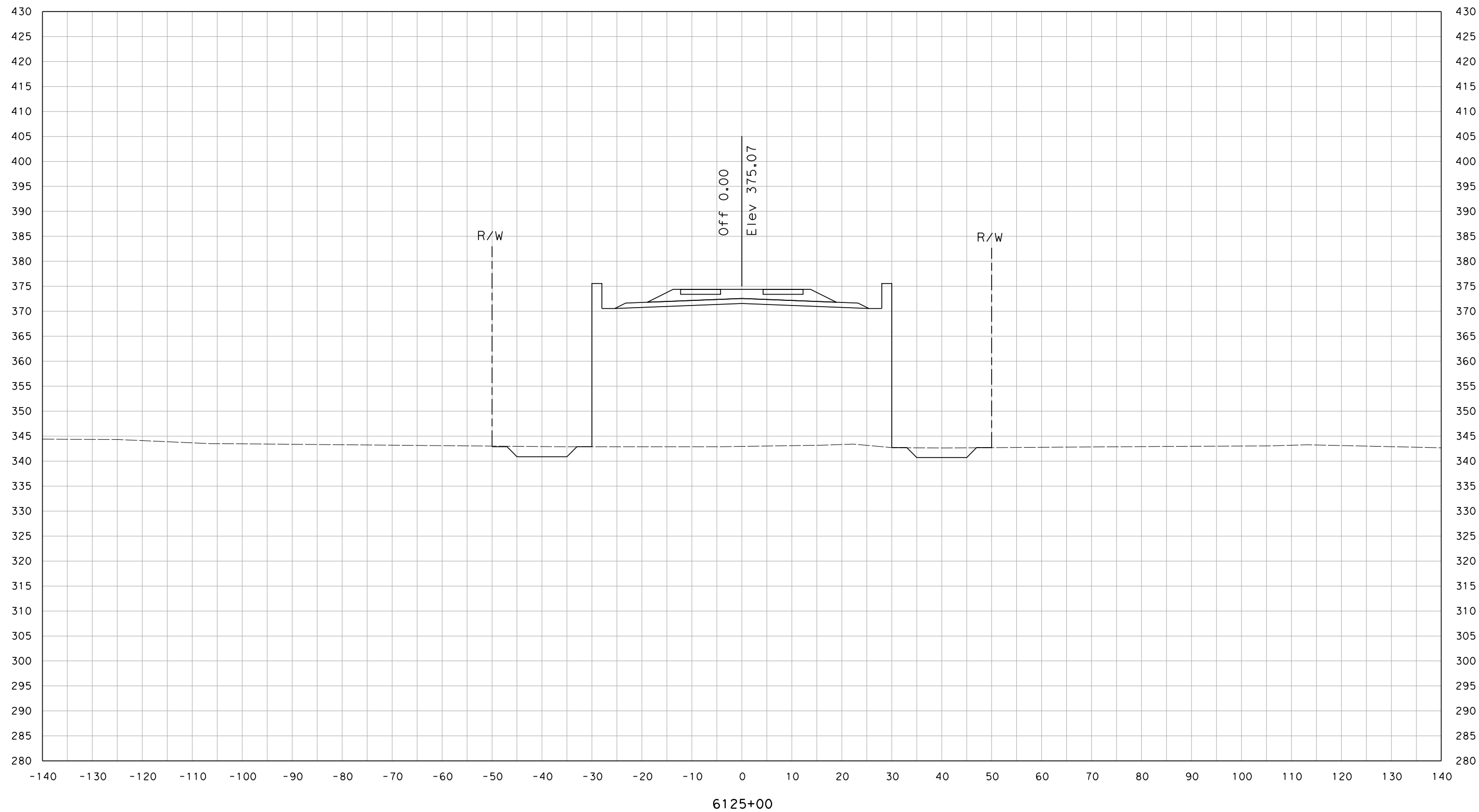
STA. 5955+30 TO STA. 5955+30



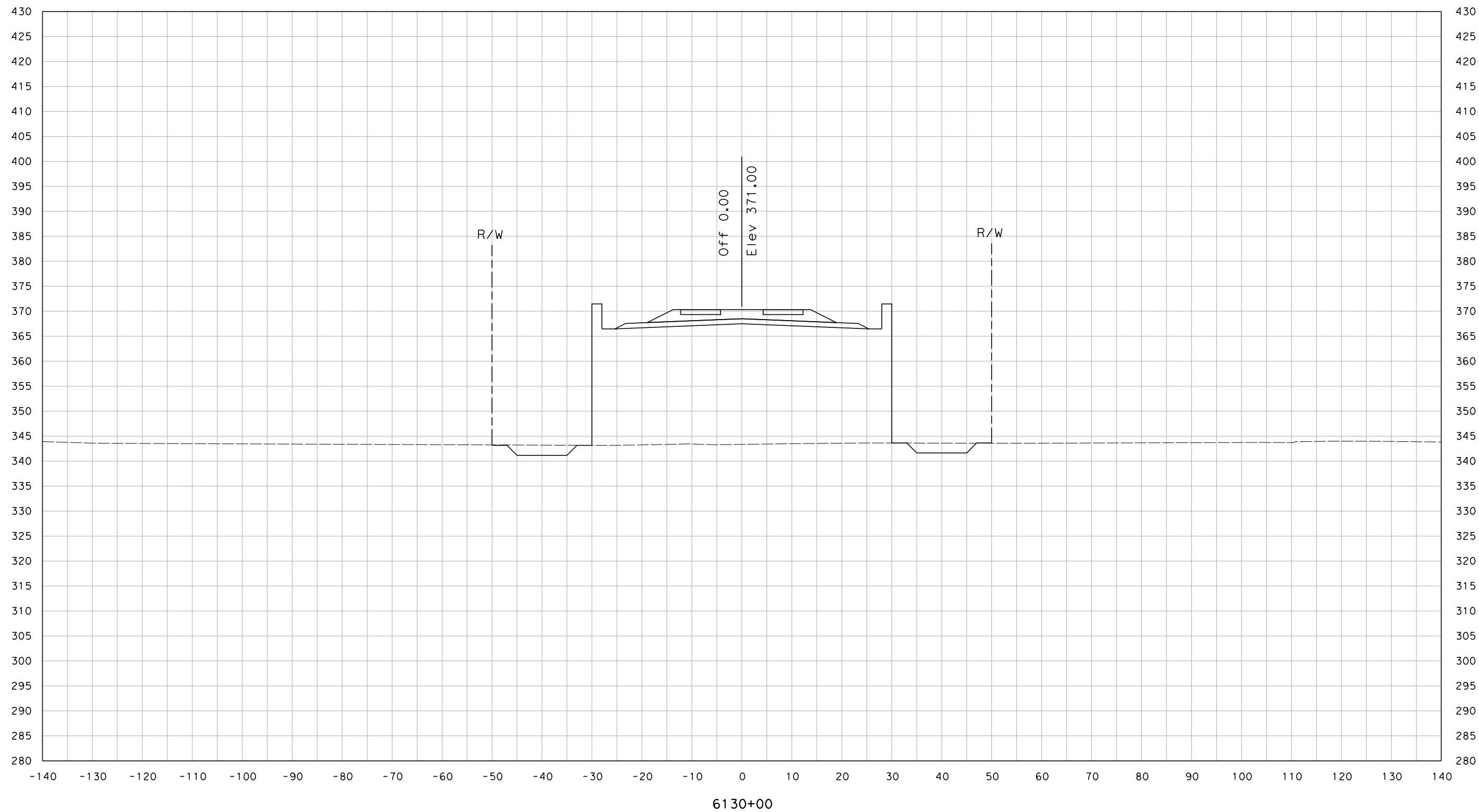
STA. 6117+25 TO STA. 6117+25



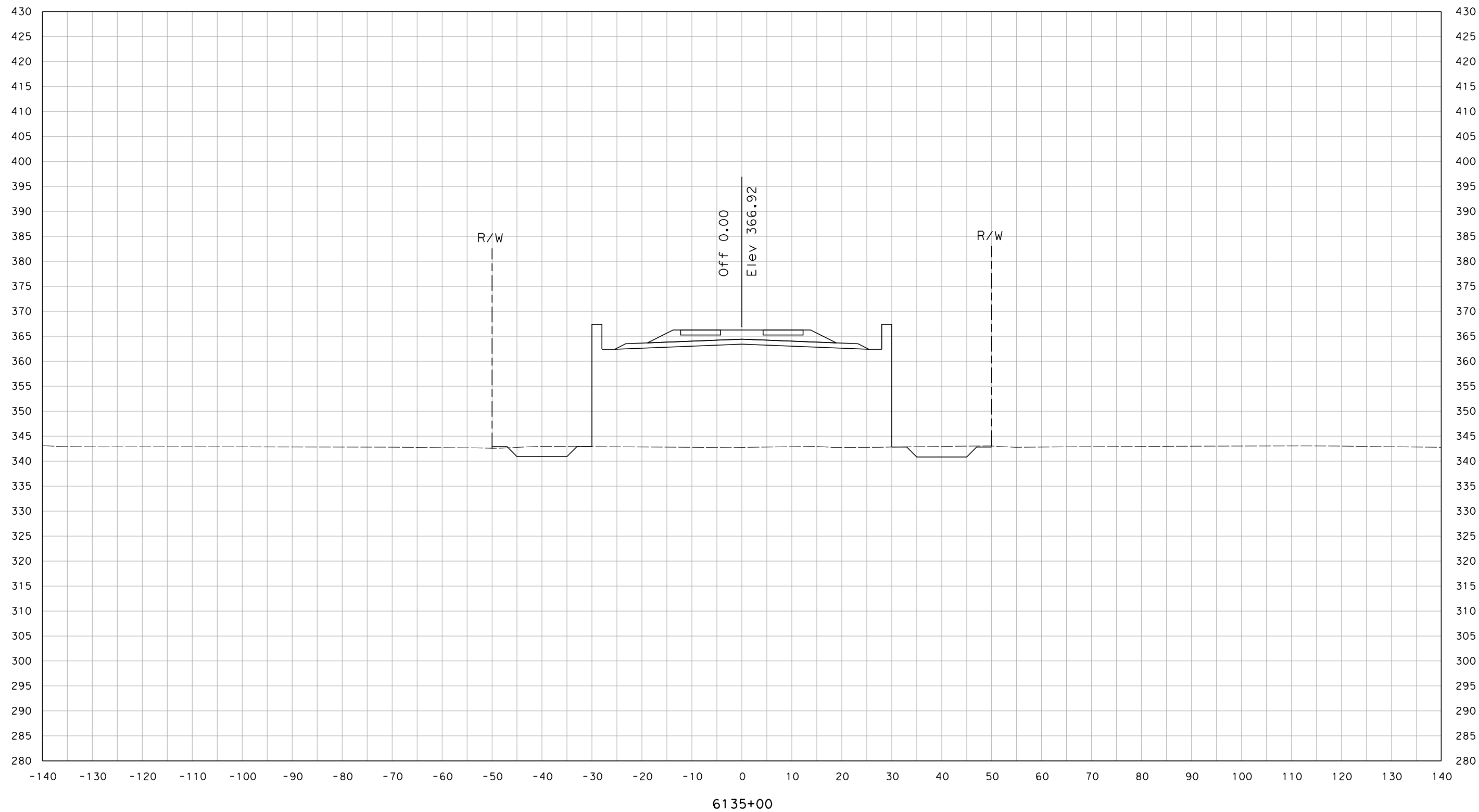
STA. 6120+00 TO STA. 6120+00



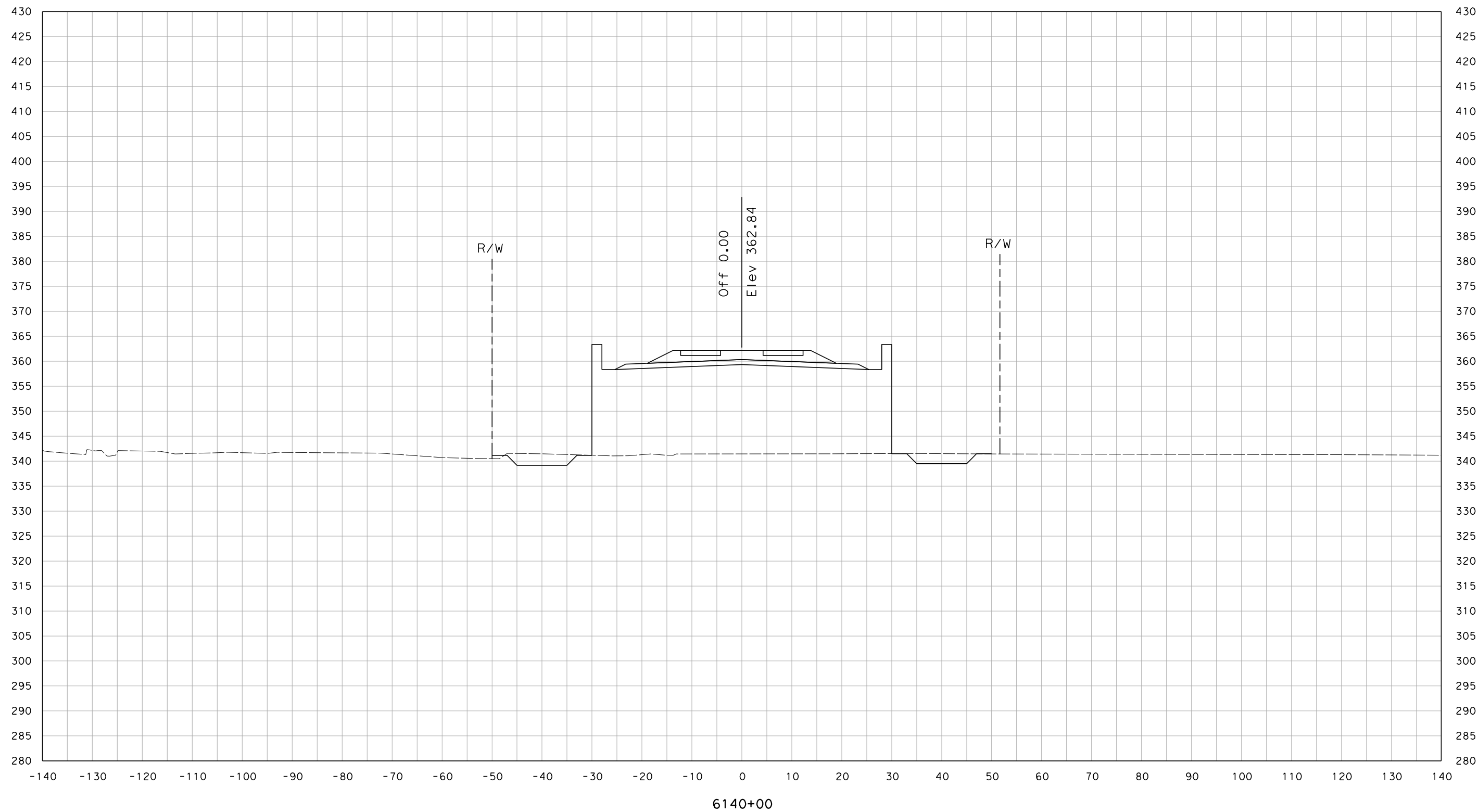
STA. 6125+00 TO STA. 6125+00



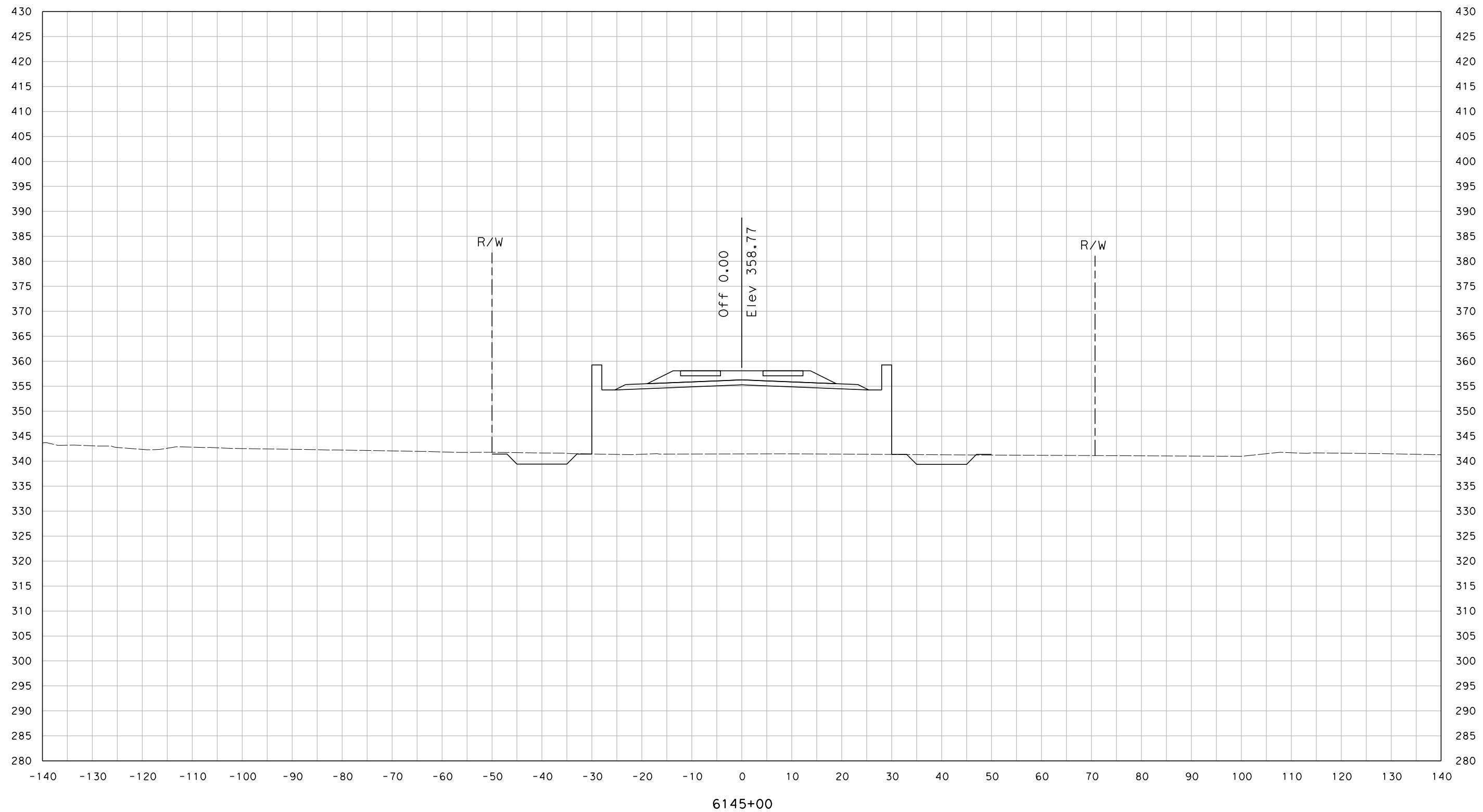
STA. 6130+00 TO STA. 6130+00



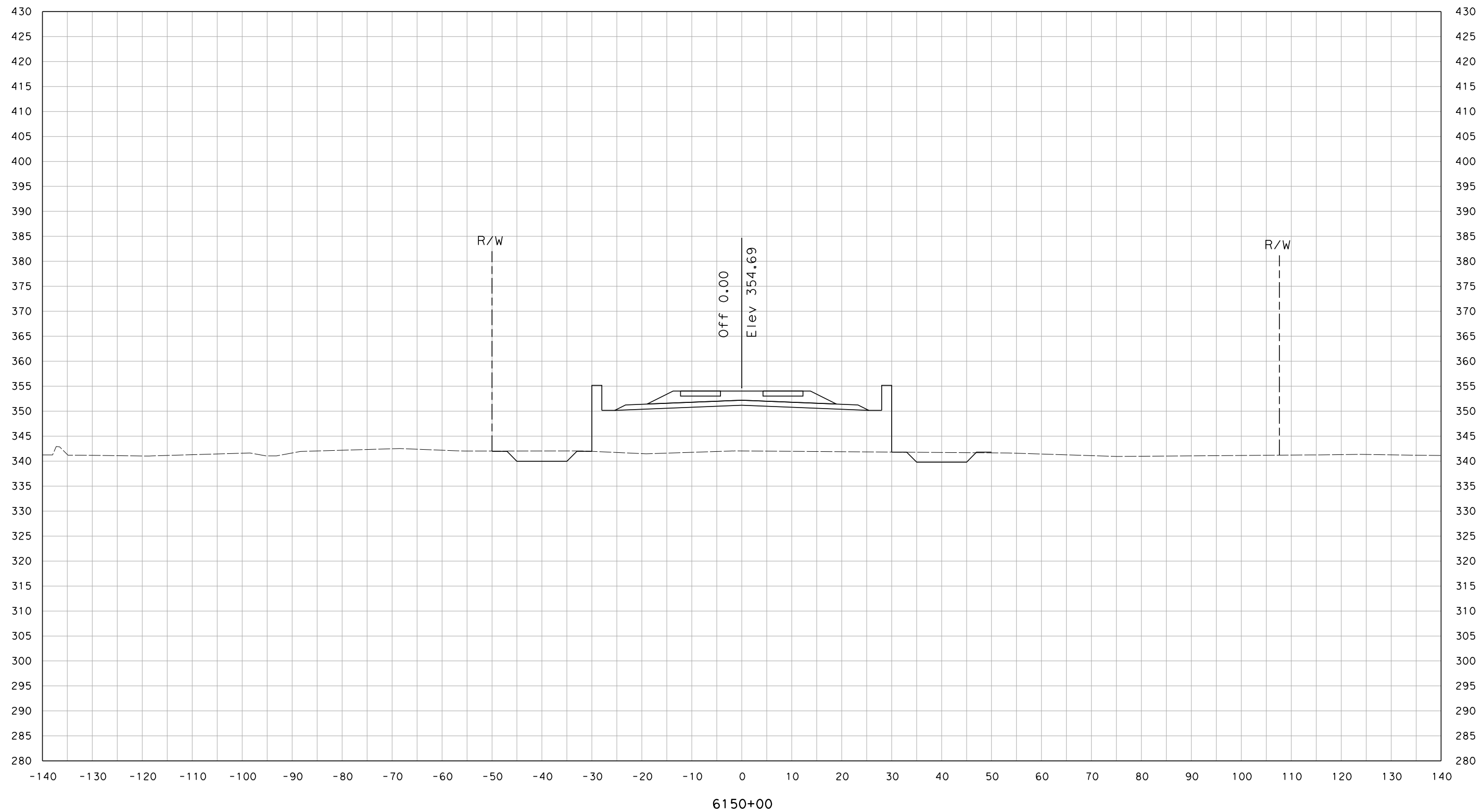
STA. 6135+00 TO STA. 6135+00



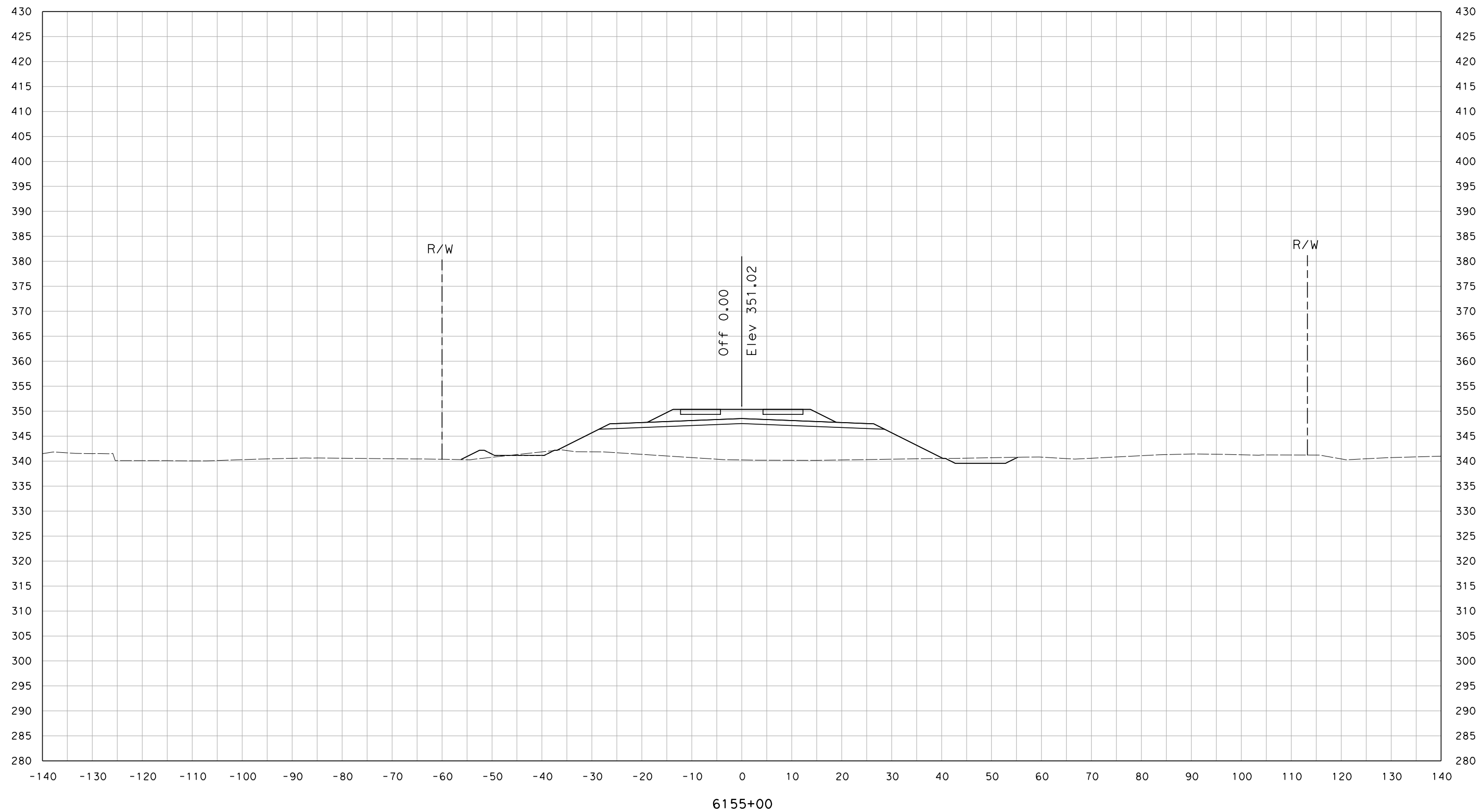
STA. 6140+00 TO STA. 6140+00



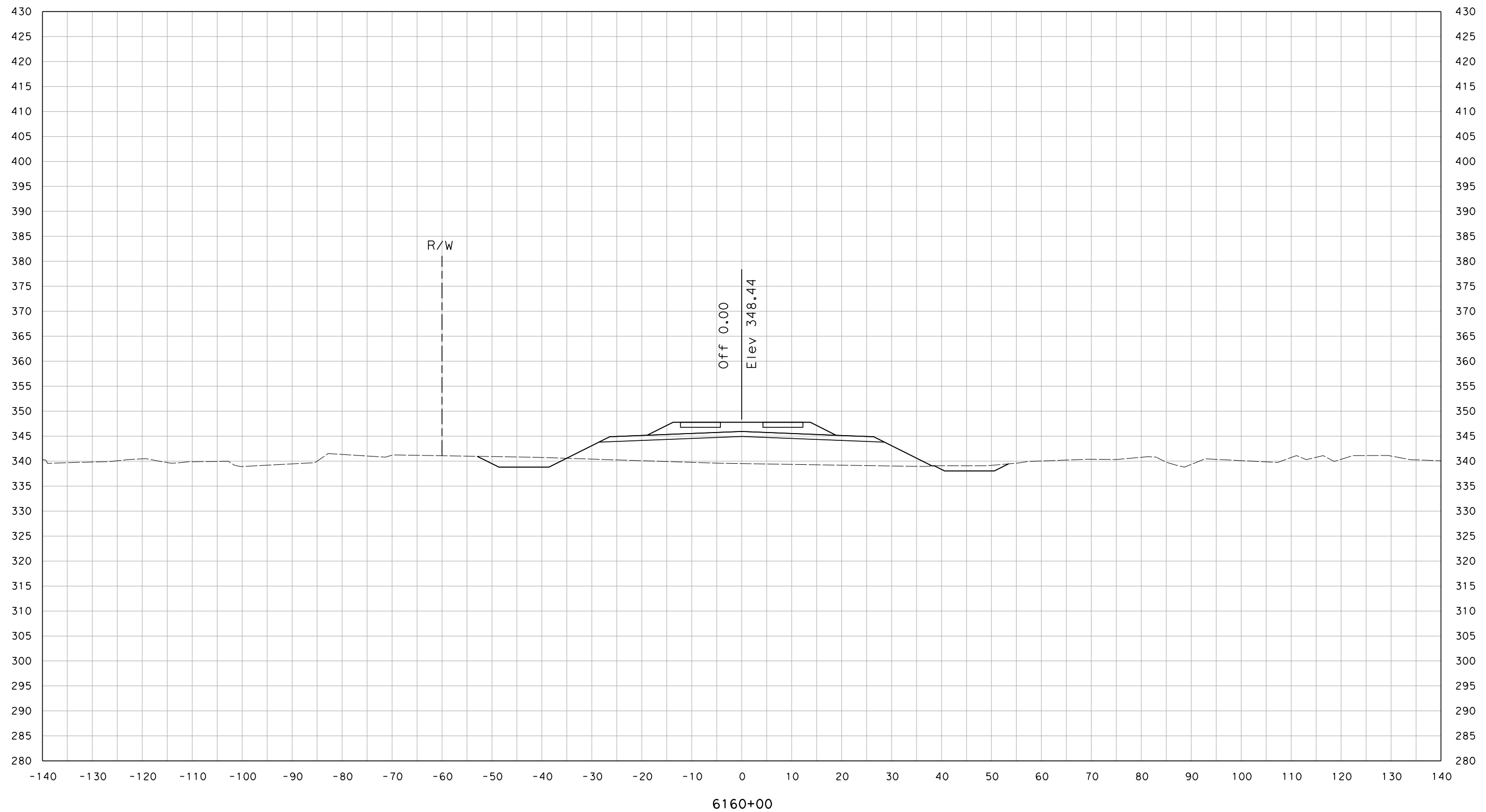
STA. 6145+00 TO STA. 6145+00



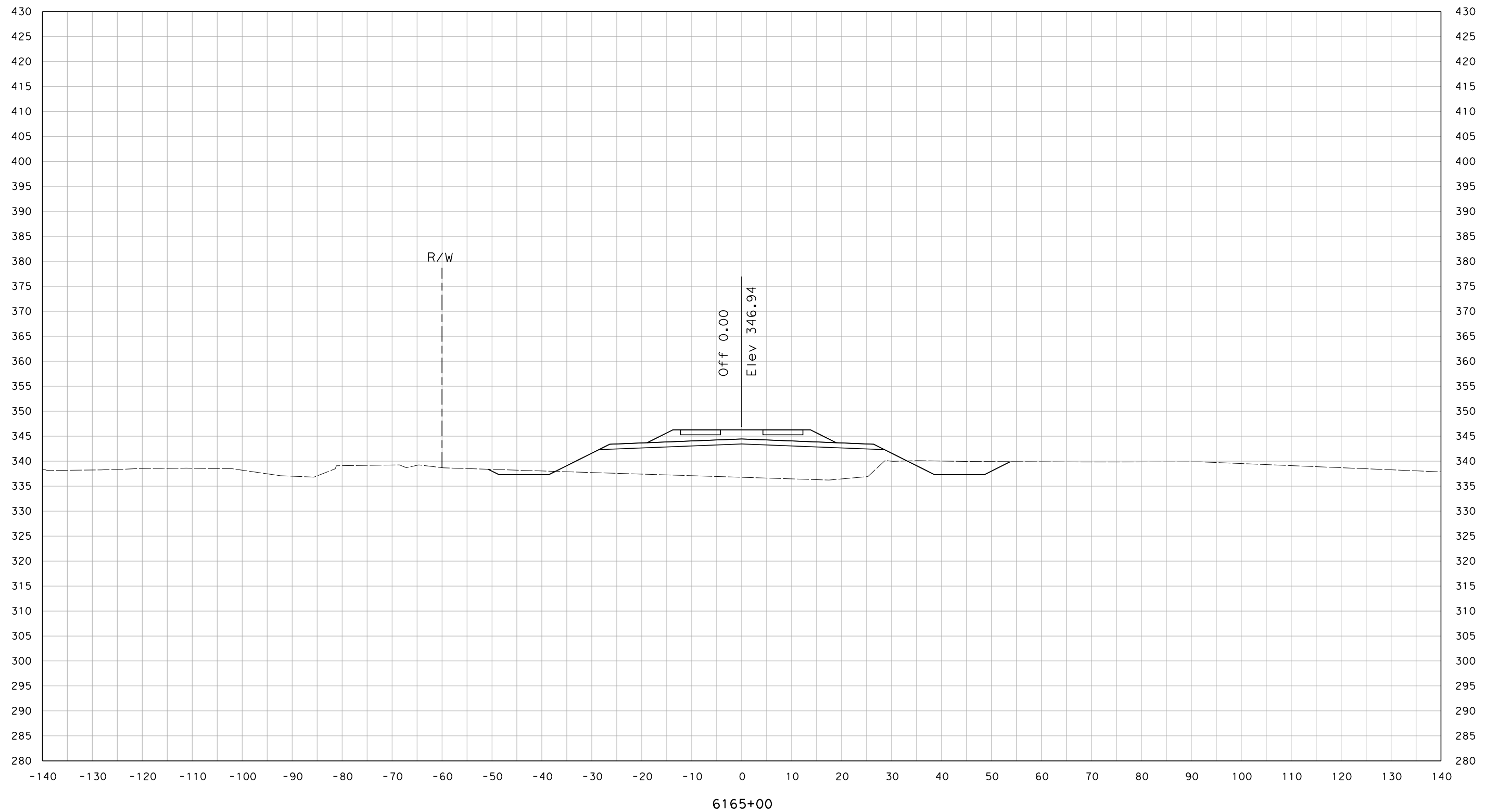
STA. 6150+00 TO STA. 6150+00



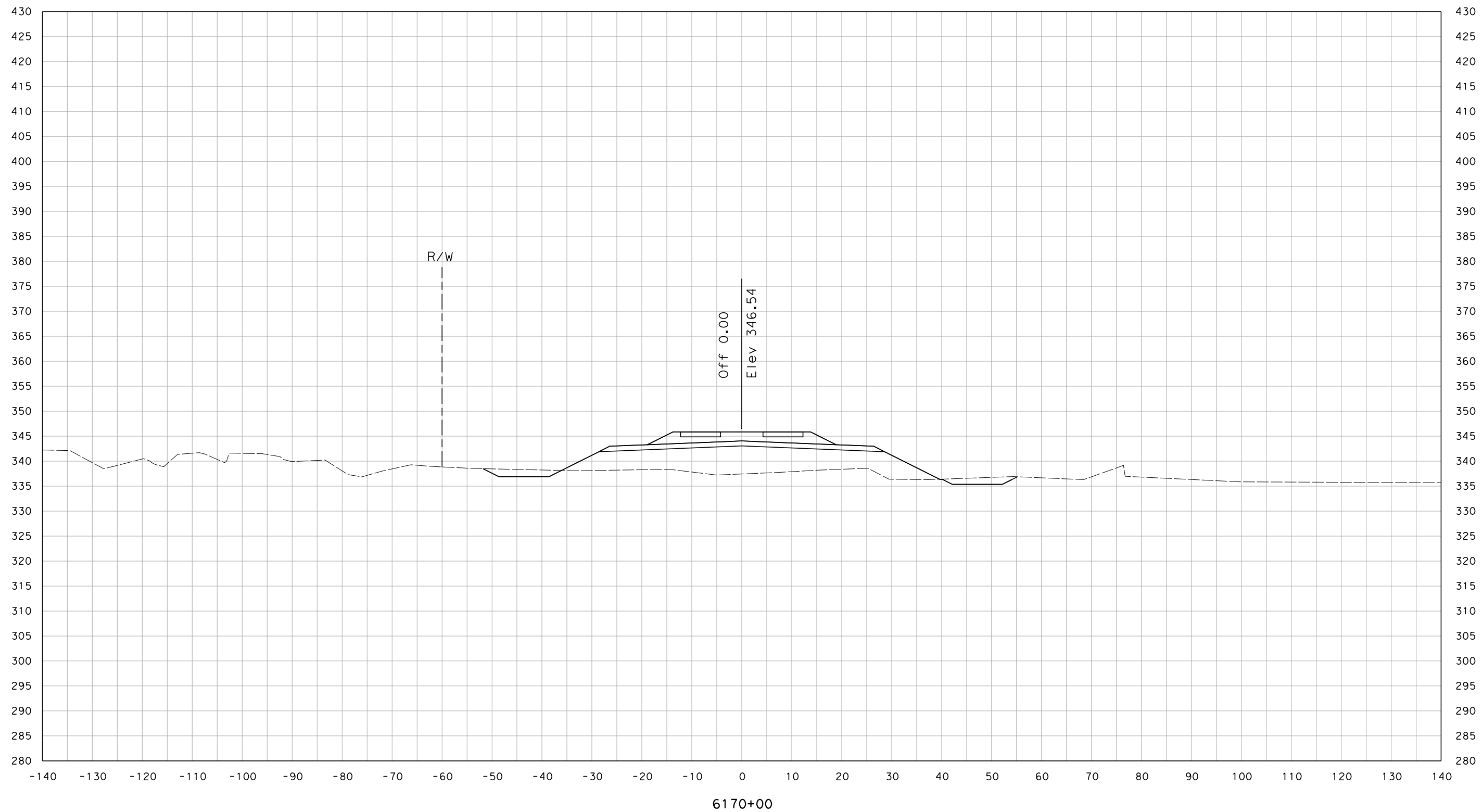
STA. 6155+00 TO STA. 6155+00



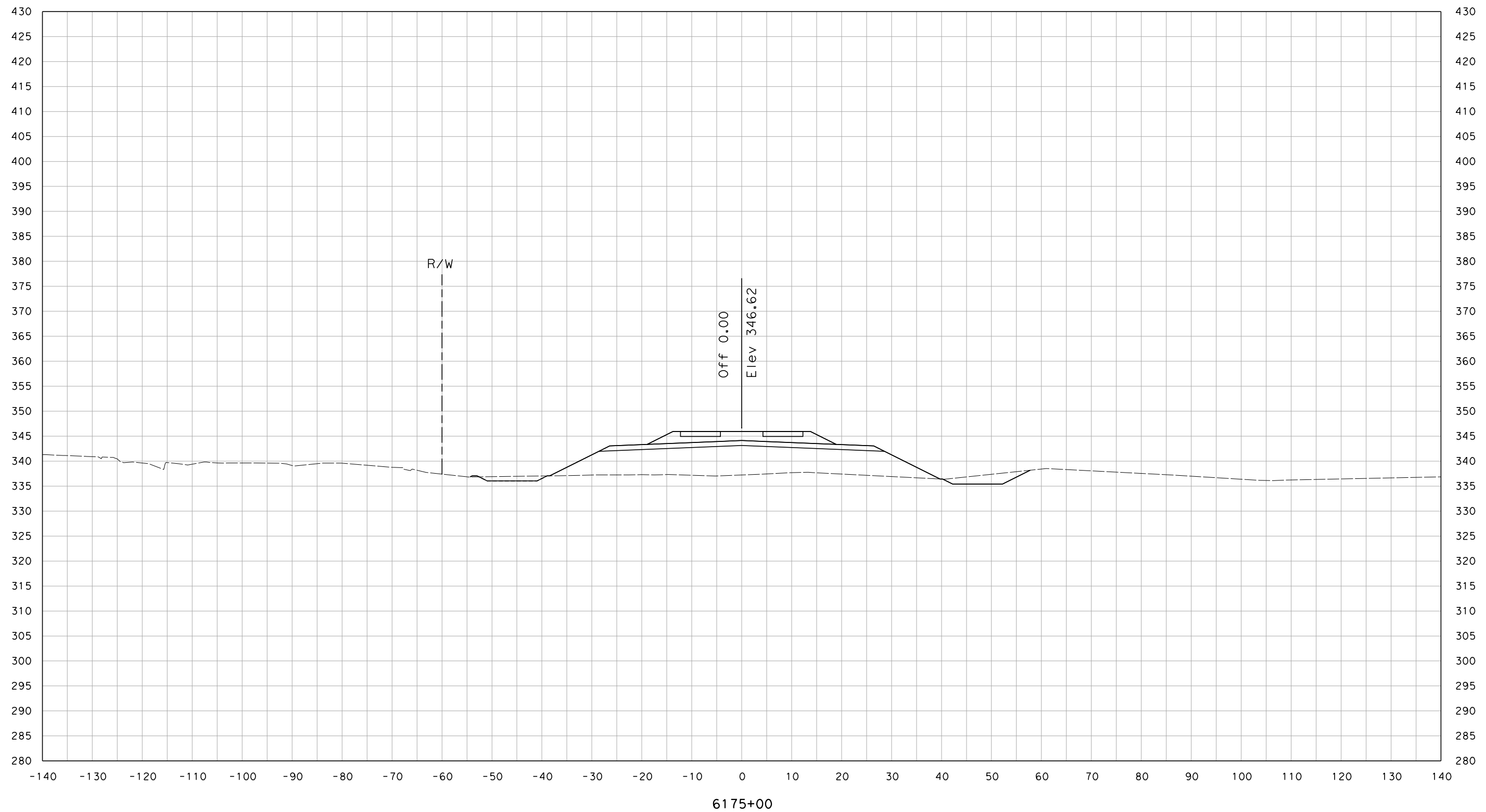
STA. 6160+00 TO STA. 6160+00



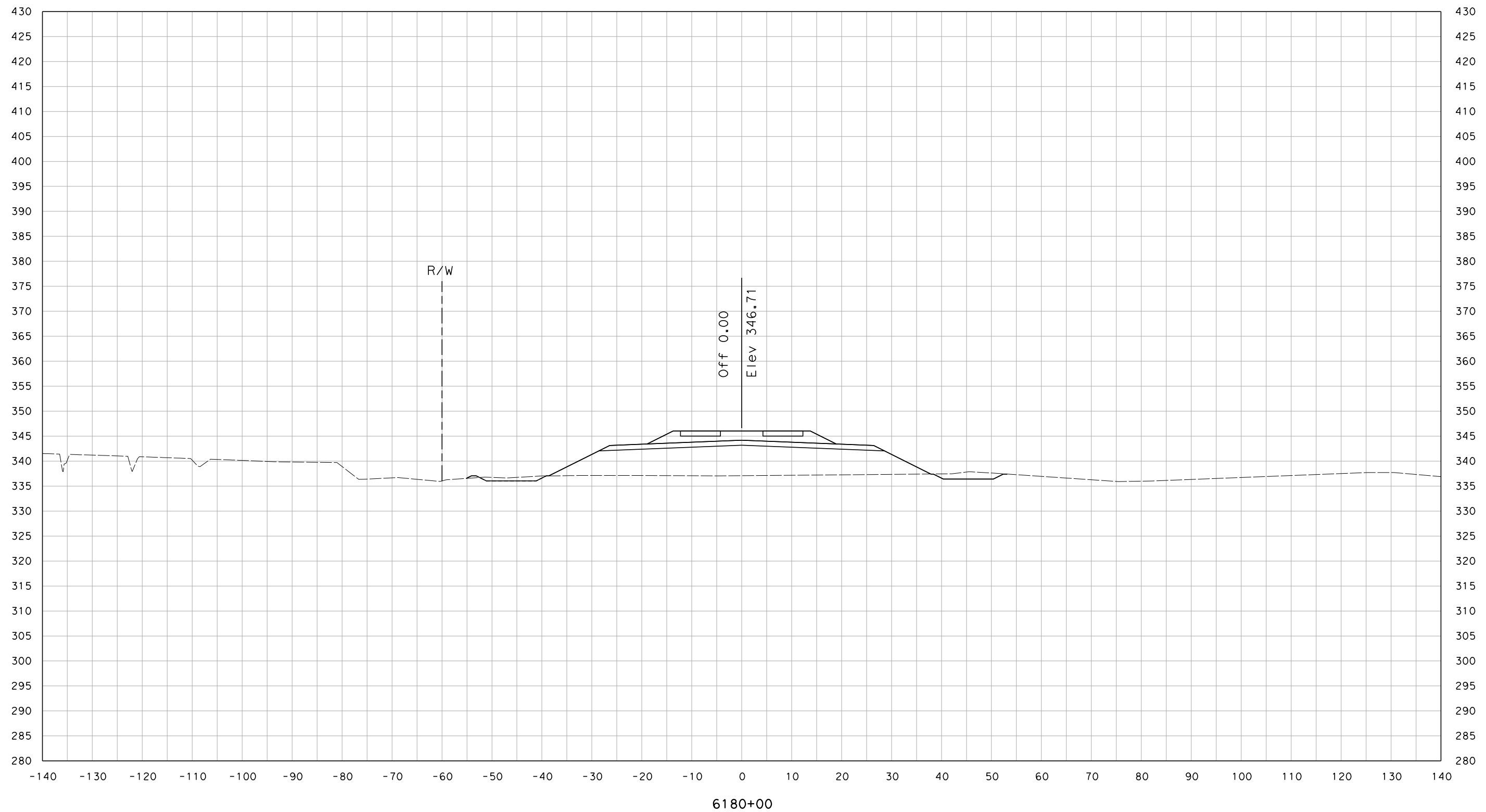
STA. 6165+00 TO STA. 6165+00



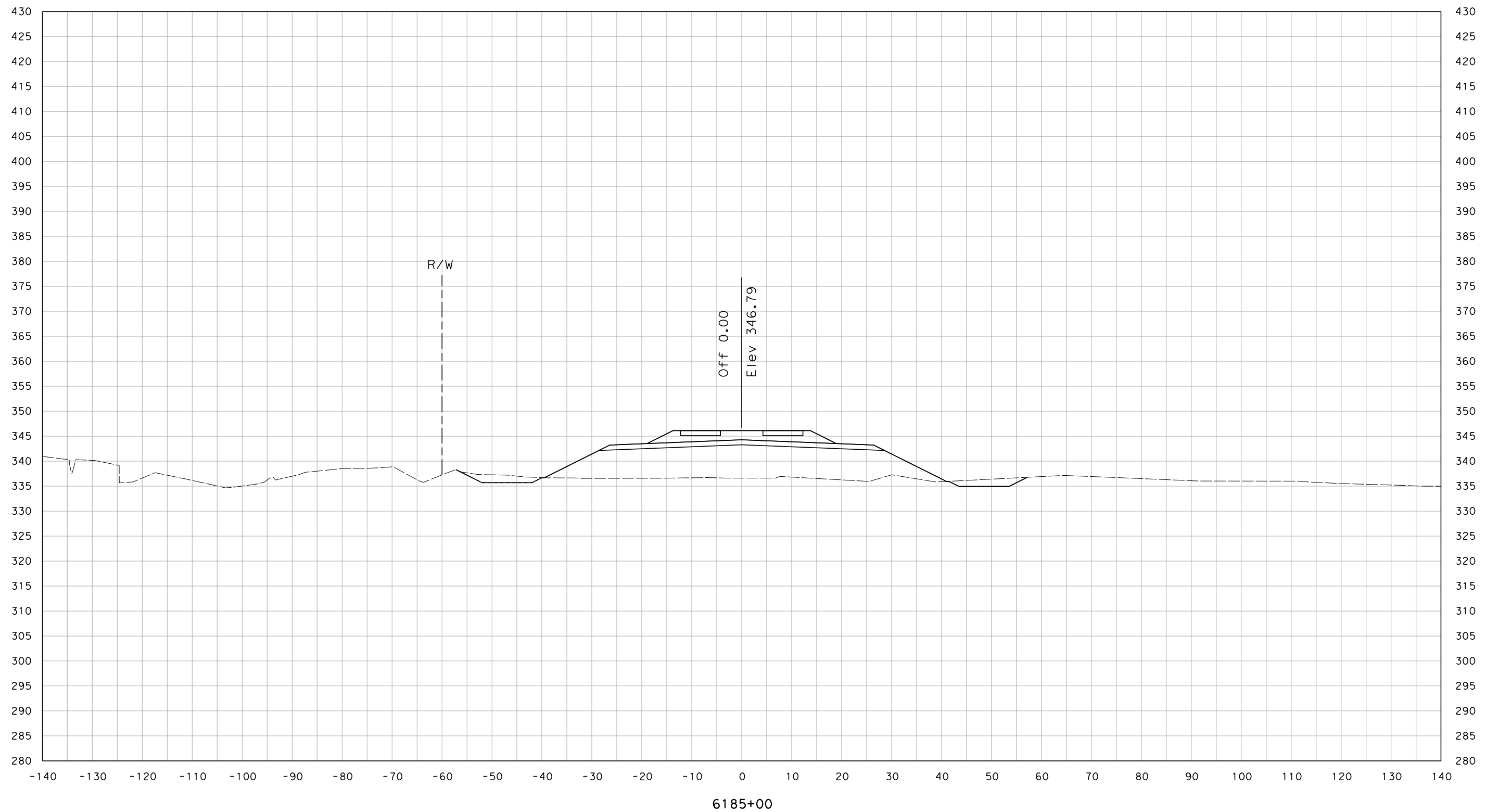
STA. 6170+00 TO STA. 6170+00



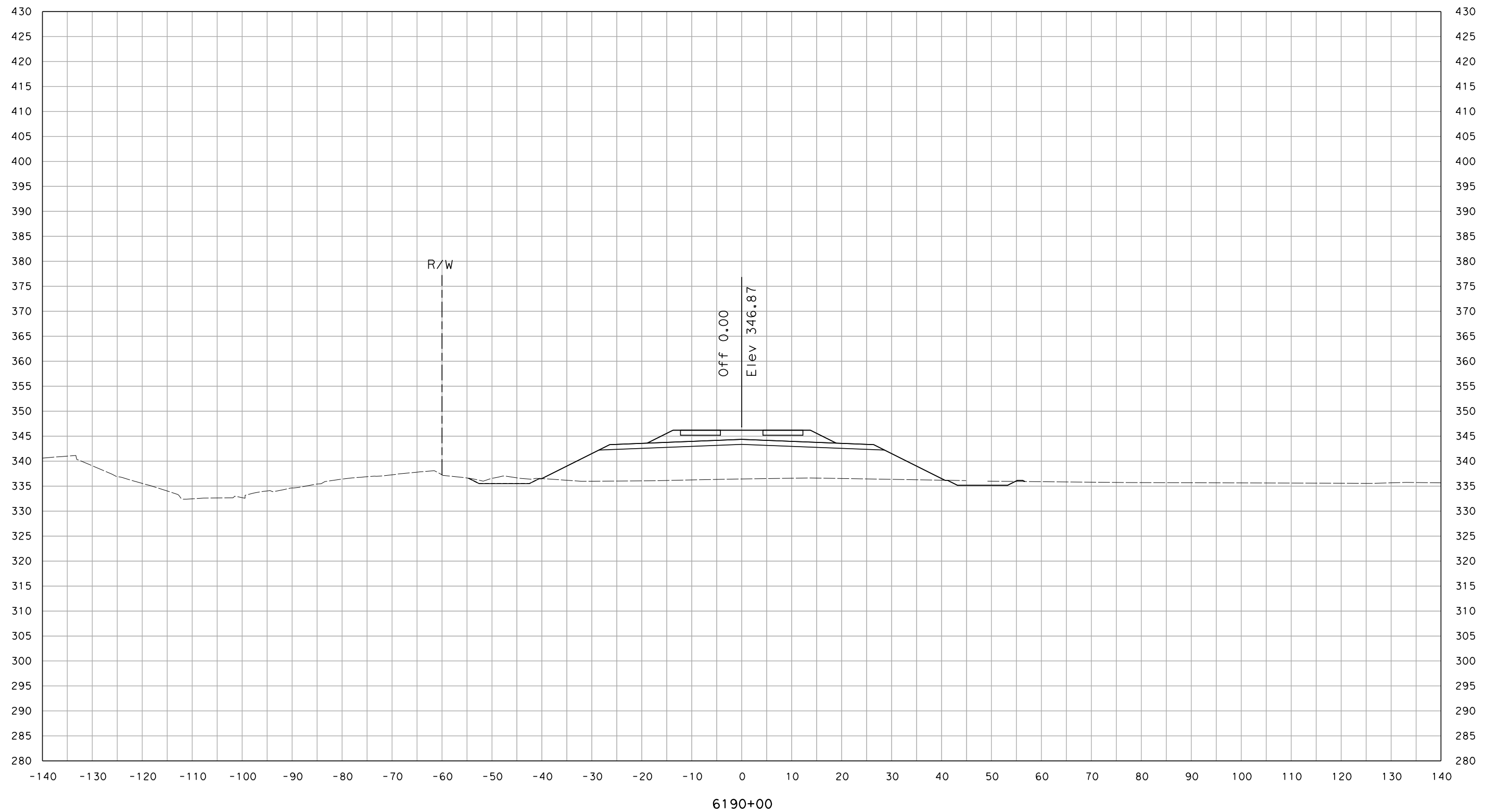
STA. 6175+00 TO STA. 6175+00



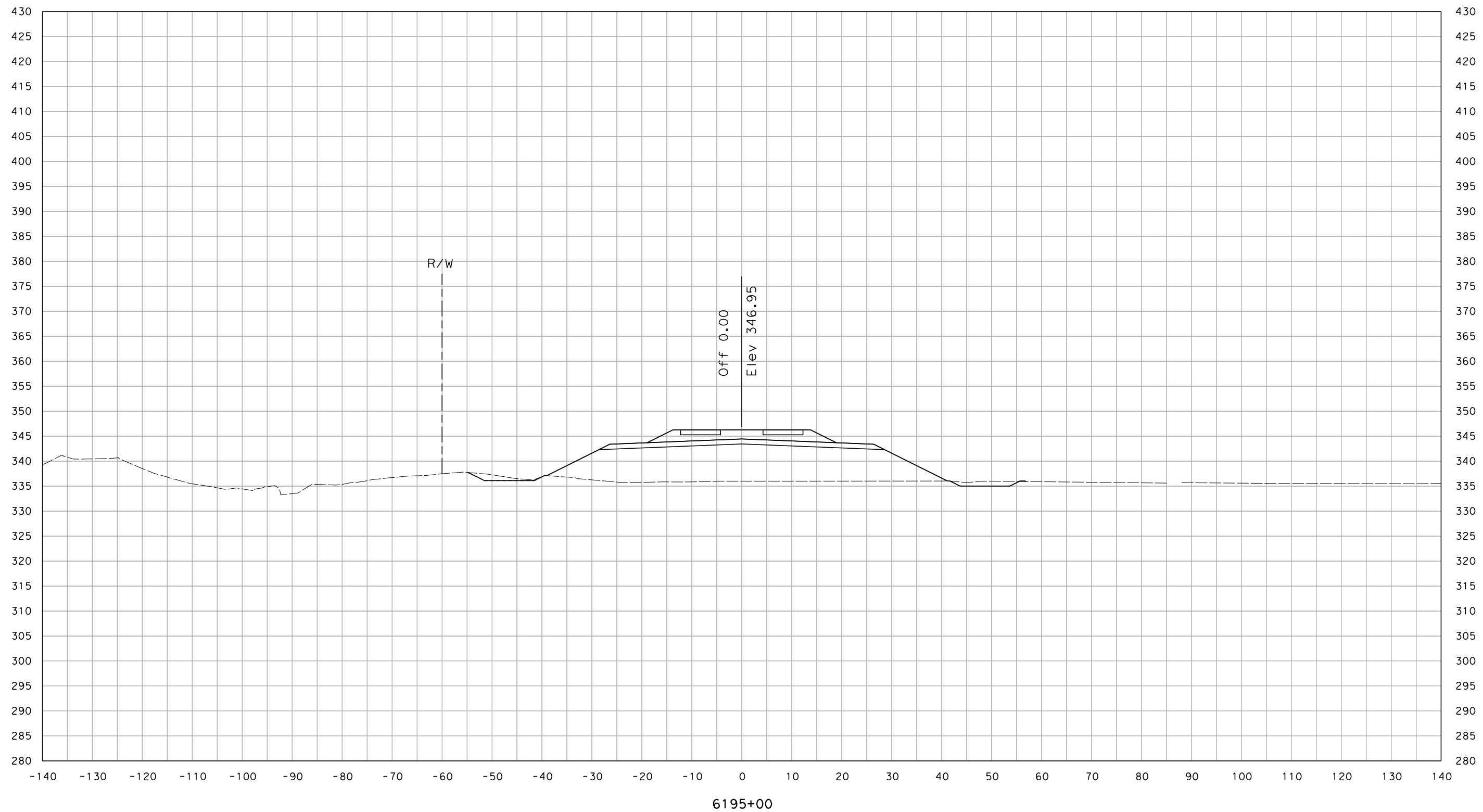
STA. 6180+00 TO STA. 6180+00



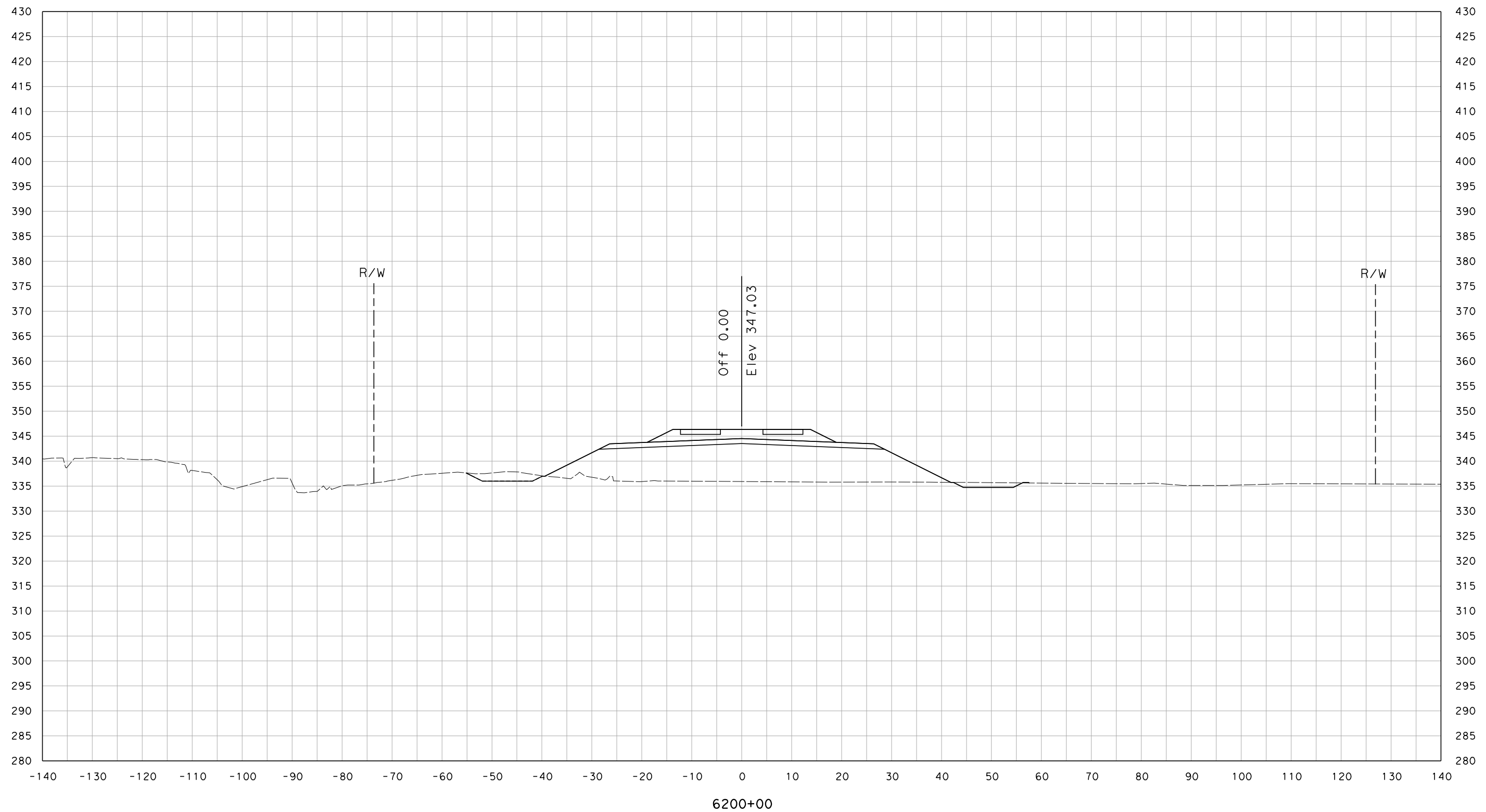
STA. 6185+00 TO STA. 6185+00



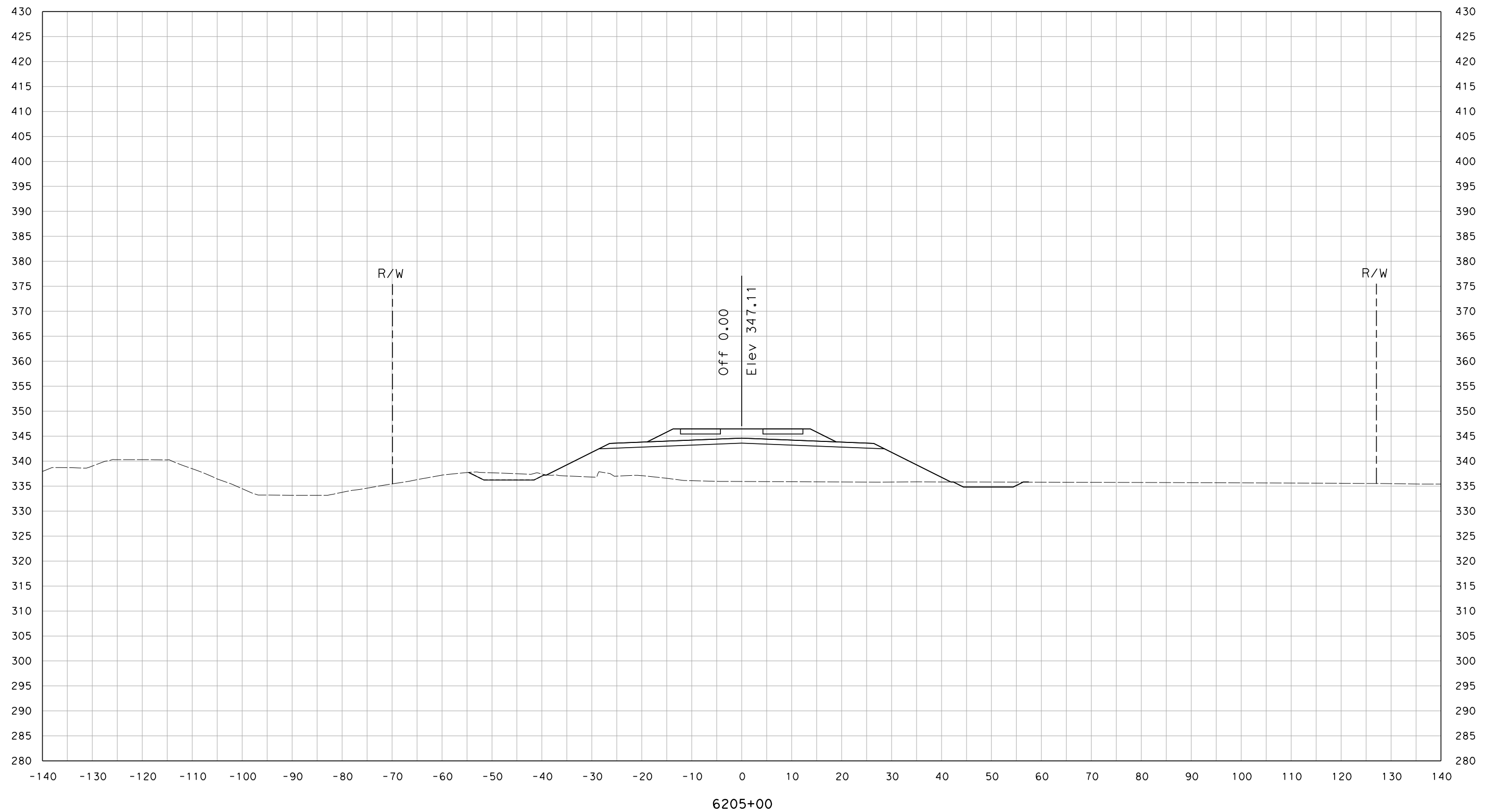
STA. 6190+00 TO STA. 6190+00



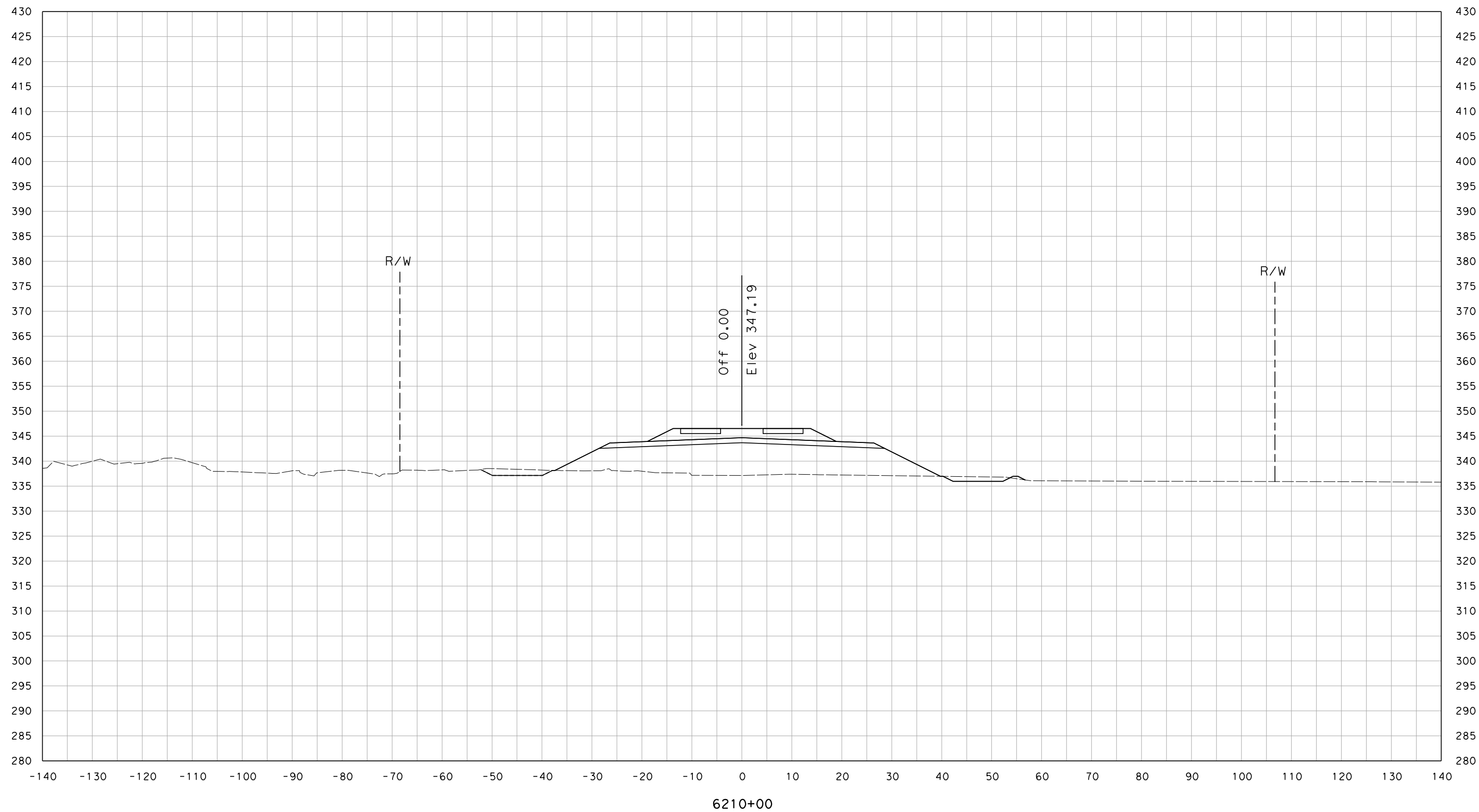
STA. 6195+00 TO STA. 6195+00



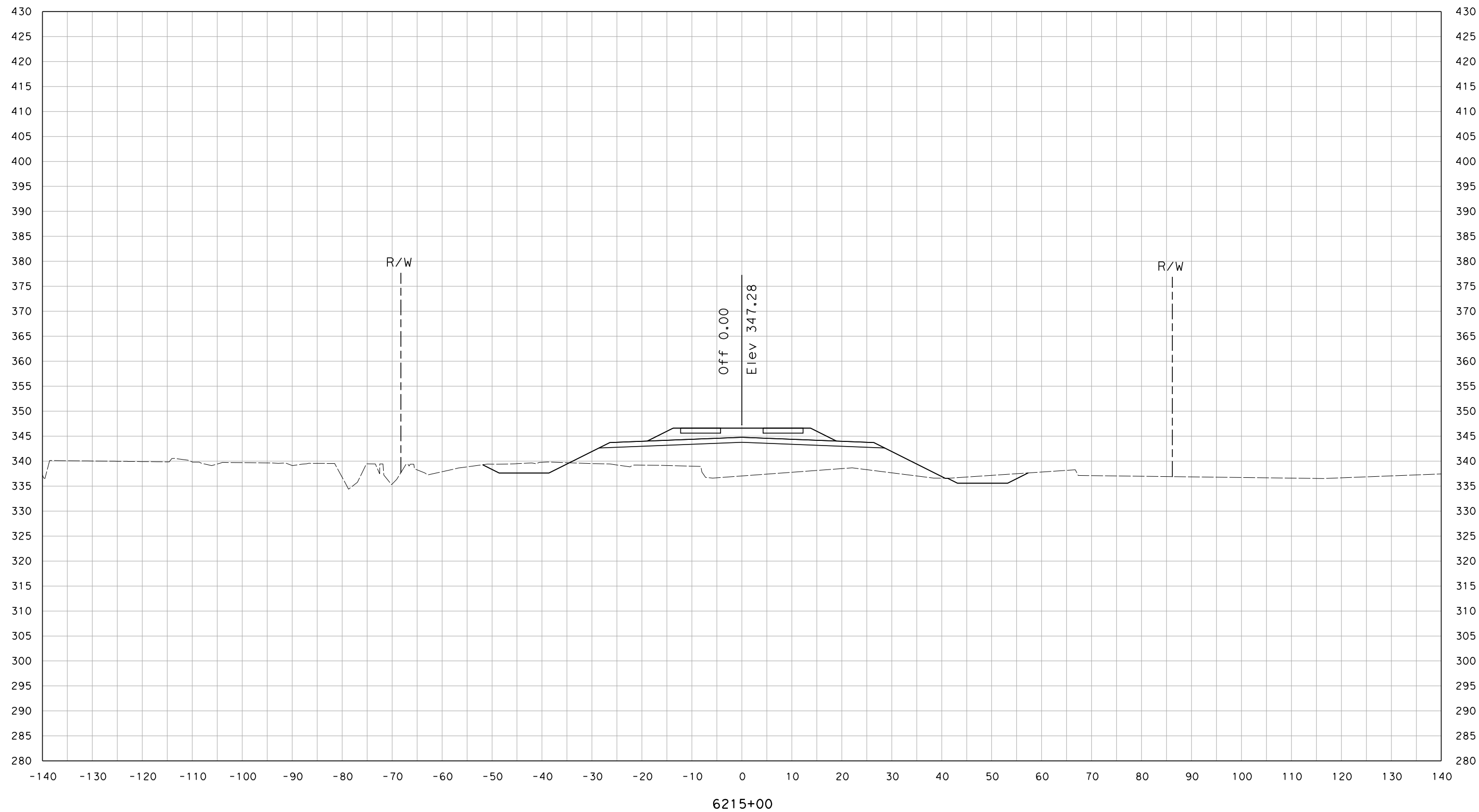
STA. 6200+00 TO STA. 6200+00



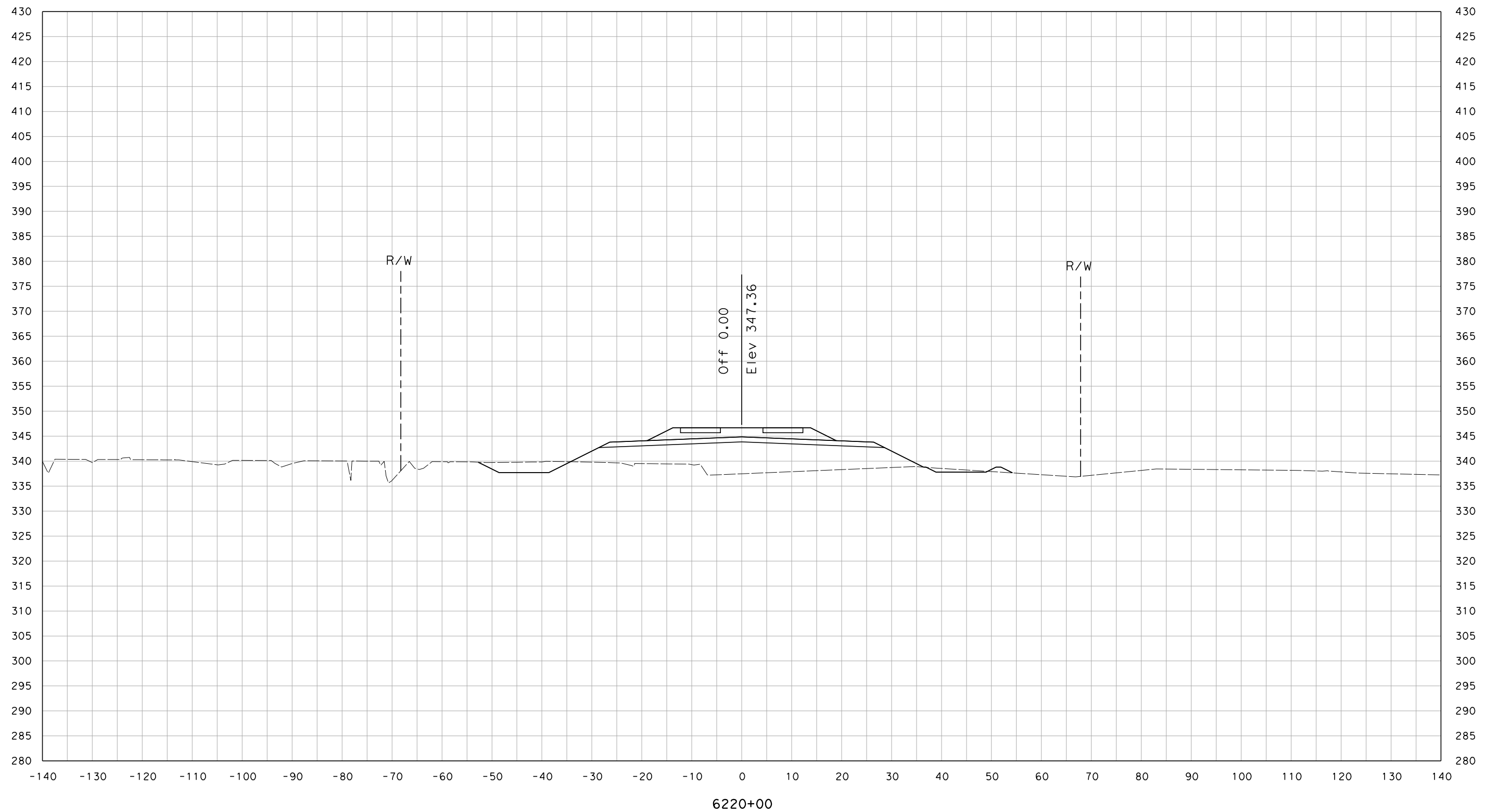
STA. 6205+00 TO STA. 6205+00



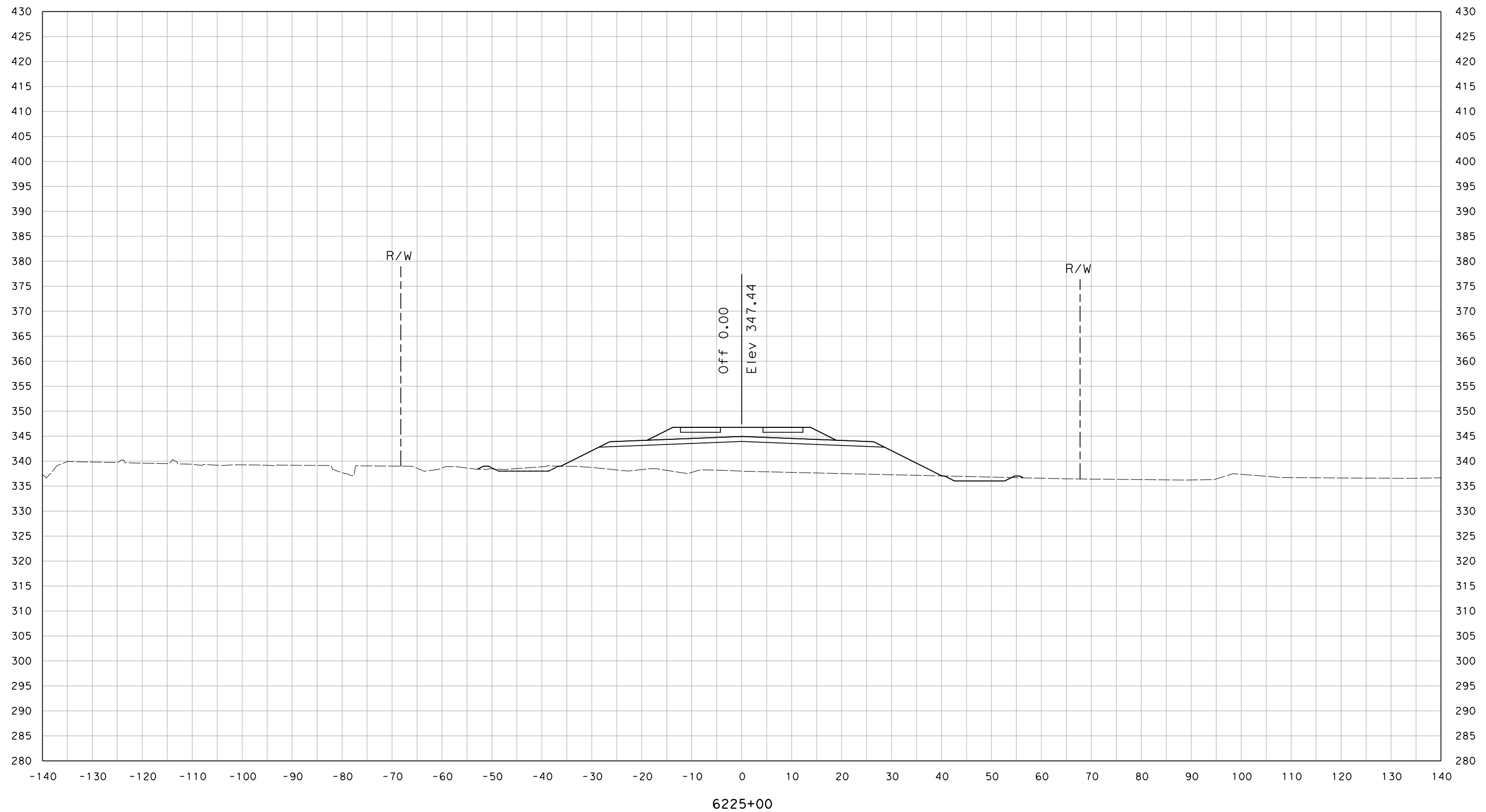
STA. 6210+00 TO STA. 6210+00



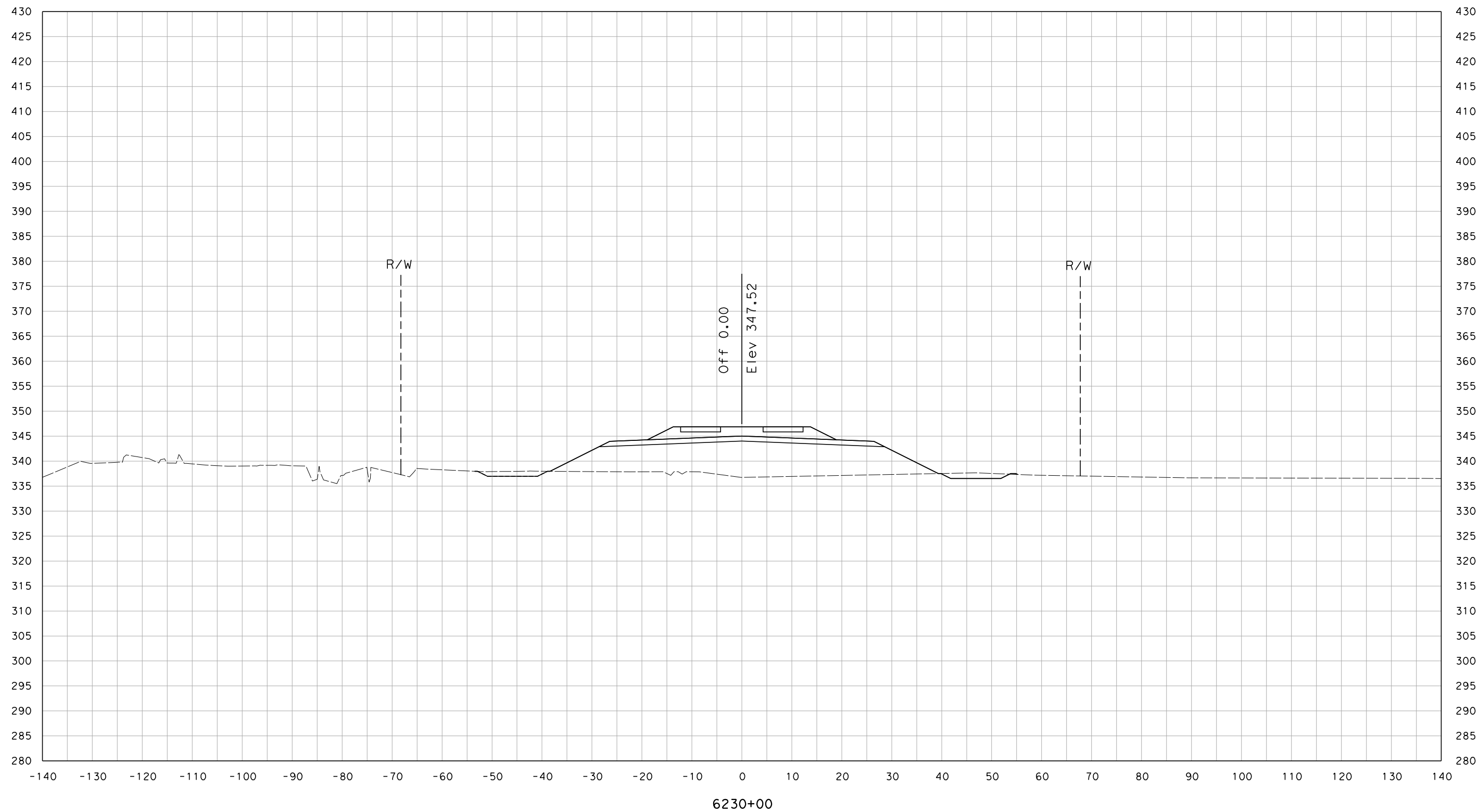
STA. 6215+00 TO STA. 6215+00



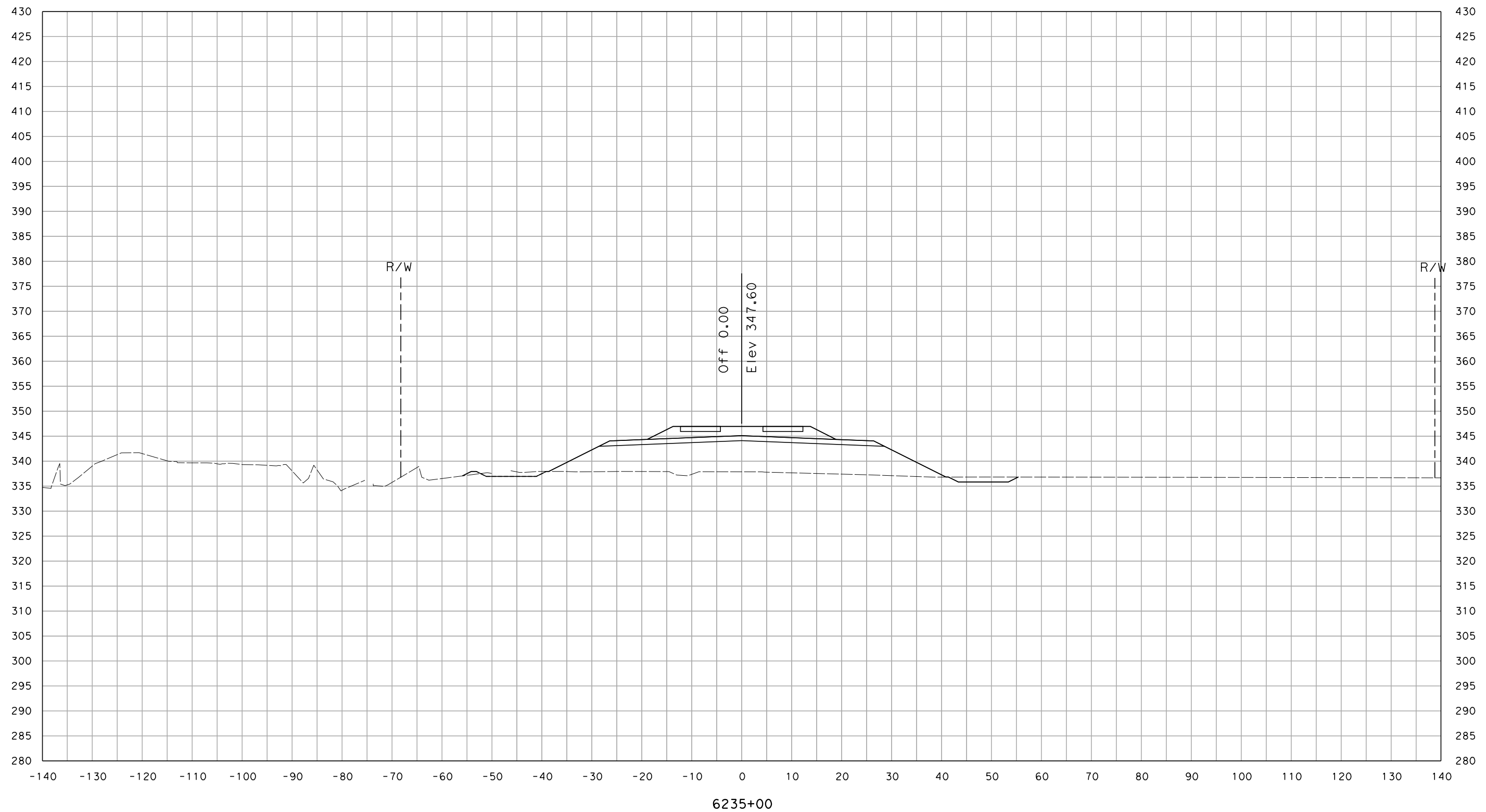
STA. 6220+00 TO STA. 6220+00



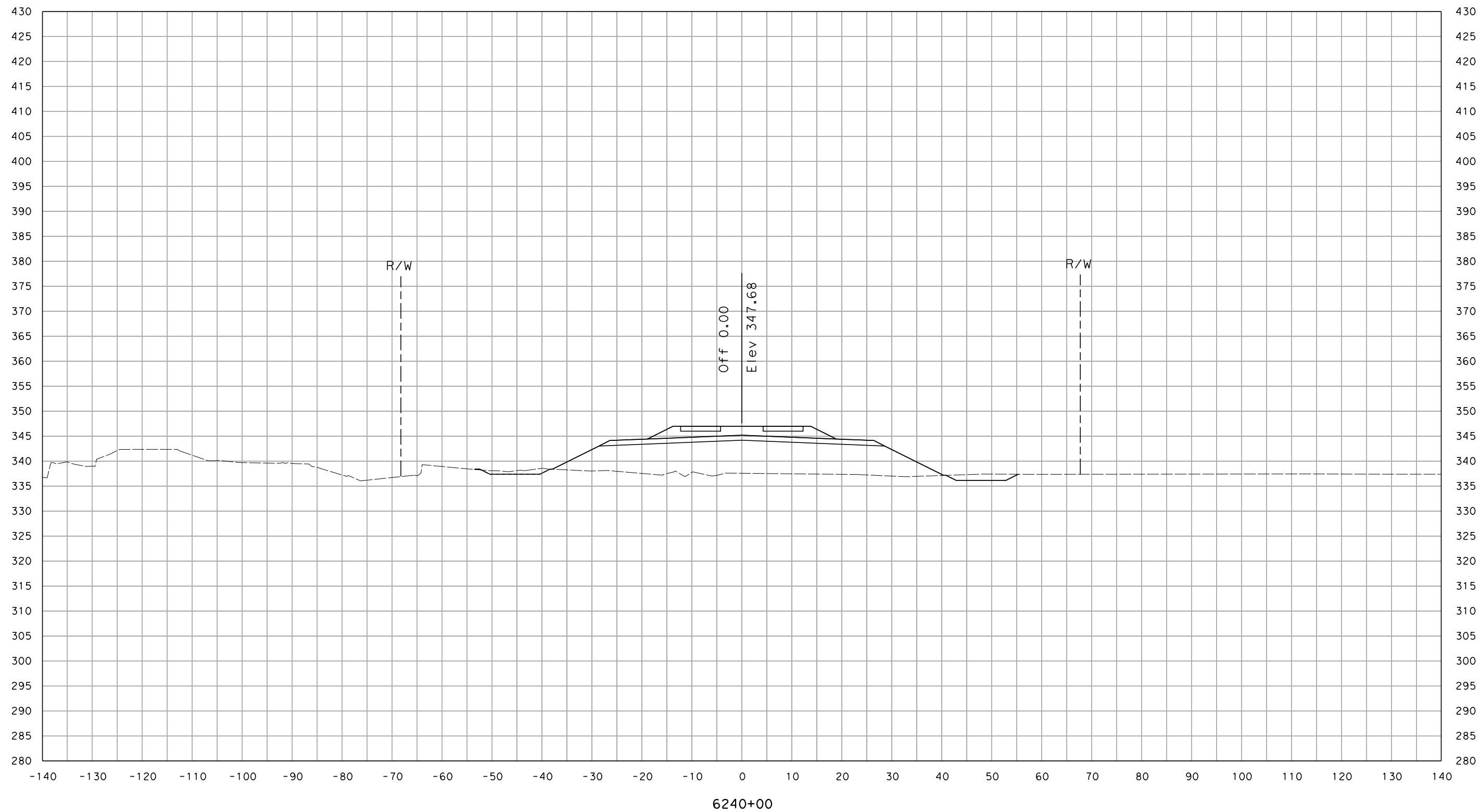
STA. 6225+00 TO STA. 6225+00



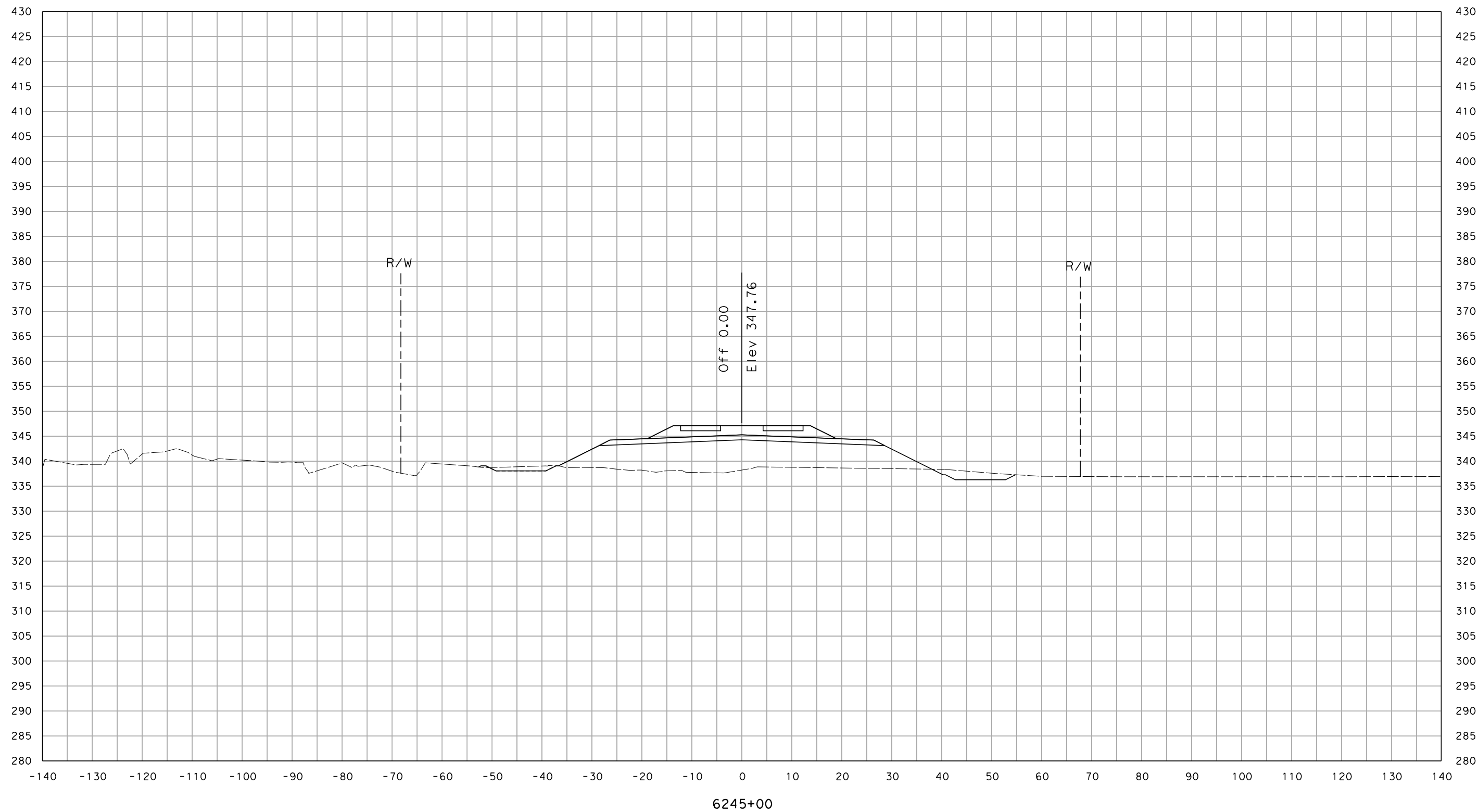
STA. 6230+00 TO STA. 6230+00



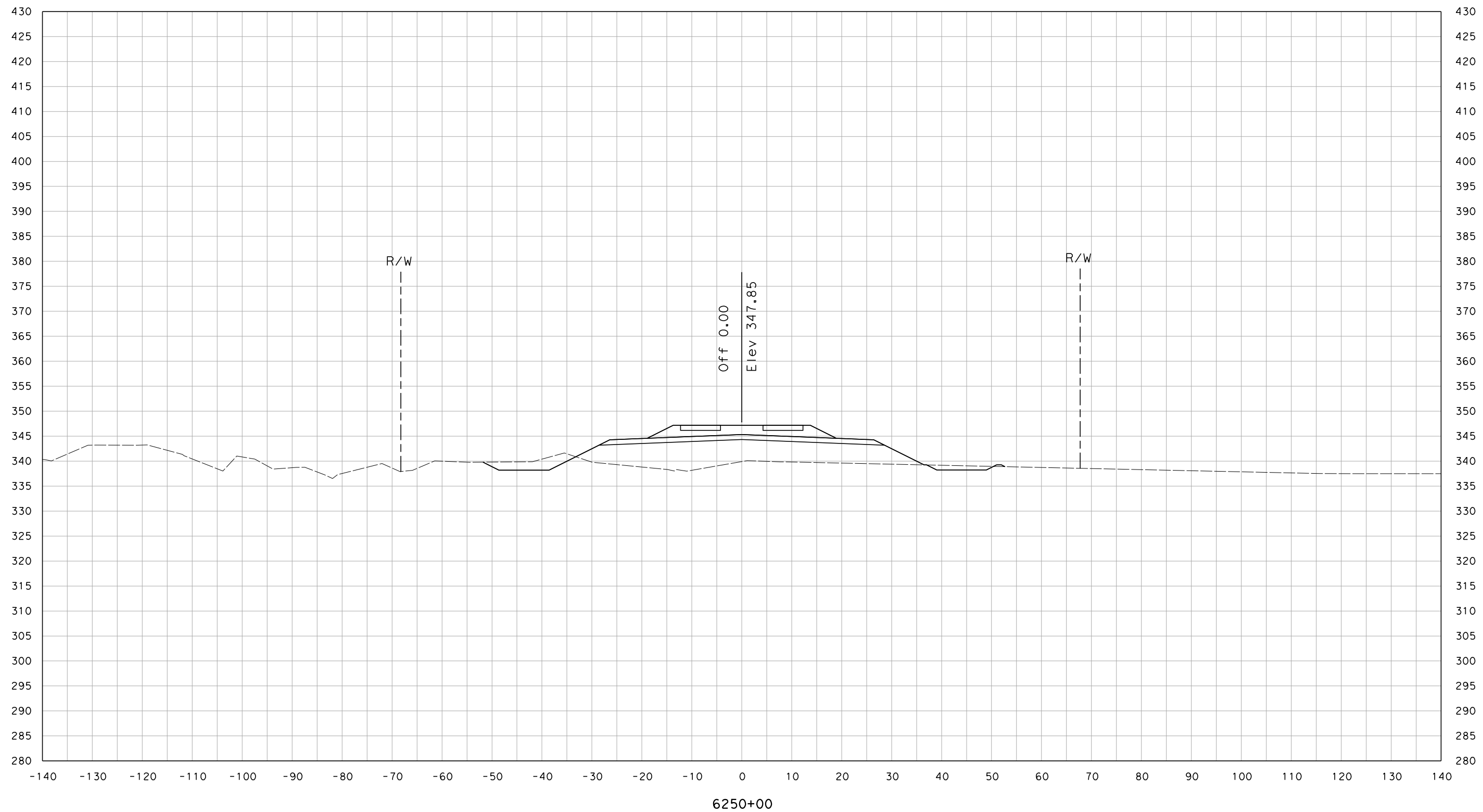
STA. 6235+00 TO STA. 6235+00



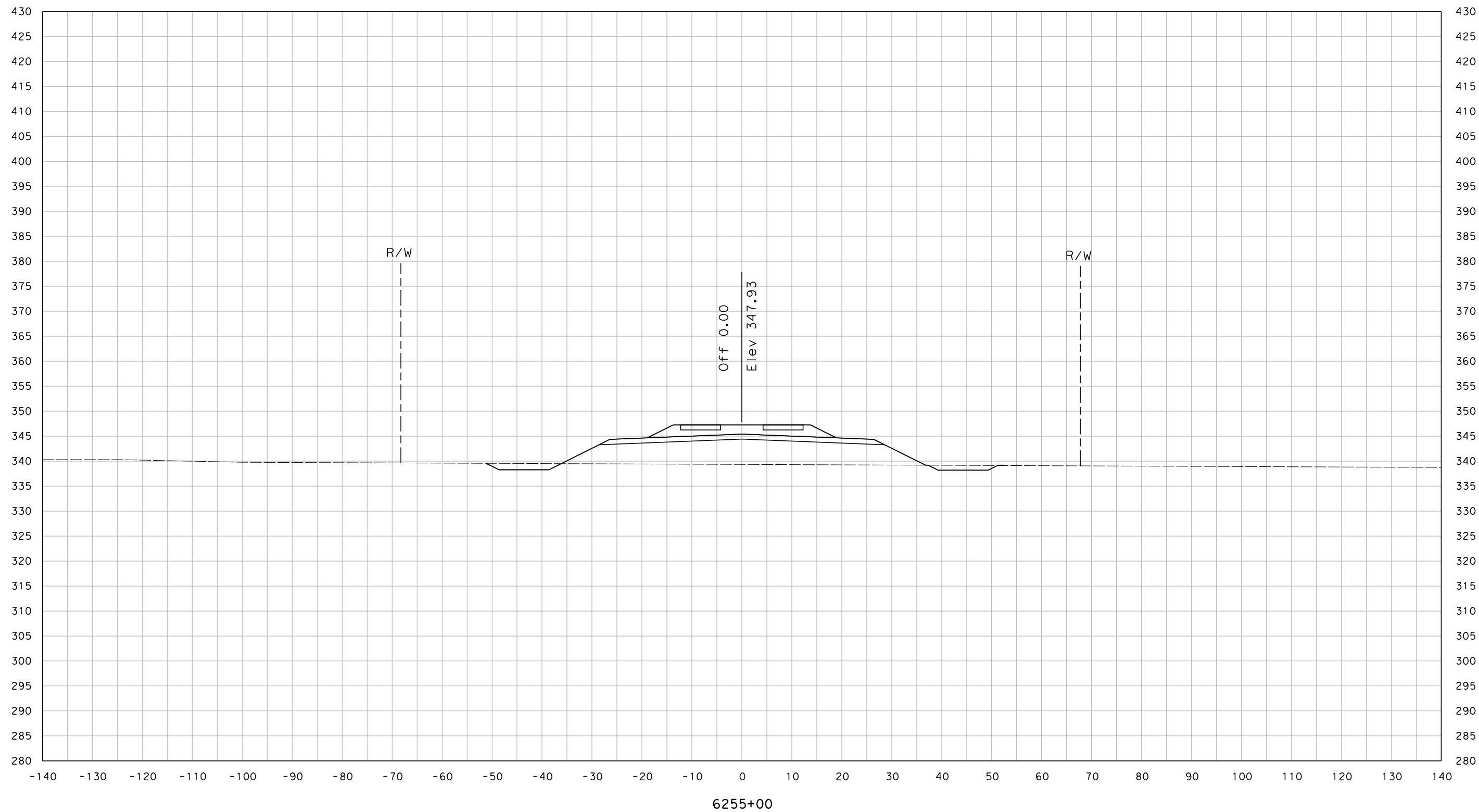
STA. 6240+00 TO STA. 6240+00



STA. 6245+00 TO STA. 6245+00



STA. 6250+00 TO STA. 6250+00



STA. 6255+00 TO STA. 6255+00